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(54)Method for generating a pedal output signal of an accelerator pedal

(57)A method for generating a pedal output signal of an accelerator pedal for electronic throttle control is proposed, wherein a variation in the pedal output signal is produced by a displacement of the accelerator pedal.

According to the method, under given conditions, a given absolute variation in the pedal output signal is produced by a different accelerator pedal displacement depending on whether the accelerator pedal is depressed or released.

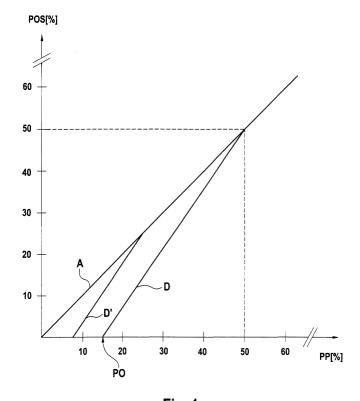


Fig. 1

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Description

Field of the invention

[0001] The present invention generally relates to a method for generating a pedal output signal of an accelerator pedal.

Background of the invention

[0002] In order to meet the demands of today's hightech vehicles, internal combustion engines have been equipped with an electronic throttle control (ETC). On ETC applications, displacements of the accelerator pedal are electronically determined and transmitted. For example, a sensor is coupled to the accelerator pedal and the position of the pedal is converted into a pedal output signal supplied to the ETC. As the pedal output signal depends on the pedal position, it is used as an indication of the pedal position by the ETC.

If a precise control of the intake air flow can be achieved with such ETC, it has however been observed that drivers have trouble to get accustomed to the feel of an accelerator pedal that is no longer linked with a cable and a return spring to the throttle blade. As a consequence, they often do not entirely release the accelerator pedal before depressing the clutch pedal, thereby causing engine speed flares during gearshifts.

Object of the invention

[0003] The object of the present invention is to provide a method for generating a pedal output signal for ETC, which permits to reduce engine speed flares and more generally to improve a vehicle's driveability. This object is achieved by a method as claimed in claim 1.

Summary of the invention

[0004] A method in accordance with the invention generally concerns the generation of a pedal output signal of an accelerator pedal for electronic load control of an internal combustion engine. The pedal output signal depends on the position of the accelerator pedal. Therefore, a variation of the pedal output signal may be produced by a displacement of said accelerator pedal. It shall be appreciated that, under given conditions, a given absolute variation in said pedal output signal is generated by a different accelerator pedal displacement depending on whether the accelerator pedal is depressed or released. This means that, under given conditions, a different pedal output signal is generated for a same pedal position depending on whether the accelerator pedal has been depressed or released.

It follows that the pedal output signal is no longer generated so as to simulate a conventional accelerator pedal, i.e. mechanical accelerator pedal, producing a unique response of the engine to a given pedal displace-

ment respectively a given pedal position, irrespective of acceleration or deceleration. On the contrary, the method of the invention permits to generate, in definable conditions, a different pedal output signal for a same pedal position. Therefore, the response of the engine to a pedal displacement can be adapted in function of preferred driving behaviors, thereby improving the driveability of the vehicle.

Advantageously, a given absolute variation in said pedal output signal is produced by a shorter accelerator pedal displacement when said accelerator pedal is released than when it is depressed. This means that a pedal output signal indicating an entirely released pedal, i.e. an idle output signal, may be generated before the accelerator pedal has been entirely released. A first advantage of this method is that the engine speed will earlier reach the idle engine speed, thereby reducing engine speed flares during gearshifts. Secondly, even if a driver doesn't entirely release the pedal during a gearshift, an idle output signal can be generated to prevent engine speed flares.

However, it will be understood that, anticipating the pedal position for which an idle output signal is generated introduces an offset in the accelerator pedal, which under certain conditions may prove troublesome. Indeed, after a series of accelerations and decelerations the driver may arrive at a point where the pedal is almost entirely depressed and where an idle output signal is generated, thereby hindering acceleration. In order to prevent such a situation, given conditions for generating a different pedal output signal for a same pedal position will be preferably defined.

An initial pedal movement is to depress the accelerator pedal from the entirely released pedal position. When the accelerator pedal is depressed from this entirely released pedal position, the pedal output signal may be generated according to an acceleration function with the actual pedal position as an argument. Now, if the pedal output signal is generated according to the acceleration function, i.e. the pedal is being depressed, and if the accelerator pedal is suddenly released, then the pedal output signal will be advantageously generated according to a deceleration function. The deceleration function is steeper than the acceleration function, thereby ensuring a rapid decrease of the pedal output signal to the idle output signal.

As previously explained, generating the pedal output signal according to the deceleration function implies an offset in the accelerator pedal. Thus, the deceleration function already generates an idle output signal for an offset pedal position different from the entirely released pedal position. Once the accelerator pedal has been released to the offset pedal position, it is clear that if the accelerator pedal is further released the pedal output signal has to remain an idle output signal. Therefore, if the accelerator pedal is further released from the offset pedal position, then the deceleration function is preferably translated so as to generate an idle signal output

for the actual pedal position.

In some cases, the deceleration function, or a translation thereof, may be used to generate the pedal output signal when the pedal has been released and is subsequently depressed. Indeed, using the acceleration function each time that the pedal is depressed and using the deceleration function each time that the accelerator pedal is released would produce discontinuities in the pedal output signal. In case that the pedal has been released and is subsequently depressed, the pedal output signal is therefore preferably generated according to the deceleration function or to a translation thereof, i.e. an analogous function which is parallel shifted, until it reaches the pedal output signal that would be generated according to the acceleration function. Then the pedal output signal will be generated according to the acceleration function.

The deceleration function is translated so as to obtain a coherent and continuous pedal output signal. If the accelerator pedal has been released from the offset pedal position to a given pedal position and if it is depressed again, then the pedal output signal is generated according to the deceleration function shifted or translated by a value corresponding to the difference between the offset pedal position and the given pedal position. Hence, this shifted deceleration function generates an idle output signal for said given pedal position and the pedal output signal varies as with the deceleration function when the accelerator pedal is depressed.

In a preferred embodiment, the acceleration function is of the form:

f_{acc}(pedal position) = A1*pedal position,

with f_{acc} (entirely released pedal position)=idle output signal; and the deceleration function is of the form:

 $f_{dec}(pedal\ position) = A2^*(pedal\ position\ -\ pedal\ offset),$ with A2>A1 and $f_{dec}(offset\ pedal\ position)=idle\ output\ signal.$

It has to be noted, that the method described above can be used as well for the control of a gasoline (petrol) engine (e.g. by electronic throttle control) as for the control of a diesel engine.

Brief description of the drawings

[0005] The present invention will now be described, by way of example, with reference to the accompanying drawings, in which:

Fig.1: is a graph of the functions used in a preferred embodiment of the

method of the invention;

Fig.2: is a flowchart of the present

method;

Fig.3 and Fig.4: illustrate the variations in pedal

position and pedal output signal

during gearshifts.

Detailed description of a preferred embodiment

[0006] In a preferred embodiment, the method of the invention is implemented in order to generate a pedal output signal of an accelerator pedal in such a way that engine speed flares may be reduced or avoided during gearshifts.

Therefore, in this preferred embodiment, under given conditions, a given absolute variation in the pedal output signal is produced by a shorter accelerator pedal displacement when the accelerator pedal is released than when it is depressed. The expression "absolute variation" here means the absolute value of the variation, i. e. the value of the variation independently of its sign. It follows that a pedal output signal indicating an entirely released pedal, i.e. an idle output signal, may be generated before the accelerator pedal has been entirely released. It also means that, under given conditions, a different pedal output signal is generated for a same pedal position depending on whether the accelerator pedal has been depressed or released.

In the present method, the pedal output signal is preferably generated according to an acceleration function (face) or to a deceleration ($f_{\rm dec}$), depending on the situation. An example for such acceleration and deceleration functions is shown in Fig. 1, where each of the functions is represented by a straight line. Numerical examples will be used for a better understanding of this preferred embodiment of the method of the invention.

The accelerator pedal has a pedal position (PP) assumed to range from 0 to 100%. For an entirely released accelerator pedal, PP=0%. For an entirely depressed accelerator pedal, PP=100%. The pedal output signal (POS) is also assumed to range from 0 to 100%. A pedal output signal of 0% corresponds to an idle output signal. POS=100% brings the engine to full load.

In the present example, the functions are defined as follows:

 $f_{acc}(PP)=PP$, and

f_{dec}(PP)= S(PP - PO), where S (S>1) is the slope of the straight line and PO is the pedal offset.

When the accelerator pedal is entirely released, the pedal output signal and the pedal position are identical, since an idle output signal has to be generated when the pedal is entirely released. If the accelerator pedal is then depressed, the pedal output signal is given by the acceleration function (POS = $f_{acc}(PP)$), thus POS and PP stay identical. This can be seen on Fig.1 where the pedal output signal is plotted versus the pedal position, and f_{acc} is indicated by reference sign A whereas f_{dec} is indicated by reference sign D.

Let us now assume that the accelerator pedal is depressed until the pedal position reaches e.g. 50%, and that it is subsequently released from that position, which shall be called switch position. Then, in accordance with the present method, the pedal output signal shall be given by the deceleration function. This means that the pedal output signal was given by f_{acc} and that conse-

quently to the release of the pedal, the pedal output signal is then given by $f_{dec}.$ More generally, this condition shall be respected every time the pedal is released while the pedal output signal is given by $f_{acc}.$

As can be seen in Fig.1, the deceleration function is steeper than the acceleration function. Hence, a pedal output signal of 0%, i.e. idle output signal, will be obtained before the accelerator pedal is entirely released. This introduces an offset in the position of the accelerator pedal, which is clear in view of Fig.1. From the equation of f_{dec}, it is also clear that f_{dec} equals 0% when the pedal position reaches an offset pedal position equal to PO, which shall be called offset pedal position. In practice, the slope of the deceleration function is a constant. It is preferably about 10% steeper than the slope of the acceleration function. However, in this embodiment, the slope has been set to 1.4 (i.e. 40% steeper) in order to exaggerate the effect of the method. As understood, the pedal offset depends on the switch position and is needed to obtain the equation of the deceleration function. Hence the pedal offset is calculated as soon as a release of the pedal is detected and a transition from face to f_{dec} should be performed. The value of PO is obtained by equating f_{acc} to f_{dec} at the switch position (50%), which yields the following pedal offset:

$$PO = \frac{(S-1)}{S} * PP$$

[0007] In the present example, S=1.4 and PP=50%, thus PO=14.3 % and offset pedal position = 14.3 % as well.

So, if the pedal is released from the switch position, the pedal output signal is generated according to the deceleration function. As a matter of fact, the pedal output signal is advantageously given by $f_{\rm dec}$ as long as the pedal position is between the switch position and the offset pedal position. This ensures a coherent control of the pedal and the continuity of the pedal output signal. Indeed, if $f_{\rm acc}$ was only used for depressed pedals and $f_{\rm dec}$ for released pedals, there would be evident discontinuities in the values of the pedal output signal.

Hence, if the pedal is released from the switch position (50%) to e.g. PP=40%, then POS is given by $f_{dec}.$ If the pedal is again depressed from PP=40%, then POS is still given by $f_{dec},$ until it reaches $f_{acc}.$ In other words, if the pedal has been released and is subsequently depressed, the pedal output signal is given by f_{dec} until it reaches the pedal output signal that would be given by $f_{acc},$ and the pedal output signal is then further generated according to $f_{acc}.$

Now, if the pedal is released from the switch position to the offset pedal position, then the pedal output signal is 0% (idle output signal), since f_{dec} (offset pedal position) =0%. It will be understood that if the pedal is further released, the pedal output signal should still be 0%. Therefore, the deceleration function is advantageously shifted (parallel shift) to the current pedal position (i.e. towards

decreasing pedal positions) as long as the pedal position decreases so that an idle output signal can be generated. The translation may be carried out by modifying the variable PP in the deceleration function f_{dec} by a value corresponding to the pedal displacement between the offset pedal position and the current pedal position. For example, if the pedal is decreased to 7.5%, then POS shall be given by $f_{dec}(PP+(14.3-7.5))$. In the present embodiment, the same result can be achieved by giving PO the value of the current pedal position. If the pedal is now again depressed from PP=7.5%, then the pedal output signal should be given by the translated deceleration function calculated at that position, i.e. POS=f_{dec}(PP+(14.3-7.5)). This translated f_{dec} is indicated by reference sign D' in Fig.1. It allows the generation of a coherent pedal output signal since the pedal output signal thus varies as with the initial deceleration function. When f_{dec}(PP+(14.3-7.5)) reaches facc(PP) then the pedal output signal is again given by facc, as previously explained.

A convenient way to implement the present method is to adapt it for an on-board computer. The preferred flow-chart for programming the method is shown in Fig.2. At 10 the pedal position is compared to the old pedal position in order to detect acceleration or deceleration. When depressing the pedal for the first time, i.e. from 0%, f_{dec} is not in use and as long as the pedal is depressed the comparison at 10 yields YES. Therefore, the pedal output signal is generated according to the acceleration function, as shown at 12: pedal output signal = pedal position.

If the pedal is depressed to PP=50% and then released (switch position), then the answer to the test at 10 is NO. At that moment, the deceleration function is not yet in use (13) and the pedal offset is to be calculated first, as shown at 14. Then, f_{dec} is set in use and the pedal output signal is calculated at 16.

As long as the pedal position is between the offset pedal position and the pedal position of the switch position, the answer to the test at 13 is YES and the pedal output signal is given by $f_{\rm dec}$ as previously calculated. For example, if the pedal is released from the switch position to 40% and subsequently depressed, then the test at 10 yields YES and POS = $f_{\rm dec}(PP)$, see 20. If the pedal position increases above the switch position, then the pedal output signal given by $f_{\rm dec}$ is greater than the pedal position (more generally to the pedal output signal that would be given by $f_{\rm acc}$). Hence, as imposed after the test at 22, the deceleration function is disabled and the pedal output signal is given by $f_{\rm acc}$, as shown at 24.

[0008] Now, if the pedal is released from the switch position (50%) to the offset pedal position and further to a pedal position below e.g. 7.5%, then the test at 18 yields YES and Pedal Offset is given the value of the current pedal position, 26, so that an idle output signal may be generated by $f_{\rm dec}$. As $f_{\rm dec}$ is a straight line, directly modifying the Pedal Offset is equivalent to shifting the deceleration function by a value corresponding to

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(offset pedal position - pedal position).

[0009] The following points are clear from Fig.2:

- if f_{acc} is in use and pedal position < old pedal position then f_{dec} is set in use;
- if pedal position>old pedal position and f_{dec} is in use, then as soon as f_{dec}(pedal position) reaches f_{acc}(pedal position) the deceleration function is disabled and the pedal output signal given by f_{acc}.

[0010] In Fig.3 and Fig.4, graphs representing the pedal position (continuous lines) and the resulting pedal output signal (dashed lines) are shown. On these graphs, the vertical axis represents the percentage of pedal position, resp. of pedal output signal. The horizontal axis indicates the time in ms, the origin of time being arbitrary.

In Fig.3, each peak corresponds to a gearshift. When the pedal is depressed from 0% then POS = $f_{acc}(PP)$. Therefore in the increasing part of the peak the pedal output signal equals the pedal position. Next, as the pedal is released while f_{acc} was in use, the deceleration function is used.

For the first two peaks, as indicated by the continuous line, the driver did entirely release the accelerator pedal. During the decreasing part of the curve $f_{\rm dec}$ is used and the pedal output signal anticipates the pedal position. The decreasing part of the second peak is shown in Fig. 4. The present method permits, in this example, to reach the idle output signal (0%) 32 ms before the accelerator pedal is entirely released. It follows that engine speed flares can be reduced during gearshifts since an idle output signal is generated before the pedal has been entirely released.

Turning back to Fig.3, the third peak is characteristic of a "driver mistake" which can be corrected with the present method. As can be seen, the driver did not entirely release the accelerator pedal. However, the driver did release the pedal to PP=10%, which is below the offset pedal position, and therefore an idle output signal is generated. Indeed, the pedal has been decreased from about 75%, hence the pedal offset is PO=(1.4-1)/1.4*75=21.4%=offset pedal position.

Claims

1. A method for generating a pedal output signal of an accelerator pedal for electronic load control of an internal combustion engine, wherein a variation of said pedal output signal is produced by a displacement of said accelerator pedal, characterized in that, under given conditions, a given absolute variation in said pedal output signal is produced by a different accelerator pedal displacement depending on whether the accelerator pedal is depressed or released.

- The method according to claim 1, characterized in that a given absolute variation in said pedal output signal is produced by a shorter accelerator pedal displacement when said accelerator pedal is released than when it is depressed.
- 3. The method according to claim 2, wherein said pedal output signal may be generated according to an acceleration function, characterized in that, if said pedal output signal is generated according to said acceleration function and if said accelerator pedal is released, then said pedal output signal is generated according to a deceleration function which is steeper than said acceleration function.
- 4. The method according to claim 3, characterized in that said deceleration function generates an idle output signal for an offset pedal position different from an entirely released pedal position; and in that if said accelerator pedal is further released from said offset pedal position then said deceleration function is translated so as to generate an idle signal output for the actual pedal position.
- 25 5. The method according to claim 4, characterized in that if said accelerator pedal has been released, then if said accelerator pedal is depressed said pedal output signal is generated according to said deceleration function or to a translation thereof;
 30 and in that, if said pedal output signal generated according to said deceleration function or translated deceleration function reaches the pedal output signal that would be generated according to said acceleration function, then said pedal output signal is generated according to said acceleration function.
 - 6. The method according to claim 5, characterized in that if said accelerator pedal has been released from the offset pedal position to a given pedal position and if said accelerator pedal is subsequently depressed, then the pedal output signal is generated according to said deceleration function translated by a value corresponding to the difference between said offset pedal position and said given pedal position.
 - The method according to anyone of claims 3 to 6, characterized in that

said acceleration function is of the form: f_{acc} (pedal position) = A1*pedal position, with f_{acc} (entirely released pedal position)=idle output signal; and the deceleration function is of the form:

 f_{dec} (pedal position) = $A2^*$ (pedal position-pedal offset), with A2>A1 and

f_{dec}(offset pedal position)=idle output signal .

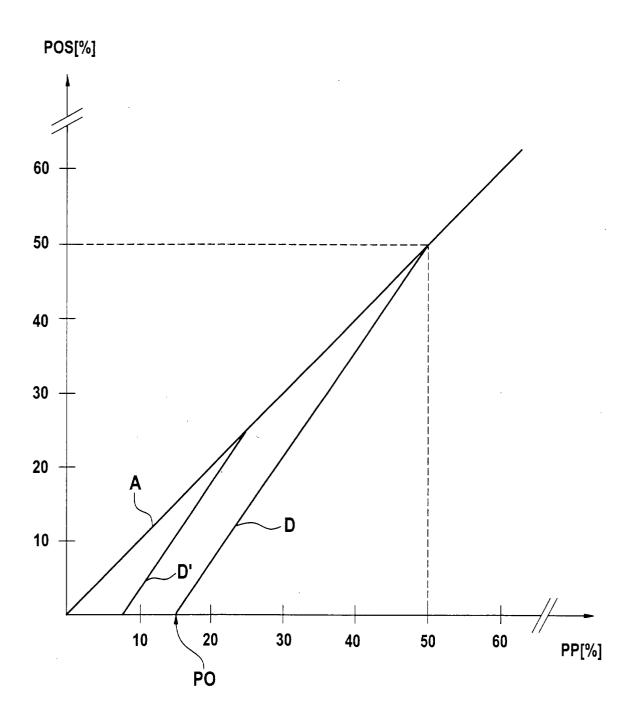


Fig. 1

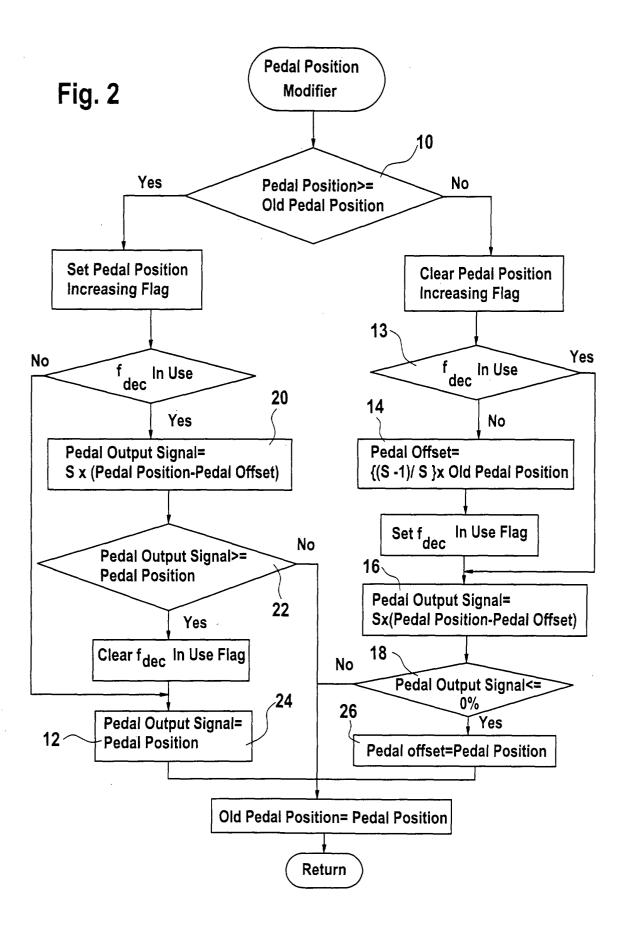
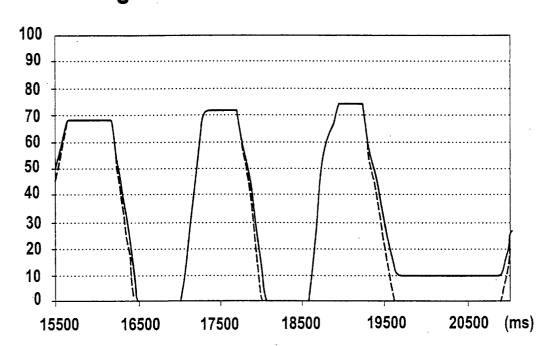
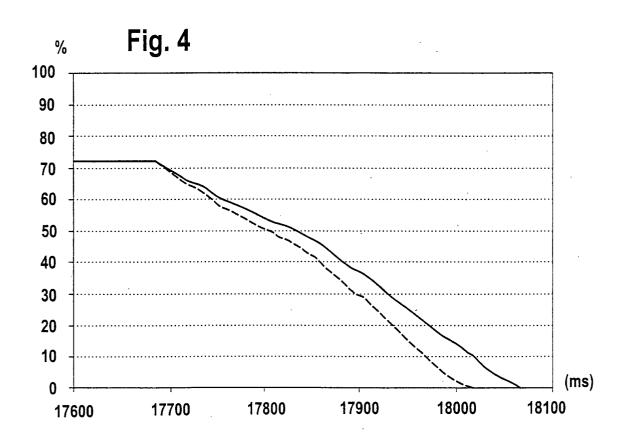


Fig. 3







EUROPEAN SEARCH REPORT

Application Number EP 01 10 5216

		ERED TO BE RELEVANT idication, where appropriate,	Relevant	CLASSIFICATION OF THE
Category	of relevant pass		to claim	APPLICATION (Int.CI.7)
χ	DE 44 03 604 A (A B 10 August 1995 (199		1-3	F02D11/10
Α	* abstract * * column 1, line 1 * column 4, line 42 * figures 1,2 *			
X	EP 0 341 659 A (SIE 15 November 1989 (1	1-3		
A	* abstract * * column 5, line 51 * figures 1,2 *	* 4		
Α	FR 2 718 191 A (PEU 6 October 1995 (199 * abstract * * page 6, line 21 - * figures 1,2 *	5-10-06)	1	
A	EP 0 962 350 A (BAY AG) 8 December 1999 * abstract * * column 3, line 3 * figure 1 *	1	TECHNICAL FIELDS SEARCHED (Int.CI.7)	
A	DE 196 44 477 A (JA 30 April 1997 (1997 * abstract * * figures *			
Α	US 4 881 428 A (SAS 21 November 1989 (19 * figure 4 *			
Α	DE 43 05 737 A (DAII 3 March 1994 (1994- * figure 2 *	03-03)		
		-/		
	The present search report has b	een drawn up for all claims		
	Place of search	Date of completion of the search	1	Examiner
	THE HAGUE	19 July 2001	Tro	otereau, D
X : parti Y : parti docu A : tech O : non	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone icularly relevant if combined with another interest of the same category nological backgroundwritten disclosure mediate document	E : earlier paten after the filin ner D : document cit L : document cit	ted in the application ed for other reasons	lished on, or

EPO FORM 1503 03.82 (P04C01)



EUROPEAN SEARCH REPORT

Application Number EP 01 10 5216

DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document with indication, where appropriate, of relevant passages CLASSIFICATION OF THE APPLICATION (Int.CI.7) Relevant Category to claim Α US 5 540 633 A (YAMANAKA AKIHIRO ET AL) 30 July 1996 (1996-07-30) * figure 2 * TECHNICAL FIELDS SEARCHED (Int.CI.7) The present search report has been drawn up for all claims Date of completion of the search Examiner Place of search THE HAGUE 19 July 2001 Trotereau, D T: theory or principle underlying the invention
 E: earlier patent document, but published on, or after the filling date
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EPO FORM 1503 03.82 (P04C01)

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 01 10 5216

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

19-07-2001

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
DE 4403604	Α	10-08-1995	NONE	
EP 0341659	A	15-11-1989	FR 2631389 A DE 68900153 D JP 2061335 A	17-11-198 29-08-199 01-03-199
FR 2718191	Α	06-10-1995	NONE	
EP 0962350	Α	08-12-1999	DE 19825306 A	09-12-199
DE 19644477	Α	30-04-1997	JP 9119328 A	06-05-199
US 4881428	Α	21-11-1989	JP 62298642 A	25-12-198
DE 4305737	Α	03-03-1994	NONE	
US 5540633	Α	30-07-1996	JP 2976766 B JP 7083082 A	10-11-199 28-03-199

FORM P0459

 $\stackrel{\circ}{\mathbb{L}}$ For more details about this annex : see Official Journal of the European Patent Office, No. 12/82