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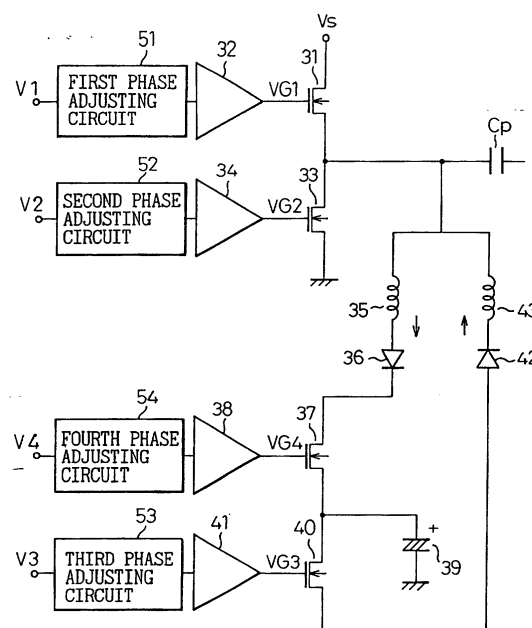
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(54) **Plasma display apparatus and manufacturing method**

(57) A PDP apparatus equipped with a sustaining circuit that prevents the on/off timing shifts and consequent deterioration of sustaining pulses. Phase adjusting circuits (51-54), which adjust the timing of the changing edge of the sustaining pulses, are provided in a sustaining circuit, and the efficiency of a power recovery circuit is improved by optimizing the timing of the changing edge of the sustaining pulses. The circuit devices used in the sustaining circuits are classified according to delay times and sets of the circuit devices are selected so that the timing of the changing edge of the sustaining pulse falls within a predetermined allowance range, and the selected sets of the circuit devices are installed in the PDP. In this way, power consumption is reduced and malfunctions are prevented.

Fig.7





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EUROPEAN SEARCH REPORT

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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 16 July 2003	Examiner O'Reilly, D
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
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