



(11) **EP 1 146 634 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:  
**Corrected version no 1 (W1 B1)**  
**Corrections, see**  
**Bibliography INID code(s) 73**

(51) Int Cl.:  
**H03B 5/36 (2006.01)**

(48) Corrigendum issued on:  
**26.05.2010 Bulletin 2010/21**

(45) Date of publication and mention  
of the grant of the patent:  
**17.03.2010 Bulletin 2010/11**

(21) Application number: **00127908.2**

(22) Date of filing: **20.12.2000**

(54) **High frequency oscillation circuit**  
Hochfrequenzoszillatorschaltung  
Circuit d'oscillateur à haute fréquence

(84) Designated Contracting States:  
**CH DE FR GB LI SE**

(30) Priority: **09.02.2000 JP 2000031513**  
**05.04.2000 JP 2000103709**

(43) Date of publication of application:  
**17.10.2001 Bulletin 2001/42**

(73) Proprietor: **National Institute of Advanced  
Industrial Science  
and Technology  
Tokyo-to (JP)**

(72) Inventor: **Kurosawa, Shigeru  
Tsuchiura-shi,  
Ibaraki (JP)**

(74) Representative: **Forstmeyer, Dietmar  
BOETERS & LIECK  
Oberanger 32  
80331 München (DE)**

(56) References cited:  
**US-A- 3 676 801 US-A- 3 689 907**  
**US-A- 3 879 992 US-A- 4 735 081**  
**US-A- 4 862 114**

- **Application note AN-88, 'CMOS Linear  
Applications', 1986, Linear Applications  
Databook, National Semiconductor Corporation,  
Santa Clara, CA, USA**

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

**EP 1 146 634 B8**