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(72) Inventor: **Gerber, Johannes**  
**80995 München (DE)**

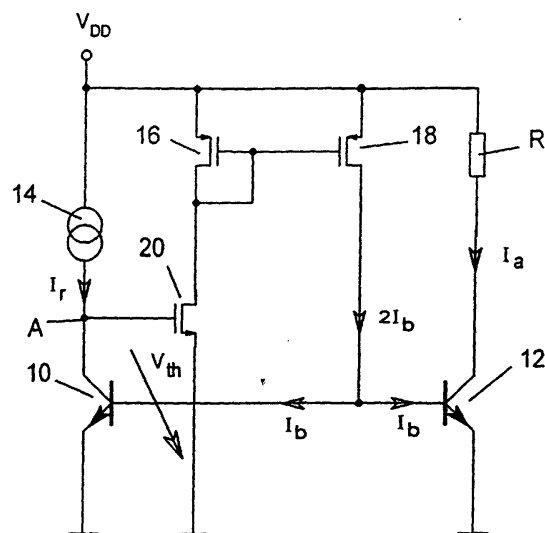
(74) Representative:  
**Degwert, Hartmut, Dipl.-Phys. et al**  
**Prinz & Partner GbR,**  
**Manzingerweg 7**  
**81241 München (DE)**

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(71) Applicant: **TEXAS INSTRUMENTS**  
**DEUTSCHLAND GMBH**  
**85356 Freising (DE)**

(54) **Current mirror circuit**

(57) A current mirror circuit serves to produce an output current flowing in an output branch corresponding to a reference current flowing in an input branch located between a supply voltage terminal and ground. Located in the input branch is a first bipolar transistor (10) through the collector/emitter circuit of which the reference current furnished by a current source (14) connected to the collector flows. Through the collector/emitter circuit of a second bipolar transistor (12) located in the output branch the output current flows. The bases of the two transistors (10, 12) are connected to each other. A further current mirror circuit (16, 18, 20) is provided having an input branch located between the supply voltage terminal and ground and an output branch located between the supply voltage terminal and the connected bases of the two bipolar transistors (10, 12) for generating a base current ( $I_b$ ) for these transistors. A current source (20) controlled by the collector voltage of the first bipolar transistor (10) is located in the input branch of this further current mirror circuit (16, 18, 20), the output current ( $2I_b$ ) of said current source (20) being mirrored in the output branch of said further current mirror circuit (16, 18, 20).



**Fig.1**



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# EUROPEAN SEARCH REPORT

Application Number  
EP 01 12 7473

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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