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(54) Interface apparatus

(57) A Gigabit Media Independent Interface (RG-MII), which is adapted to also implement a ten bit interface (RBTI) that is intended to be an alternative to both the IEEE 802.3z GMII and the TBI is disclosed. The interface has a reduced number of input and output pins, i.e., pin-count, that can implement the above BMII and TBI standards. More particularly, the interface reduces the number of pins required to interconnect the MAC

and the PHY from a maximum of 28 pins (TBI) to 12 pins in a cost effective and technology independent manner. The RGMII maps pins to transfer data at the same data rate with control functionality with a minimum number of input and output pins, and does so by utilizing both the raising and falling edges of the clock signal and complies with existing interface specifications set forth in the IEEE standards.

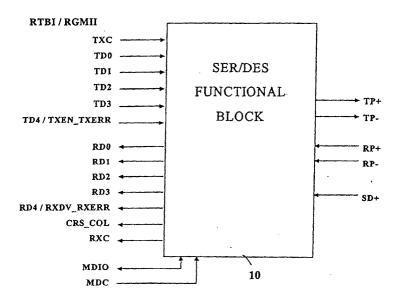


FIG. 1



EUROPEAN SEARCH REPORT

Application Number EP 02 25 5636

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ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

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