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(11) **EP 1 376 522 A3**

(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
24.03.2004 Bulletin 2004/13

(51) Int Cl.7: **G09G 3/28**

(43) Date of publication A2:
02.01.2004 Bulletin 2004/01

(21) Application number: **03018996.3**

(22) Date of filing: **01.11.2000**

(84) Designated Contracting States:
DE FR GB

(30) Priority: **09.11.1999 JP 31763799**
30.05.2000 JP 2000160080

(62) Document number(s) of the earlier application(s) in
accordance with Art. 76 EPC:
00971735.6 / 1 160 756

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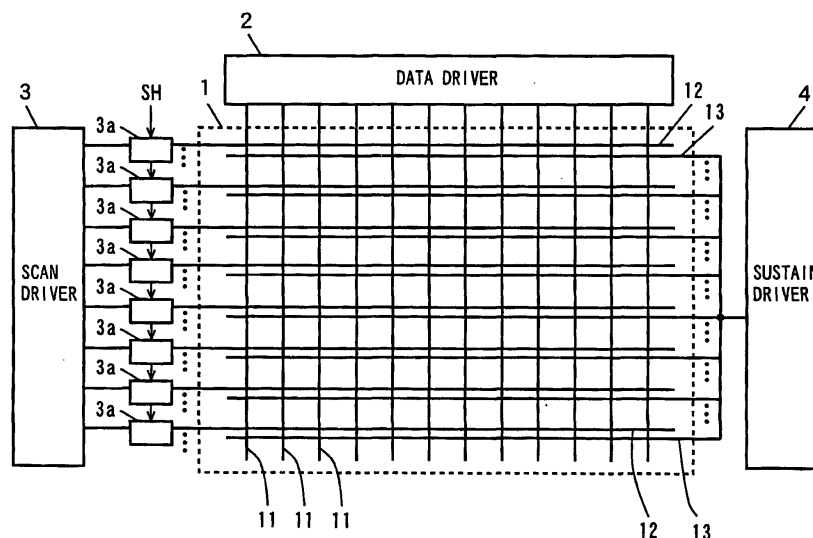
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(54) **Driving circuit and display**

(57) In a circuit driving a capacitive load C_p , current passed through a transistor Q3, a diode D1 and a recovering coil L is passed through lines L1, L2, and the inductance components of the lines L1 and L2, and the drain-source capacitances of the transistors Q1 and Q2 generate LC resonance. Capacitors C1 and C2 are con-

nected in parallel to the drain-source regions of the transistors Q1 and Q2 to increase the total drain-source capacitance and reduce the resonance frequency, so that unwanted electromagnetic wave radiation in a frequency band affecting other electronic devices is suppressed.

FIG. 1



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EUROPEAN SEARCH REPORT

Application Number
EP 03 01 8996

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			G09G
The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
The Hague		28 January 2004	Van Roost, L
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03.82 (P04C01)

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EPO FORM P0459

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