



(11) **EP 1 387 250 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:  
**Corrected version no 1 (W1 B1)**  
**Corrections, see**  
**Bibliography INID code(s) 73**

(51) Int Cl.:  
**G06F 9/30 (2006.01)**

(48) Corrigendum issued on:  
**29.02.2012 Bulletin 2012/09**

(45) Date of publication and mention  
of the grant of the patent:  
**13.07.2011 Bulletin 2011/28**

(21) Application number: **03291917.7**

(22) Date of filing: **30.07.2003**

(54) **Processor that accomodates multiple instruction sets and multiple decode modes**

Prozessor mit Mehrfachbefehlssätzen und Mehrfachdecodierungsmodi

Processeur à jeux d'instructions et à modes de decodage multiples

(84) Designated Contracting States:  
**DE FR GB NL**

(43) Date of publication of application:  
**04.02.2004 Bulletin 2004/06**

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