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### (54) Duplexer, and laminate-type high-frequency device and communication equipment using the same

(57) A duplexer is composed of a laminate in which dielectric layers (1a, 1b, 1c, 1d) and electrode layers are laminated alternately. A first filter for transmitting and a second filter for receiving, having different pass band frequencies, are provided in the laminate. A matching circuit composed of a coupling line (9), having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first filter and the second filter. The first filter includes two first stripline resonators (3a, 3b), each having one end that is short-circuited. The second filter includes two second stripline resonators (4a, 4b), each having one end that is short-circuited. The coupling line (9) is coupled to the first stripline resonator (3b) close to the coupling line (9) by electromagnetic field coupling. The coupling line (9) is coupled to the second stripline resonator (4a) close to the coupling line (9) by electromagnetic coupling.

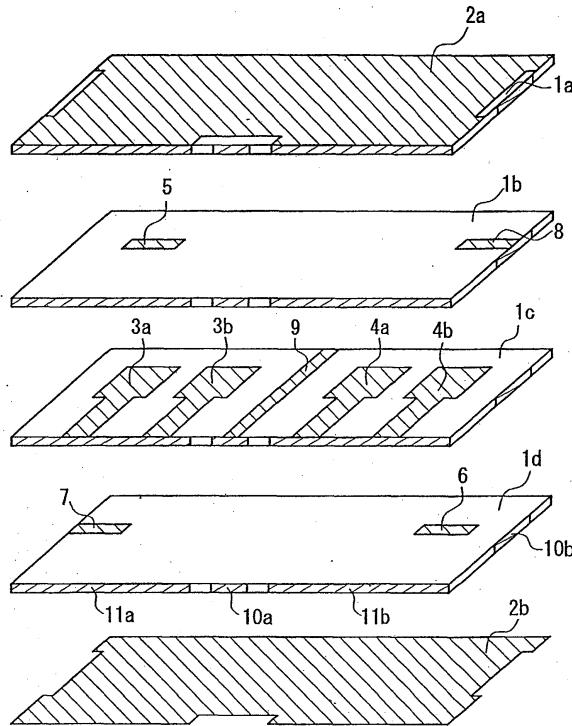


FIG. 2

## Description

**[0001]** The present invention relates to a duplexer, and a laminate-type high-frequency device and communication equipment provided with the duplexer. More specifically, the present invention relates to an antenna duplexer sharing one antenna on a transmitting side and a receiving side and separating a transmitted signal and a received signal from each other, and a laminate-type high-frequency device and communication equipment provided with the antenna duplexer.

**[0002]** In an antenna duplexer sharing one antenna on a transmitting side and a receiving side, and separating a transmitted signal and a received signal from each other, two filters having different center frequencies are connected. Therefore, it is required that, at a center frequency of one of the two filters, the impedance of the other filter seen from an antenna that is an input/output terminal shared by the two filters is set to be infinite, whereby the transmission characteristics of the one of the two filters are prevented from being impaired.

**[0003]** For this purpose, a duplexer as shown in FIG. 24 is proposed (e.g., see pages 4-5 and FIG. 3 in JP 3,204,753). FIG. 24 is an exploded perspective view of the duplexer in the prior art.

**[0004]** As shown in FIG. 24, a first end ground electrode (not shown) is provided on a lower surface of a dielectric layer 11p. Furthermore, a through-hole 91p for connecting the first end ground electrode to a tip portion of a transmission line 70p (described later) is formed in the dielectric layer 11p.

**[0005]** On an upper surface of a dielectric layer 14p, resonant elements 21p, 22p, 23p, 24p, and 25p are provided, each having one end that is connected to the first end ground electrode, respectively, to form a 1/4 wavelength stripline resonator. Furthermore, on the upper surface of the dielectric layer 14p, electrodes 31p, 32p, 33p, 34p, and 35p are provided, each having one end that is connected to the first end ground electrode, respectively, and the other end that is opposed to open ends of the resonant elements 21p, 22p, 23p, 24p, and 25p, respectively, with a predetermined gap. A filter 500p in a comb-line shape is configured by using the distribution coupling of the resonant elements 21p and 22p, and a filter 600p in a comb-line shape is configured by using the distribution coupling of the resonant elements 23p, 24p, and 25p. Furthermore, on the upper surface of the dielectric layer 14p, the transmission line 70p for constituting an inductor between the resonant elements 22p and 23p is provided. A through-hole 94p is formed at the tip portion of the transmission line 70 so as to be connected to the first end ground electrode.

**[0006]** On an upper surface of a dielectric layer 12p, an inner layer ground electrode 81p positioned on an open end side of the resonant elements 21p and 22p and connected to the first ground electrode is provided, and dielectric layers 13p and 14p are interposed between the inner ground electrode 81p and the open end

side of the resonant elements 21p and 22p. Furthermore, on the upper surface of the dielectric layer 12p, an inner layer ground electrode 83p positioned on an open end side of the resonant elements 23p, 24p, and 25p and connected to the first end ground electrode is provided, and the dielectric layers 13p and 14p are interposed between the inner layer ground electrode 83p and the open end side of the resonant elements 23p, 24p, and 25p. Furthermore, a through-hole 92p for connecting the first end ground electrode to the tip portion of the transmission line 70p is formed in the dielectric layer 12p.

**[0007]** On an upper surface of the dielectric layer 13p, a capacitive coupling electrode 50p is provided, one end 51p of which is overlapped with a part of the transmission line 70p with the dielectric layer 14p interposed therebetween and the other end 52p of which is overlapped with a part of the resonant element 23p with the dielectric layer 14p interposed therebetween. Furthermore, on the upper surface of the dielectric layer 13p, an input/output electrode 42p overlapped with a part of the resonant element 25p with the dielectric layer 14p interposed therebetween is provided. A through-hole 93p for connecting the first end ground electrode to the tip portion of the transmission line 70p is formed in the dielectric layer 13p.

**[0008]** The tip portion of the transmission line 70p is short-circuited with the first end ground electrode provided on the lower surface of the dielectric layer 11p via the through-holes 94p, 93p, 92p, and 91p. The through-hole 94p is positioned so that an electric length of the transmission line 70p is a predetermined length of 90 degrees or less. Thus, the transmission line 70p constitutes an inductor.

**[0009]** On an upper surface of the dielectric layer 15p, a capacitive electrode 60p is provided, one end 61p of which is overlapped with a part of the transmission line 70p with the dielectric layer 15p interposed therebetween, and the other end 62p of which is overlapped with a part of the resonant element 22p with the dielectric layer 15p interposed therebetween. Furthermore, on the upper surface of the dielectric layer 15p, an input/output electrode 41p is provided, a part of which is overlapped with a part of the resonant element 21p with the dielectric layer 15p interposed therebetween.

**[0010]** A second end ground electrode 110p is provided on an upper surface of a dielectric layer 17p.

**[0011]** On an upper surface of a dielectric layer 16p, an inner layer ground electrode 82p positioned on the open end side of the resonant elements 21p and 22p and connected to the second end ground electrode 110p is provided, and the dielectric layers 15p and 16p are interposed between the inner layer ground electrode 82p and the open end side of the resonant elements 21p and 22p. Furthermore, on the upper surface of the dielectric layer 16p, an inner layer ground electrode 84p positioned on the open end side of the resonant elements 23p, 24p, and 25p and connected to the second

end ground electrode 110p is provided, and the dielectric layers 15p and 16p are interposed between the inner layer ground electrode 84p and the open end side of the resonant elements 23p, 24p, and 25p.

**[0012]** The dielectric layers 11p, 12p, 13p, 14p, 15p, 16p, and 17p having the above-mentioned configuration are laminated to be integrated, followed by sintering, providing a duplexer composed of a laminate, in which dielectric layers and electrode layers are laminated alternately.

**[0013]** FIG. 25 shows an equivalent circuit of the duplexer having the above-mentioned configuration. As shown in FIGS. 24 and 25, a band-pass filter 500p is composed of the resonant elements 21p and 22p, and a band-pass filter 600p is composed of the resonant elements 23p, 24p, and 25p. A static capacitor 401p is formed between the capacitive electrode 60p and the transmission line 70p, and a static capacitor 402p is formed between the capacitive electrode 50p and the transmission line 70p. The inductor 403p is composed of the transmission line 70p. The inductor 403p is connected in parallel with the filters 500p and 600p, and the static capacitor 401p is connected in series between an antenna 700p and the filter 500p. The static capacitor 402p is connected in series between the antenna 700p and the filter 600p. The inductor 403p, and the static capacitors 401p and 402p constitute a branching circuit 400p.

**[0014]** FIG. 26 shows the transmission characteristics of a duplexer having the above-mentioned configuration. As shown in FIG. 26, it is understood that the branching circuit 400p shares one antenna on a transmitting side and a receiving side, and separates a transmitted signal and a received signal from each other.

**[0015]** As described above, the duplexer in the prior art includes a branching circuit provided between two filters in a triplet shape, in which resonant elements are provided integrally in a dielectric substrate. The branching circuit is configured by using static capacitors connected in series to each of the two filters and an inductor connected in parallel therewith.

**[0016]** However, in the above-mentioned configuration, the circuit configuration is complicated, so that a loss in the branching circuit is increased, and the layout space is necessarily increased. Furthermore, it is difficult to obtain a large L-value in the inductor connected in parallel with two filters, respectively, so that the degree of design freedom is lowered.

**[0017]** Therefore, with the foregoing in mind, it is an object of the present invention to provide a duplexer that can be realized with a very simple configuration, designed so as to substantially reduce a loss, and miniaturized easily, and a laminate-type high-frequency device and communication equipment using the duplexer. Furthermore, it is another object of the present invention to provide a duplexer that is improved so as to obtain a sufficient degree of design freedom, and a laminate-type high-frequency device and communication equip-

ment using the duplexer.

**[0018]** In order to achieve the above-mentioned object, a first configuration of a duplexer of the present invention relates to a duplexer composed of a laminate in which dielectric layers and electrode layers are laminated alternately. The duplexer includes: a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and a matching circuit composed of a coupling line, having one end that is short-circuited and the other end that is connected to an external terminal, provided between the first filter and the second filter. The first filter includes at least one first stripline resonator, having one end that is short-circuited. The second filter includes at least one second stripline resonator, having one end that is short-circuited, and the first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling.

**[0019]** In the first configuration of the duplexer, a duplexer can be realized with a simple configuration in which the first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling, without using lumped-constant elements such as an inductor and a capacitor.

25 Therefore, a loss can be reduced substantially.

**[0020]** In the first configuration of the duplexer of the present invention, it is preferable that at least one of the first stripline resonator and the second stripline resonator has a large line width on an open end side and a small line width on a short-circuited side. According to this preferred example, the coupling degree between the first and second stripline resonators and the coupling line can be changed arbitrarily. Therefore, the degree of design freedom is enhanced substantially.

**[0021]** In the first configuration of the duplexer of the present invention, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, and a fourth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode constituting the first filter and an input/output coupling capacitive electrode constituting the second filter, placed between the first dielectric layer and the second dielectric layer; a first resonator electrode constituting the first filter, a second resonator electrode constituting the second filter, and a coupling line electrode constituting the matching circuit, placed between the second dielectric layer and the third dielectric layer; an input/output coupling capacitive electrode constituting the first filter and an interstage coupling capacitive electrode constituting the second filter, placed between the third dielectric layer and the fourth dielectric layer; a second shield electrode placed on a lower surface of the fourth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode constituting the first filter, the input/output coupling capacitive electrode constituting

the second filter, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer; and an end face electrode connecting the first shield electrode and the second shield electrode to each other.

**[0022]** According to the above preferred example, a duplexer can be realized with a very simple configuration, so that a substantial reduction in loss and miniaturization can be achieved.

**[0023]** Furthermore, in this case, it is preferable that at least one of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer has a dielectric constant different from that of the other dielectric layers. According to this preferred example, the capacitance of a capacitor can be adjusted.

**[0024]** Furthermore, in this case, it is preferable that the duplexer further includes an adjusting capacitive electrode provided so as to be opposed to the first shield electrode with the first dielectric layer interposed therebetween. An antenna can be used for both transmitting and receiving. According to the preferred example, the matching between transmitting and receiving can be optimized.

**[0025]** In the first configuration of the duplexer of the present invention, at least one of the first stripline resonator and the second stripline resonator is formed on a dielectric layer different from a dielectric layer on which the coupling line is formed. According to this preferred example, the degree of design freedom and versatility can be enhanced.

**[0026]** In the first configuration of the duplexer of the present invention, it is preferable that the coupling line is composed of at least two striplines having different line widths, connected to each other. According to this preferred example, the strength of electromagnetic field coupling can be changed arbitrarily.

**[0027]** In the first configuration of the duplexer of the present invention, it is preferable that the coupling line is composed of a plurality of striplines, and the plurality of striplines are provided on different dielectric layers. According to this preferred example, an electric potential can be stabilized. Furthermore, in this case, it is preferable that at least one of the plurality of striplines has a line width different from that of the other striplines. According to this preferred example, stronger electromagnetic field coupling can be obtained. Furthermore, in this case, it is preferable that the plurality of striplines are connected to each other by a via hole. According to this preferred example, because of the shield effect of the via hole, the first stripline resonator and the second stripline resonator can be separated from each other. Therefore, a frequency component can be prevented from being transmitted to a receiving side during transmitting, and a frequency component can be prevented from being transmitted to a transmitting side during receiving.

**[0028]** In the first configuration of the duplexer of the

present invention, it is preferable that the duplexer further includes a coupling capacitor provided so as to be overlapped with the coupling line and the stripline resonator with the dielectric layer interposed therebetween.

5 According to this preferred example, the electric field coupling occurring due to a coupling capacitor is combined with electromagnetic field coupling occurring between the stripline resonator and the coupling line. In the coupling occurring between the stripline resonator 10 and the coupling line, a magnetic field component is dominant. However, by providing the coupling capacitor, an electric field capacitance component can be increased.

**[0029]** In the first configuration of the duplexer of the 15 present invention, it is preferable that the first filter and the second filter are provided so as to be opposed to each other with a shield electrode interposed therebetween. According to this preferred example, the first filter for transmitting and the second filter for receiving can 20 be arranged in a vertical direction, so that the area occupied by the duplexer can be reduced.

**[0030]** Furthermore, in this case, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, 25 a fifth dielectric layer, and a sixth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode and an input/output 30 coupling capacitive electrode constituting the first filter, placed between the first dielectric layer and the second dielectric layer; a plurality of resonator electrodes constituting the first filter, placed between the second dielectric layer and the third dielectric layer; a third shield 35 electrode and a coupling line electrode constituting the matching circuit, placed between the third dielectric layer and the fourth dielectric layer; a plurality of resonator electrodes and an input/output line electrode connected to the resonator electrodes constituting the second filter, 40 placed between the fourth dielectric layer and the fifth dielectric layer; an interstage coupling capacitive electrode constituting the second filter, placed between the fifth dielectric layer and the sixth dielectric layer; a second shield electrode placed on a lower surface of the 45 sixth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode, the input/output line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, the fifth dielectric layer, and the sixth dielectric layer; and an end face electrode connecting the first shield electrode, the second shield electrode, and the third shield electrode to each other.

55 **[0031]** According to the above preferred example, the first filter and the second filter can be arranged in a vertical direction, so that the area occupied by the duplexer can be reduced.

**[0032]** A second configuration of a duplexer of the present invention relates to a duplexer composed of a laminate in which dielectric layers and electrode layers are laminated alternately. The duplexer includes: a first shield electrode, a second shield electrode, a third shield electrode, and a fourth shield electrode arranged in the laminate in a lamination direction; a first filter configured in such a manner that a plurality of stripline resonators, each having one end that is short-circuited, are adjacent to each other in parallel, placed between the first shield electrode and the second shield electrode; a matching circuit composed of a coupling line, placed between the second shield electrode and the third shield electrode; and a second filter that is configured in such a manner that a plurality of stripline resonators, each having one end that is short-circuited, are adjacent to each other in parallel and that has a pass band frequency different from that of the first filter, placed between the third shield electrode and the fourth shield electrode. The second and third shield electrodes are provided with coupling windows, and the stripline resonators constituting the first and second filters and the coupling line are coupled to each other by electromagnetic field coupling via the coupling windows, respectively.

**[0033]** According to the second configuration of the duplexer, the versatility of the strength of electromagnetic field coupling can be obtained easily.

**[0034]** In the second configuration of the duplexer of the present invention, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, a fifth dielectric layer, a sixth dielectric layer, a seventh dielectric layer, and an eighth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode and an input/output coupling capacitive electrode constituting the first filter, placed between the first dielectric layer and the second dielectric layer; a plurality of resonator electrodes constituting the first filter, placed between the second dielectric layer and the third dielectric layer; a third shield electrode partially provided with the coupling window, placed between the third dielectric layer and the fourth dielectric layer; a coupling line electrode constituting the matching circuit, placed between the fourth dielectric layer and the fifth dielectric layer; a fourth shield electrode partially provided with the coupling window, placed between the fifth dielectric layer and the sixth dielectric layer; a plurality of resonator electrodes and an input/output line electrode connected to the resonator electrodes constituting the second filter, placed between the sixth dielectric layer and the seventh dielectric layer; an interstage coupling capacitive electrode constituting the second filter, placed between the seventh dielectric layer and the eighth dielectric layer; a second shield electrode placed on a lower surface of the eighth dielectric layer; at least three terminal electrodes connected to the input/output

coupling capacitive electrode, the input/output line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, the fifth dielectric layer, the sixth dielectric layer, the seventh dielectric layer, and the eighth dielectric layer; and an end face electrode connecting the first shield electrode, the second shield electrode, the third shield electrode, and the fourth shield electrode to each other.

**[0035]** According to the above preferred example, a shield electrode is printed onto a dielectric layer, excluding a part thereof, whereby a coupling window is provided in the excluded part. Therefore, the strength of electromagnetic field coupling can be changed easily by such a simple method.

**[0036]** A third configuration of a duplexer of the present invention relates to a duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately. The duplexer includes: a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and a matching circuit composed of a coupling line, having one end that is opened and the other end that is connected to an external terminal, provided between the first filter and the second filter. The first filter includes at least one first stripline resonator, having one end that is short-circuited. The second filter includes at least one second stripline resonator, having one end that is short-circuited, and the first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling.

**[0037]** According to the third configuration of the duplexer, even by using a coupling line, having one end that is opened and the other end that is connected to an external terminal, a duplexer can be operated.

**[0038]** In the third configuration of the duplexer of the present invention, it is preferable that a matching capacitive electrode is connected to an open end side of the coupling line with a dielectric layer interposed therebetween. In the case where one end of the coupling line is opened, floating capacitance is generated in the open end, which causes variation. However, according to this preferred example, such variation can be stabilized. Furthermore, by changing a capacitance value, the degree of design freedom can be obtained.

**[0039]** In the third configuration of the duplexer of the present invention, it is preferable that at least one of the first stripline resonator and the second stripline resonator has a larger line width on an open end side and a smaller line width on a short-circuited end side.

**[0040]** In the third configuration of the duplexer of the present invention, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, and a fourth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on

an upper surface of the first dielectric layer; an interstage coupling capacitive electrode constituting the first filter and an input/output coupling capacitive electrode constituting the second filter, placed between the first dielectric layer and the second dielectric layer; a first resonator electrode constituting the first filter, a second resonator electrode constituting the second filter, and a coupling line electrode constituting the matching circuit, placed between the second dielectric layer and the third dielectric layer; an input/output coupling capacitive electrode constituting the first filter and an interstage coupling capacitor electrode constituting the second filter, placed between the third dielectric layer and the fourth dielectric layer; a second shield electrode placed on a lower surface of the fourth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode constituting the first filter, the input/output coupling capacitive electrode constituting the second filter, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer; and an end face electrode connecting the first shield electrode and the second shield electrode to each other.

**[0041]** Furthermore, in this case, it is preferable that at least one of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer has a dielectric constant different from that of the other dielectric layers.

**[0042]** Furthermore, in this case, it is preferable that the duplexer further includes an adjusting capacitive electrode provided so as to be opposed to the first shield electrode with the first dielectric layer interposed therebetween.

**[0043]** In the third configuration of the duplexer of the present invention, it is preferable that at least one of the first stripline resonator and the second stripline resonator is formed on a dielectric layer different from a dielectric layer on which the coupling line is formed.

**[0044]** In the third configuration of the duplexer of the present invention, it is preferable that the coupling line is composed of at least two striplines having different line widths, connected to each other.

**[0045]** In the third configuration of the duplexer of the present invention, it is preferable that the coupling line is composed of a plurality of striplines, and the plurality of striplines are provided on different dielectric layers. Furthermore, in this case, it is preferable that at least one of the plurality of striplines has a line width different from that of the other striplines. Furthermore, in this case, it is preferable that the plurality of striplines are connected to each other by a via hole.

**[0046]** In the third configuration of the duplexer of the present invention, it is preferable that the duplexer further includes a coupling capacitor provided so as to be overlapped with the coupling line and the stripline resonator with the dielectric layer interposed therebetween.

**[0047]** In the third configuration of the duplexer of the

present invention, it is preferable that the first filter and the second filter are provided so as to be opposed to each other with a shield electrode interposed therebetween.

**5 [0048]** Furthermore, in this case, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, a fifth dielectric layer, and a sixth dielectric layer laminated successively. It also is preferable that the 10 electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode and an input/output coupling capacitive electrode constituting the first filter, placed between the first dielectric layer and the second dielectric layer; a plurality of resonator electrodes constituting the first filter, placed between the second dielectric layer and the third dielectric layer; a third shield electrode and a coupling line electrode constituting the matching circuit, placed between the third dielectric layer and the fourth dielectric layer; a plurality of resonator electrodes and an input/output line electrode connected to the resonator electrodes constituting the second filter, placed between the fourth dielectric layer and the fifth dielectric layer; an interstage coupling capacitive electrode constituting the second filter, placed between the fifth dielectric layer and the sixth dielectric layer; a second shield electrode placed on a lower surface of the sixth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode, the input/output line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, the fifth dielectric layer, and the sixth dielectric layer; and an 30 end face electrode connecting the first shield electrode, the second shield electrode, and the third shield electrode to each other.

**[0049]** A fourth configuration of the duplexer of the present invention relates to a duplexer composed of a 40 laminate in which dielectric layers and electrode layers are laminated alternately. The duplexer includes: a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and a matching circuit composed of a coupling line, provided between the first filter and the second filter. At least one of the first and second filters is a filter composed of a stripline resonator, having one end that is short-circuited, and a transmission line, having band elimination characteristics, and the transmission line and the coupling line are coupled to each other by electromagnetic field coupling.

**[0050]** According to the fourth configuration of the duplexer, the filter eliminates only a particular high-frequency component. In the filter having band elimination characteristics, a loss can be reduced, compared with a filter having band pass characteristics. Therefore, a loss of a transmitting filter can be reduced.

**[0051]** In the fourth configuration of the duplexer of

the present invention, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, and a fifth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode constituting the first filter, placed between the first dielectric layer and the second dielectric layer; a plurality of resonator electrodes constituting the first filter and a coupling line electrode constituting the matching circuit, placed between the second dielectric layer and the third dielectric layer; an input/output coupling capacitive electrode constituting the first filter, a transmission line electrode constituting the second filter, having band elimination characteristics, and a coupling line electrode constituting a matching circuit, placed between the third dielectric layer and the fourth dielectric layer; a resonator electrode constituting the second filter, placed between the fourth dielectric layer and the fifth dielectric layer; a second shield electrode placed on a lower surface of the fifth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode, the transmission line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, and the fifth dielectric layer; and an end face electrode connecting the first shield electrode and the second shield electrode to each other.

**[0052]** According to the above preferred example, because of the above-mentioned lamination configuration, a filter having band elimination characteristics can be formed easily.

**[0053]** In the fourth configuration of the duplexer of the present invention, it is preferable that the laminate includes a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, and a fifth dielectric layer laminated successively. It also is preferable that the electrode layers include: a first shield electrode placed on an upper surface of the first dielectric layer; an interstage coupling capacitive electrode constituting the first filter, placed between the first dielectric layer and the second dielectric layer; a plurality of resonator electrodes constituting the first filter and a coupling line electrode constituting the matching circuit, placed between the second dielectric layer and the third dielectric layer; an input/output coupling capacitive electrode constituting the first filter and a transmission line electrode constituting the second filter, having band elimination characteristics, placed between the third dielectric layer and the fourth dielectric layer; a resonator electrode constituting the second filter, placed between the fourth dielectric layer and the fifth dielectric layer; a second shield electrode placed on a lower surface of the fifth dielectric layer; at least three terminal electrodes connected to the input/output coupling capacitive electrode, the transmission line electrode, and the coupling

line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, and the fifth dielectric layer; and an end face electrode connecting the first shield electrode and the second shield electrode to each other. It also is preferable that a part of the transmission line electrode is overlapped with the coupling line electrode with the third dielectric layer interposed therebetween in a projection in a lamination direction, and in the projection in the lamination direction, a width of the part of the transmission line electrode is the same as or different from that of the coupling line electrode.

**[0054]** According to the above preferred example, since the coupling line electrode and the transmission line electrode are placed in a vertical direction, electromagnetic field coupling becomes stronger. Furthermore, the width of a part of the transmission line electrode is set to be different from that of the coupling line electrode, whereby a margin can be provided with respect to a shift in overlapping during lamination.

**[0055]** A laminate-type high-frequency device of the present invention includes: a duplexer composed of a laminate in which dielectric layers and electrode layers are laminated alternately; and a semiconductor chip and/or a surface acoustic wave device mounted on an upper surface of the laminate. The duplexer of the present invention is used as the duplexer.

**[0056]** According to the configuration of the laminate-type high-frequency device, since a duplexer can be realized with a very simple configuration, a laminate-type high-frequency device, in which a loss can be reduced substantially and which is provided with a semiconductor chip and/or a surface acoustic wave device, can be obtained.

**[0057]** Communication equipment of the present invention includes: an antenna; and a duplexer for transmitting a frequency component output from a transmitting circuit to the antenna and transmitting a frequency component received from the antenna to a receiving circuit, composed of a laminate in which dielectric layers and electrode layers are laminated alternately. The duplexer of the present invention is used as the duplexer.

**[0058]** According to the configuration of the communication equipment, a duplexer can be realized with a very simple configuration. Therefore, communication equipment in which a loss can be reduced substantially can be obtained.

**[0059]** In the configuration of the communication equipment of the present invention, it is preferable that the communication equipment further includes at least one selected from a semiconductor chip and a surface acoustic wave device mounted on an upper surface of the laminate. According to this preferred example, communication equipment can be obtained that includes a laminate-type high-frequency device in which a loss can be reduced substantially and has a semiconductor chip and/or a surface acoustic wave device.

**[0060]** According to the present invention, it is possible to obtain a duplexer that can be realized with a very simple configuration, designed so as to substantially reduce a loss, and miniaturized easily, and a laminate-type high-frequency device and communication equipment using the duplexer. Furthermore, according to the present invention, it is possible to obtain a duplexer that is improved so as to obtain a sufficient degree of design freedom, and a laminate-type high-frequency device and communication equipment using the duplexer.

**[0061]** These and other advantages of the present invention will become apparent to those skilled in the art upon reading and understanding the following detailed description of a number of preferred embodiments of the invention, which is given by way of example only and with reference to the accompanying figures.

FIG. 1 is a schematic view showing a duplexer according to an embodiment of the present invention.

FIG. 2 is an exploded perspective view showing a duplexer according to Embodiment 1 of the present invention.

FIG. 3 is an equivalent circuit diagram of the duplexer according to Embodiment 1 of the present invention.

FIG. 4 shows transmission characteristics of the duplexer according to Embodiment 1 of the present invention.

FIG. 5 is an exploded perspective view showing another example of a duplexer according to the embodiment of the present invention.

FIG. 6 is an exploded perspective view showing a duplexer according to Embodiment 2 of the present invention.

FIG. 7 is an exploded perspective view showing a duplexer according to Embodiment 3 of the present invention.

FIG. 8 is an exploded perspective view showing a duplexer according to Embodiment 4 of the present invention.

FIG. 9 is an exploded perspective view showing a duplexer according to Embodiment 5 of the present invention.

FIG. 10 is an exploded perspective view showing a duplexer according to Embodiment 6 of the present invention.

FIG. 11 is an equivalent circuit diagram of the duplexer according to Embodiment 6 of the present invention.

FIG. 12 is an exploded perspective view showing a duplexer according to Embodiment 7 of the present invention.

FIG. 13 is an exploded perspective view showing a duplexer according to Embodiment 8 of the present invention.

FIG. 14 is an exploded perspective view showing another example of the duplexer according to Embodiment 8 of the present invention.

FIG. 15 is an equivalent circuit diagram of the duplexer according to Embodiment 8 of the present invention shown in FIG. 14.

FIG. 16 is an exploded perspective view showing a duplexer according to Embodiment 9 of the present invention.

FIG. 17 is an equivalent circuit diagram of the duplexer according to Embodiment 9 of the present invention.

FIG. 18 shows transmission characteristics of the duplexer according to Embodiment 9 of the present invention.

FIG. 19 is an exploded perspective view showing another example of the duplexer according to Embodiment 9 of the present invention.

FIG. 20 is an equivalent circuit diagram of the duplexer according to Embodiment 9 of the present invention shown in FIG. 19.

FIG. 21 is an exploded perspective view showing a duplexer according to Embodiment 10 of the present invention.

FIG. 22 is a schematic perspective view showing a laminate-type high-frequency device according to Embodiment 11 of the present invention.

FIG. 23 is a schematic view showing communication equipment according to Embodiment 12 of the present invention.

FIG. 24 is an exploded perspective view showing a duplexer in the prior art.

FIG. 25 is an equivalent circuit diagram of the duplexer in the prior art.

FIG. 26 illustrates the function of a branching circuit.

**[0062]** Hereinafter, the present invention will be described in detail by way of illustrative embodiments with reference to the drawings.

**[0063]** FIG. 1 is a schematic view showing a duplexer according to an embodiment of the present invention. As shown in FIG. 1, in the duplexer according to the present embodiment, resonators and a coupling line are provided integrally in a dielectric substrate. Two filters for transmitting and receiving are configured in a triplet shape. A matching circuit composed of a coupling line is provided between the transmitting filter and the receiving filter. The coupling line is connected to an antenna terminal (ANT). The transmitting filter is connected to a terminal (TX) of a transmitting circuit, and the receiving filter is connected to a terminal (RX) of a receiving circuit. The coupling line is coupled directly to the resonators of the transmitting filter and the receiving filter, closest to the coupling line, by electromagnetic field coupling (M). More specifically, the coupling line is coupled to the resonators of the transmitting and receiving filters in terms of high frequencies by the combination of magnetic coupling and electric field coupling, whereby desired duplexer characteristics are realized. Thus, according to the present embodiment, a duplexer can be realized with a very simple configuration, so that

a loss can be reduced substantially.

### Embodiment 1

**[0064]** FIG. 2 is an exploded perspective view showing a duplexer according to Embodiment 1 of the present invention.

**[0065]** As shown in FIG. 2, the duplexer according to the present embodiment is composed of a laminate in which dielectric layers and electrode layers are laminated alternately. In the laminate, a first filter for transmitting and a second filter for receiving, having different pass band frequencies, are provided. Furthermore, a matching circuit composed of a coupling line 9, having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first filter and the second filter.

**[0066]** The first filter includes two first stripline resonators 3a and 3b, each having one end that is short-circuited. The second filter includes two second stripline resonators 4a and 4b, each having one end that is short-circuited. The coupling line 9 is coupled to the first stripline resonator 3b close to the coupling line 9 by electromagnetic field coupling. The coupling line 9 is coupled to the second stripline resonator 4a close to the coupling line 9 by electromagnetic field coupling.

**[0067]** In the present embodiment, the first stripline resonators 3a, 3b and the second stripline resonators 4a, 4b have a large line width (wide portion) on the open end side and a small line width (narrow portion) on the short-circuited end side. Thus, by adopting a configuration in which the line width of the stripline resonators is varied between the open end side and the short-circuited end side, the coupling degree between the first and second stripline resonators 3b, 4a and the coupling line 9 can be varied arbitrarily, whereby the degree of design freedom is enhanced greatly.

**[0068]** Next, an actual configuration of the duplexer according to the present embodiment will be described in detail.

**[0069]** The laminate includes a first dielectric layer 1a, a second dielectric layer 1b, a third dielectric layer 1c, and a fourth dielectric layer 1d laminated successively. Herein, each dielectric layer may be made of glass ceramic.

**[0070]** The electrode layer has the following configuration. A first shield electrode 2a is placed on an upper surface of the first dielectric layer 1a. An interstage coupling capacitive electrode 5 constituting the first filter and an input/output coupling capacitive electrode 8 constituting the second filter are placed between the first dielectric layer 1a and the second dielectric layer 1b. First resonator electrodes (first stripline resonators) 3a, 3b constituting the first filter, second resonator electrodes (second stripline resonators) 4a, 4b constituting the second filter, and the coupling line electrode (coupling line) 9 are placed between the second dielectric layer 1b and the third dielectric layer 1c. The input/out-

put coupling capacitive electrode 7 constituting the first filter and the interstage coupling capacitive electrode 6 constituting the second filter are placed between the third dielectric layer 1c and the fourth dielectric layer 1d.

5 A second shield electrode 2b is placed on a lower surface of the fourth dielectric layer 1d. At least three terminal electrodes (external terminals) 10a, 10b (terminal electrodes (external terminals) corresponding to the input/output coupling capacitive electrode 7 are not shown) connected to the input/output coupling capacitive electrodes 7, 8 and the coupling line electrode 9 are provided on side surfaces of the first dielectric layer 1a, the second dielectric layer 1b, the third dielectric layer 1c, and the fourth dielectric layer 1d. The first shield electrode 2a and the second shield electrode 2b are connected by end face electrodes 11a and 11b. Herein, each electrode may be made of a conductive material mainly containing silver.

**[0071]** According to the configuration of the present embodiment, a duplexer can be realized with a very simple configuration in which the first resonator electrode 3b and the second resonator electrode 4a are coupled to the coupling line electrode 9 by electromagnetic field coupling without using lumped-constant elements such as an inductor and a capacitor. Therefore, a loss can be reduced substantially.

**[0072]** In the duplexer according to the present embodiment, it is desirable that at least one of the first dielectric layer 1a, the second dielectric layer 1b, the third dielectric layer 1c, and the fourth dielectric layer 1d has a dielectric constant different from those of the other dielectric layers. The dielectric constant can be adjusted by varying the composition of glass ceramic. Thus, by setting the dielectric constant of at least one of a plurality of dielectric layers to be different from those of the other dielectric layers, the capacitance of a capacitor can be adjusted.

**[0073]** FIG. 3 shows an equivalent circuit of the duplexer having the above-mentioned configuration. As shown in FIG. 3, the duplexer according to the present embodiment includes interstage coupling capacitors 23, 24, input/output coupling capacitors 25, 26, a coupling line 27, and resonators 21a, 21b, 22a, and 22b. Herein, the coupling line 27 is coupled to a wide portion of the resonator 21b close to the coupling line 27 by electromagnetic field coupling M1. The coupling line 27 is coupled to a narrow portion of the resonator 21b by electromagnetic field coupling M2. The coupling line 27 is coupled to a wide portion of the resonator 22a close to the coupling line 27 by electromagnetic field coupling M3. Furthermore, the coupling line 27 is coupled to a narrow portion of the resonator 22a by electromagnetic field coupling M4.

**[0074]** FIG. 4 shows the transmission characteristics of a duplexer having the above-mentioned configuration. In FIG. 4, TX→ANT represents the characteristics of the transmitting filter, and ANT→RX represents the characteristics of the receiving filter. As shown in FIG.

4, it is understood that the duplexer according to the present embodiment transmits only a required frequency component and does not transmit an unnecessary frequency component during transmitting. Furthermore, it is understood that the duplexer according to the present embodiment transmits only a required frequency component and does not transmit an unnecessary frequency component during receiving. Thus, if the duplexer according to the present embodiment is used, a frequency component can be prevented from being transmitted to a receiving side during transmitting, and a frequency component can be prevented from being transmitted to the transmitting side during receiving.

**[0075]** As a variation of the present embodiment, it also is desirable that a coupling capacitor 18 is provided so as to be overlapped with the coupling line 9 and the stripline resonators 3b, 4a via the dielectric layer 1b. According to this configuration, the electric field coupling occurring due to the coupling capacitor 18 is combined with the electromagnetic field coupling occurring between the stripline resonators 3b, 4a and the coupling line 9. In the electromagnetic field coupling occurring between the stripline resonators 3b, 4a and the coupling line 9, a magnetic field component is dominant; however, an electric field capacitive component can be increased by providing the coupling capacitor 18.

**[0076]** In the present embodiment, the first filter is configured so as to include the first stripline resonators 3a, 3b, each having one end that is short-circuited, and the second filter is configured so as to include the second stripline resonators 4a, 4b, each having one end that is short-circuited. However, the present invention is not limited thereto. The first filter may include at least one first stripline resonator, having one end that is short-circuited, and the second filter may include at least one second stripline resonator, having one end that is short-circuited.

**[0077]** Furthermore, in the present embodiment, the first stripline resonators 3a, 3b and the second stripline resonators 4a, 4b are configured so as to have a large line width on an open end side and a small line width on a short-circuited side. However, the present invention is not limited thereto. At least one of the first and second stripline resonators may have a large line width on the open end side and a small line width on the short-circuited end side.

## Embodiment 2

**[0078]** FIG. 6 is an exploded perspective view showing a duplexer according to Embodiment 2 of the present invention. The duplexer according to the present embodiment is the same as that according to Embodiment 1 except for the points described below. Therefore, like components are denoted with like numerals, and their description will be omitted here.

**[0079]** As shown in FIG. 6, in the duplexer according to the present embodiment, the first stripline resonators

3a, 3b and the second stripline resonators 4a, 4b are formed on dielectric layers 1c, 1e, different from the dielectric layer 1d on which the coupling line 9 is formed. Thus, by forming the first stripline resonators 3a, 3b and the second stripline resonators 4a, 4b on the dielectric layers 1c, 1e different from the dielectric layer 1d on which the coupling line 9 is formed, the degree of design freedom and versatility can be provided.

**[0080]** Furthermore, the coupling line 9 is composed of two striplines (wide portion and narrow portion) having different line widths and connected to each other. Thus, by configuring the coupling line 9 with two striplines having different line widths, the strength of electromagnetic field coupling can be varied arbitrarily.

**[0081]** In the present embodiment, the first stripline resonators 3a, 3b and the second stripline resonators 4a, 4b are formed on the dielectric layers 1c, 1e different from the dielectric layer 1d on which the coupling line 9 is formed. However, the present invention is not limited thereto. At least one of the first stripline resonator and the second stripline resonator may be formed on a dielectric layer different from a dielectric layer on which a coupling line is formed.

**[0082]** Furthermore, in the present embodiment, although the coupling line 9 is composed of two striplines having different line widths connected to each other, the present invention is not limited thereto. The coupling line may be composed of three or more striplines having different line widths connected to each other.

## Embodiment 3

**[0083]** FIG. 7 is an exploded perspective view showing a duplexer according to Embodiment 3 of the present invention. The duplexer according to the present embodiment is the same as that according to Embodiment 2 except for the points described below. Therefore, like components are denoted with like numerals, and their description will be omitted here.

**[0084]** As shown in FIG. 7, in the duplexer according to the present embodiment, the coupling line is composed of three striplines 9a, 9b, and 9c, and the three striplines 9a, 9b, and 9c are provided on different dielectric layers 1c, 1d, and 1e, respectively. In the case where there is only one stripline, an electric potential is fluctuated. However, by using three striplines, the electric potential can be stabilized.

**[0085]** It is desirable that at least one of the three striplines 9a, 9b, and 9c has a line width different from those of the others. In the present embodiment, all the striplines 9a, 9b, and 9c are configured so as to have different line widths. The electromagnetic field coupling is varied depending upon the line width of a connecting line. Therefore, by varying the line width of the coupling line, stronger electromagnetic field coupling can be obtained.

**[0086]** In the present embodiment, although the coupling line is composed of three striplines 9a, 9b, and 9c,

the present invention is not limited thereto. The coupling line may be composed of a plurality of striplines.

#### Embodiment 4

**[0087]** FIG. 8 is an exploded perspective view showing a duplexer according to Embodiment 4 of the present invention. The duplexer according to the present embodiment is the same as that according to Embodiment 3 except for the points described below. Therefore, like components are denoted with like numerals, and their description will be omitted here.

**[0088]** As shown in FIG. 8, in the duplexer according to the present embodiment, three striplines 9a, 9b, and 9c are connected to each other through a via hole 12.

**[0089]** According to the configuration of the present embodiment, because of the shield effect of the via hole 12, the first stripline resonators 3a, 3b can be separated from the second stripline resonators 4a, 4b. Therefore, a frequency component can be prevented from being transmitted to a receiving side during transmitting, and a frequency component can be prevented from being transmitted to a transmitting side during receiving.

#### Embodiment 5

**[0090]** FIG. 9 is an exploded perspective view showing a duplexer according to Embodiment 5 of the present invention.

**[0091]** As shown in FIG. 9, the duplexer according to the present embodiment is composed of a laminate in which dielectric layers and electrode layers are laminated alternately. In the laminate, a first filter for transmitting and a second filter for receiving, having different pass band frequencies, are provided so as to be opposed to each other with a third shield electrode 32c interposed therebetween. Furthermore, a matching circuit composed of a coupling line 39, having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first filter and the second filter.

**[0092]** The first filter includes two first stripline resonators 33a and 33b, each having one end that is short-circuited. Furthermore, the second filter includes two second stripline resonators 34a and 34b, each having one end that is short-circuited. The coupling line 39 is connected to the first stripline resonator 33b by electromagnetic field coupling. The coupling line 39 is coupled to the second stripline resonator 34b by electromagnetic field coupling.

**[0093]** Next, an actual configuration of the duplexer according to the present embodiment will be described in detail.

**[0094]** The laminate includes a first dielectric layer 31a, a second dielectric layer 31b, a third dielectric layer 31c, a fourth dielectric layer 31d, a fifth dielectric layer 31e, and a sixth dielectric layer 31f laminated successively.

**[0095]** The electrode layer has the following configuration. A first shield electrode 32a is placed on an upper surface of the first dielectric layer 31a. An interstage coupling capacitive electrode 35 and an input/output

5 coupling capacitive electrode 37, constituting the first filter, are placed between the first dielectric layer 31a and the second dielectric layer 31b. The first resonator electrodes (first stripline resonators) 33a and 33b constituting the first filter are placed between the second dielectric layer 31b and the third dielectric layer 31c. The third shield electrode 32c and the coupling line electrode 39 constituting the matching circuit are placed between the third dielectric layer 31c and the fourth dielectric layer 31d. The second resonator electrodes (second stripline resonators) 34a, 34b and the input/output line electrode 38 connected to the resonator electrode 34a, constituting the second filter, are placed between the fourth dielectric layer 31d and the fifth dielectric layer 31e. The interstage coupling capacitive electrode 36 constituting 10 the second filter is placed between the fifth dielectric layer 31e and the sixth dielectric layer 31f. The second shield electrode 32b is placed on a lower surface of the sixth dielectric layer 31f. At least three terminal electrodes 40a, 40b (terminal electrodes corresponding to 15 the input/output coupling capacitive electrode 37 and the input/output line electrode 38 are not shown) connected to the input/output coupling capacitive electrode 37, the input/output line electrode 38, and the coupling line electrode 39, respectively, are provided on side surfaces of the first dielectric layer 31a, the second dielectric layer 31b, the third dielectric layer 31c, the fourth dielectric layer 31d, the fifth dielectric layer 31e, and the sixth dielectric layer 31f. The first shield electrode 32a, 20 the second shield electrode 32b, and the third shield electrode 32c are connected by an end face electrode 41.

**[0096]** According to the configuration of the present embodiment, the first filter for transmitting and the second filter for receiving can be arranged in a vertical direction (lamination direction), so that the area occupied by the duplexer can be decreased. The duplexer of the present embodiment has a laminated configuration, which enables vertical arrangement.

**[0097]** In the present embodiment, the first filter is 25 configured so as to include two first stripline resonators 33a and 33b, each having one end that is short-circuited, and the second filter is configured so as to include two second stripline resonators 34a and 34b, each having one end that is short-circuited. However, the present invention is not limited thereto. The first filter 30 may include three or more first stripline resonators, each having one end that is short-circuited, and the second filter may include three or more second stripline resonators, each having one end that is short-circuited.

#### Embodiment 6

**[0098]** FIG. 10 is an exploded perspective view show-

ing a duplexer according to Embodiment 6 of the present invention.

**[0099]** As shown in FIG. 10, the duplexer according to the present embodiment is composed of a laminate in which dielectric layers and electrode layers are laminated alternately. In the laminate, a first shield electrode 32a, a third shield electrode 32c, a fourth shield electrode 32d, and a second shield electrode 32b are arranged in a lamination direction. A first filter is provided between the first shield electrode 32a and the third shield electrode 32c. In the first filter, two first stripline resonators 33a and 33b, each having one end that is short-circuited, are arranged in parallel so as to be close to each other. A matching circuit composed of a coupling line 39, having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the third shield electrode 32c and the fourth shield electrode 32d. A second filter is provided between the fourth shield electrode 32d and the second shield electrode 32b. In the second filter, two second stripline resonators 34a and 34b, each having one end that is short-circuited, are arranged in parallel so as to be close to each other. The second filter has a different pass band frequency from that of the first filter. Coupling windows 42a and 42b are provided in the third shield electrode 32c and the fourth shield electrode 32d, respectively. The first stripline resonator 33b constituting the first filter and the coupling line 39 are coupled to each other by electromagnetic field coupling via the coupling window 42a. Furthermore, the second stripline resonator 34b constituting the second filter and the coupling line 39 are coupled to each other by electromagnetic field coupling via the coupling window 42b.

**[0100]** In the coupling windows 42a and 42b, shield electrodes are not printed. By providing the coupling windows 42a and 42b, the versatility of the strength of electromagnetic field coupling can be obtained easily.

**[0101]** Next, an actual configuration of the duplexer according to the present embodiment will be described in detail.

**[0102]** The laminate includes a first dielectric layer 31a, a second dielectric layer 31b, a third dielectric layer 31c, a fourth dielectric layer 31g, a fifth dielectric layer 31h, a six dielectric layer 31d, a seventh dielectric layer 31e, and an eighth dielectric layer 31f laminated successively.

**[0103]** The electrode layer has the following configuration. The first shield electrode 32a is placed on an upper surface of the first dielectric layer 31a. An interstage coupling capacitive electrode 35 and an input/output coupling capacitive electrode 37, constituting the first filter, are placed between the first dielectric layer 31a and the second dielectric layer 31b. The first resonator electrodes (first stripline resonators) 33a and 33b constituting the first filter are placed between the second dielectric layer 31b and the third dielectric layer 31c. The third shield electrode 32c partially provided with the coupling window 42a is placed between the third dielectric layer

31c and the fourth dielectric layer 31g. The coupling line electrode 39 constituting the matching circuit, having one end that is short-circuited and the other end that is connected to an external terminal, is placed between the 5 fourth dielectric layer 31g and the fifth dielectric layer 31h. The fourth shield electrode 32d partially provided with the coupling window 42b is placed between the fifth dielectric layer 31h and the sixth dielectric layer 31d. The second resonator electrodes (second stripline resonators) 34a, 34b and the input/output line electrode 38 10 connected to the second resonator electrode 34a, constituting the second filter, are placed between the sixth dielectric layer 31d and the seventh dielectric layer 31e. Furthermore, the interstage coupling capacitive electrode 36 constituting the second filter is placed between the 15 seventh dielectric layer 31e and the eighth dielectric layer 31f. The second shield electrode 32b is placed on a lower surface of the eighth dielectric layer 31f. At least three terminal electrodes 40a, 40b (terminal electrodes 20 corresponding to the input/output coupling capacitive electrode 37 and the input/output line electrode 38 are not shown) connected to the input/output coupling capacitive electrode 37, the input/output line electrode 38, and the coupling line electrode 39, respectively, are provided on side surfaces of the first dielectric layer 31a, the second dielectric layer 31b, the third dielectric layer 31c, the fourth dielectric layer 31g, the fifth dielectric layer 31h, the sixth dielectric layer 31d, the seventh dielectric layer 31e, and the eighth dielectric layer 31f. The 25 first shield electrode 32a, the third shield electrode 32c, the fourth shield electrode 32d, and the second shield electrode 32b are connected by an end face electrode 41.

**[0104]** According to the configuration of the present embodiment, the shield electrodes are printed onto the dielectric layers, excluding a part thereof, whereby coupling windows 42a and 42b are provided. Thus, the strength of electromagnetic field coupling can be changed easily by such a simple method.

**[0105]** FIG. 11 shows an equivalent circuit of a duplexer having the above-mentioned configuration. The equivalent circuit of the duplexer in the present embodiment is the same as that shown in FIG. 3 except for the following points. Therefore, like components are denoted with like reference numerals, and their description will be omitted here.

**[0106]** The equivalent circuit of the duplexer in the present embodiment is different from that shown in FIG. 3 in that a coupling capacitor 28a is provided so as to be overlapped with the coupling line 27 and the stripline resonator 21b with a dielectric layer interposed therebetween, and a coupling capacitor 28b is provided so as to be overlapped with the coupling line 27 and the stripline resonator 22a with a dielectric layer interposed therebetween. Because of this, the electric field coupling occurring due to the coupling capacitors 28a and 28b is combined with the electromagnetic field coupling occurring between the stripline resonators 22a, 21b and the

coupling line 27. In the electromagnetic field coupling occurring between the stripline resonators 22a, 21b and the coupling line 27, a magnetic field component is dominant; however, an electric field capacitive component can be increased by providing the coupling capacitors 28a and 28b.

#### Embodiment 7

**[0107]** FIG. 12 is an exploded perspective view showing a duplexer according to Embodiment 7 of the present invention. The duplexer according to the present embodiment is the same as that according to Embodiment 1 shown in FIG. 2 except for the points described below. Therefore, like components are denoted with like reference numerals, and their description will be omitted here.

**[0108]** As shown in FIG. 12, in the duplexer according to the present embodiment, an adjusting capacitive electrode 13 is provided so as to be opposed to the first shield electrode 2a with the first dielectric layer 1a interposed therebetween. An antenna is used for both transmitting and receiving. The matching between transmitting and receiving can be optimized by providing the adjusting capacitive electrode 13.

#### Embodiment 8

**[0109]** FIG. 13 is an exploded perspective view showing a duplexer according to Embodiment 8 of the present invention. The duplexer according to the present embodiment is the same as that according to Embodiment 1 shown in FIG. 2 except for the points described below. Therefore, like components are denoted with like reference numerals, and their description will be omitted here.

**[0110]** As shown in FIG. 13, in the duplexer according to the present embodiment, a matching circuit composed of a coupling line 9, having one end that is opened and the other end that is connected to an external terminal, is provided between the first filter and the second filter. Thus, by using the coupling line 9, having one end that is opened and the other end that is connected to an external terminal, a duplexer can be operated.

**[0111]** As a variation of the present embodiment, as shown in FIG. 14, it is desirable that a matching capacitive electrode 14 is provided on an open end side of the coupling line 9 with the third dielectric layer 1c interposed therebetween. In the case where one end of the coupling line 9 is opened, floating capacitance is generated on the open end, which causes the variation in capacitance. However, by providing the matching capacitive electrode 14 on the open end side of the coupling line 9 with the third dielectric layer 1c interposed therebetween, a capacitance value can be stabilized. Furthermore, by changing the capacitance value of matching capacitance, the degree of design freedom can be obtained.

**[0112]** FIG. 15 shows an equivalent circuit of the duplexer shown in FIG. 14. The equivalent circuit shown in FIG. 15 is the same as that shown in FIG. 3 except for the points described below. Therefore, like components are denoted with like reference numerals, and their description will be omitted here.

**[0113]** The equivalent circuit shown in FIG. 15 is different from that shown in FIG. 3, in that a matching capacitor 29 is provided on the open end side of a coupling line 27.

**[0114]** Furthermore, as a variation of the present embodiment, it is desirable to use the configurations described in Embodiments 2 to 5 or 7.

#### Embodiment 9

**[0115]** FIG. 16 is an exploded perspective view showing a duplexer according to Embodiment 9 of the present invention.

**[0116]** As shown in FIG. 16, the duplexer according to the present embodiment is composed of a laminate in which dielectric layers and electrode layers are laminated alternately. In the laminate, a first filter for transmitting and a second filter for receiving having different pass band frequencies are provided. At least one of the first and second filters (second filter in the present embodiment) is composed of second stripline resonators 54a, 54b, each having one end that is short-circuited and a transmission line 57, having band elimination characteristics. A matching circuit composed of coupling lines 58a and 58b, each having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first filter and the second filter. The transmission line 57 is coupled to the coupling line 58b by electromagnetic field coupling.

**[0117]** Next, an actual configuration of the duplexer according to the present embodiment will be described in detail.

**[0118]** The laminate includes a first dielectric layer 51a, a second dielectric layer 51b, a third dielectric layer 51c, a fourth dielectric layer 51d, and a fifth dielectric layer 51e laminated successively.

**[0119]** The electrode layer has the following configuration. A first shield electrode 52a is placed on an upper surface of the first dielectric layer 51a. An interstage coupling capacitive electrode 55 constituting the first filter is placed between the first dielectric layer 51a and the second dielectric layer 51b. First resonator electrodes (first stripline resonators) 53a, 53b constituting the first filter, and the coupling line electrode 58a constituting a matching circuit are placed between the second dielectric layer 51b and the third dielectric layer 51c. An input/output coupling capacitive electrode 56 constituting the first filter, the transmission line electrode 57 constituting the second filter having band elimination characteristics, and the coupling line electrode 58b constituting a matching circuit are placed between the third dielectric layer 51c and the fourth dielectric layer 51d.

The second resonator electrodes (second stripline resonators) 54a and 54b constituting the second filter are placed between the fourth dielectric layer 51d and the fifth dielectric layer 51e. A second shield electrode 52b is placed on a lower surface of the fifth dielectric layer 51e. At least three terminal electrodes 59a, 59b (terminal electrodes corresponding to the input/output coupling capacitive electrode 56 are not shown) connected to the input/output coupling capacitive electrode 56, the transmission line electrode 57, and the coupling line electrodes 58a, 58b are provided on side surfaces of the first dielectric layer 51a, the second dielectric layer 51b, the third dielectric layer 51c, the fourth dielectric layer 51d, and the fifth dielectric layer 51e. The first shield electrode 52a and the second shield electrode 52b are connected by end face electrodes 60a and 60b.

**[0120]** According to the configuration of the present embodiment, because of the above-mentioned laminated configuration, a filter having band elimination characteristics can be formed easily.

**[0121]** FIG. 17 shows an equivalent circuit of a duplexer having the above-mentioned configuration. As shown in FIG. 17, the duplexer of the present embodiment includes an interstage coupling capacitor 63, an input/output coupling capacitor 64, and a transmission line 65. The first filter includes resonators 61a and 61b. A resonator 62a is connected to the transmission line 65 via a notch capacitor 66a, and a resonator 62b is connected to the transmission line 65 via a notch capacitor 66b.

**[0122]** FIG. 18 shows the transmission characteristics of a duplexer having the above-mentioned configuration. In the present embodiment, the second filter for transmitting is composed of the second stripline resonators 54a, 54b, each having one end that is short-circuited, and the transmission line 57, having band elimination characteristics. Therefore, the second filter eliminates only a particular high-frequency component. The loss can be reduced in the filter having band elimination characteristics, compared with the filter having band pass characteristics. Therefore, the loss of a transmitting filter can be reduced.

**[0123]** As a variation of the present embodiment, even when a matching circuit composed of coupling lines 58a and 58b, each having one end that is opened and the other end that is connected to an external terminal, is provided between the first and second filters as shown in FIG. 19, the same effects can be obtained.

**[0124]** FIG. 20 shows an equivalent circuit of the duplexer shown in FIG. 19.

**[0125]** In the present embodiment, the first filter is configured so as to include the first stripline resonators 53a, 53b, each having one end that is short-circuited, and the second filter is configured so as to include the second stripline resonators 54a, 54b, each having one end that is short-circuited. However, the present invention is not limited thereto. The first filter may include three or more first stripline resonators, each having one

end that is short-circuited, and the second filter may include three or more second stripline resonators, each having one end that is short-circuited.

##### 5 Embodiment 10

**[0126]** FIG. 21 is an exploded perspective view showing a duplexer according to Embodiment 10 of the present invention. The duplexer according to the present embodiment is a variation of the duplexer according to Embodiment 9.

**[0127]** As shown in FIG. 21, a laminate includes a first dielectric layer 51a, a second dielectric layer 51b, a third dielectric layer 51c, a fourth dielectric layer 51d, and a fifth dielectric layer 51e laminated successively.

**[0128]** The electrode layer has the following configuration. A first shield electrode 52a is placed on an upper surface of the first dielectric layer 51a. An interstage coupling capacitive electrode 55 constituting the first filter is placed between the first dielectric layer 51a and the second dielectric layer 51b. First resonator electrodes (first stripline resonators) 53a and 53b constituting the first filter, and a coupling line electrode 58c constituting a matching circuit are placed between the second dielectric layer 51b and the third dielectric layer 51c. An input/output coupling capacitive electrode 56 constituting the first filter and a transmission line electrode 57 constituting the second filter having band elimination characteristics are placed between the third dielectric layer 51c and the fourth dielectric layer 51d. Second resonator electrodes (second stripline resonators) 54a and 54b constituting the second filter are placed between the fourth dielectric layer 51d and the fifth dielectric layer 51e. A second shield electrode 52b is placed on a lower surface of the fifth dielectric layer 51e. At least three terminal electrodes 59a, 59b (terminal electrodes corresponding to the input/output coupling capacitive electrode 56 are not shown) connected to the input/output coupling capacitive electrode 56, the transmission line electrode 57, and the coupling line electrode 58c, respectively, are provided on side surfaces of the first dielectric layer 51a, the second dielectric layer 51b, the third dielectric layer 51c, the fourth dielectric layer 51d, and the fifth dielectric layer 51e. The first shield electrode 52a and the second shield electrode 52b are connected by end face electrodes 60a and 60b.

**[0129]** Apart 57a of the transmission line electrode 57 is overlapped with the coupling line electrode 58c with the third dielectric layer 51c interposed therebetween in a projection in a lamination direction. The width of the part 57a of the transmission line electrode 57 is different from that of the coupling line electrode 58c in the projection in the lamination direction.

**[0130]** According to the configuration of the present embodiment, the coupling line electrode 58c and the transmission line electrode 57 are placed in a vertical direction, so that electromagnetic field coupling becomes further stronger. Furthermore, the width of the

part 57a of the transmission line electrode 57 is set to be different from that of the coupling line electrode 58c, whereby a margin can be provided with respect to a shift in overlapping during lamination. The width of the part 57a of the transmission line electrode 57 may be the same as that of the coupling line electrode 58c.

### Embodiment 11

**[0131]** FIG. 22 is a schematic perspective view showing a laminate-type high-frequency device according to Embodiment 11 of the present invention. As shown in FIG. 22, the laminate-type high-frequency device according to the present embodiment includes a duplexer composed of a laminate 71 in which dielectric layers and electrode layers are laminated alternately. A semiconductor chip 72, a surface acoustic wave device 73, a PIN diode 74, a chip capacitor 75, and a chip resistor 76 are mounted on an upper surface of the laminate 71.

**[0132]** In the present embodiment, a duplexer having the configuration described in each of the above-mentioned embodiments is used. For example, a first filter for transmitting and a second filter for receiving having different pass band frequencies are provided in the laminate 71. A matching circuit composed of a coupling line, having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first and second filters. The first filter includes at least one first stripline resonator, having one end that is short-circuited. The second filter includes at least one second stripline resonator, having one end that is short-circuited. The first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling.

**[0133]** According to the configuration of the present embodiment, a duplexer can be realized with a very simple configuration. Therefore, a laminate-type high-frequency device can be obtained, in which a loss can be reduced substantially and which is provided with a semiconductor chip and/or a surface acoustic wave device.

### Embodiment 12

**[0134]** FIG. 23 is a schematic view showing communication equipment according to Embodiment 12 of the present invention.

**[0135]** As shown in FIG. 23, communication equipment according the present embodiment includes an antenna 85, and a duplexer 84 composed of a laminate in which dielectric layers and electrode layers are laminated alternately, for transmitting a frequency component output from a transmitting circuit to the antenna 85 and transmitting a frequency component received from the antenna 85 to a receiving circuit. The duplexer 84 is connected to a transmitting circuit portion 82 and a receiving circuit portion 83, and the transmitting circuit portion 82 and the receiving circuit portion 83 are connected to a base band portion 81.

**[0136]** As the duplexer 84, a duplexer having the configuration as described in each of the above-mentioned embodiments is used. For example, a first filter for transmitting and a second filter for receiving having different

5 pass band frequencies are provided in a laminate. A matching circuit composed of a coupling line, having one end that is short-circuited and the other end that is connected to an external terminal, is provided between the first and second filters. The first filter includes at least one first stripline resonator, having one end that is short-circuited. The second filter includes at least one second stripline resonator, having one end that is short-circuited. The first stripline resonator and the second stripline resonator are connected to the coupling line by electromagnetic field coupling.

**[0137]** According to the configuration of the present embodiment, a duplexer can be realized with a very simple configuration. Therefore, communication equipment can be obtained, in which a loss can be reduced substantially.

**[0138]** As a variation of the present embodiment, a semiconductor chip and/or a surface acoustic wave device may be mounted on an upper surface of the laminate. According to this configuration, communication equipment can be obtained, including a laminate-type high-frequency device in which a loss can be reduced substantially and which is provided with a semiconductor chip and/or a surface acoustic wave device.

### **Claims**

**1.** A duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately, comprising:

a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and a matching circuit comprising a coupling line, having one end that is short-circuited and the other end that is connected to an external terminal, provided between the first filter and the second filter,

45 wherein the first filter comprises at least one first stripline resonator, having one end that is short-circuited,

50 the second filter comprises at least one second stripline resonator, having one end that is short-circuited, and

55 the first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling.

**2.** A duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately, comprising:

a first shield electrode, a second shield electrode, a third shield electrode, and a fourth shield electrode arranged in the laminate in a lamination direction;  
 a first filter configured in such a manner that a plurality of stripline resonators, each having one end that is short-circuited, are adjacent to each other in parallel, placed between the first shield electrode and the second shield electrode;  
 a matching circuit comprising a coupling line, placed between the second shield electrode and the third shield electrode; and  
 a second filter that is configured in such a manner that a plurality of stripline resonators, each having one end that is short-circuited, are adjacent to each other in parallel and that has a pass band frequency different from that of the first filter, placed between the third shield electrode and the fourth shield electrode,

wherein the second and third shield electrodes are provided with coupling windows, and  
 the stripline resonators comprising the first and second filters and the coupling line are coupled to each other by electromagnetic field coupling via the coupling windows, respectively.

3. The duplexer according to claim 2, wherein the laminate comprises a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, a fifth dielectric layer, a sixth dielectric layer, a seventh dielectric layer, and an eighth dielectric layer laminated successively,  
 the electrode layers include:

a first shield electrode placed on an upper surface of the first dielectric layer;  
 an interstage coupling capacitive electrode and an input/output coupling capacitive electrode comprising the first filter, placed between the first dielectric layer and the second dielectric layer;  
 a plurality of resonator electrodes comprising the first filter, placed between the second dielectric layer and the third dielectric layer;  
 a third shield electrode partially provided with the coupling window, placed between the third dielectric layer and the fourth dielectric layer;  
 a coupling line electrode comprising the matching circuit, placed between the fourth dielectric layer and the fifth dielectric layer;  
 a fourth shield electrode partially provided with the coupling window, placed between the fifth dielectric layer and the sixth dielectric layer;  
 a plurality of resonator electrodes and an input/output line electrode connected to the resonator electrodes comprising the second filter,

placed between the sixth dielectric layer and the seventh dielectric layer;  
 an interstage coupling capacitive electrode comprising the second filter, placed between the seventh dielectric layer and the eighth dielectric layer;  
 a second shield electrode placed on a lower surface of the eighth dielectric layer;  
 at least three terminal electrodes connected to the input/output coupling capacitive electrode, the input/output line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, the fifth dielectric layer, the sixth dielectric layer, the seventh dielectric layer, and the eighth dielectric layer; and  
 an end face electrode connecting the first shield electrode, the second shield electrode, the third shield electrode, and the fourth shield electrode to each other.

4. A duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately, comprising:

a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and  
 a matching circuit comprising a coupling line, having one end that is opened and the other end that is connected to an external terminal, provided between the first filter and the second filter,

wherein the first filter comprises at least one first stripline resonator, having one end that is short-circuited,

the second filter comprises at least one second stripline resonator, having one end that is short-circuited, and

the first stripline resonator and the second stripline resonator are coupled to the coupling line by electromagnetic field coupling.

5. The duplexer according to claim 4, wherein a matching capacitive electrode is connected on an open end side of the coupling line with a dielectric layer interposed therebetween.

6. The duplexer according to claim 1, 4 or 5, wherein at least one of the first stripline resonator and the second stripline resonator has a larger line width on an open end side and a smaller line width on a short-circuited end side.

7. The duplexer according to claim 1, 4, 5 or 6, wherein the laminate comprises a first dielectric layer, a sec-

ond dielectric layer, a third dielectric layer, and a fourth dielectric layer laminated successively, and the electrode layers include:

a first shield electrode placed on an upper surface of the first dielectric layer;  
 an interstage coupling capacitive electrode comprising the first filter and an input/output coupling capacitive electrode comprising the second filter, placed between the first dielectric layer and the second dielectric layer;  
 a first resonator electrode comprising the first filter, a second resonator electrode comprising the second filter, and a coupling line electrode comprising the matching circuit, placed between the second dielectric layer and the third dielectric layer;  
 an input/output coupling capacitive electrode comprising the first filter and an interstage coupling capacitor electrode comprising the second filter, placed between the third dielectric layer and the fourth dielectric layer;  
 a second shield electrode placed on a lower surface of the fourth dielectric layer;  
 at least three terminal electrodes connected to the input/output coupling capacitive electrode comprising the first filter, the input/output coupling capacitive electrode comprising the second filter, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer; and  
 an end face electrode connecting the first shield electrode and the second shield electrode to each other.

8. The duplexer according to claim 7, wherein at least one of the first dielectric layer, the second dielectric layer, the third dielectric layer, and the fourth dielectric layer has a dielectric constant different from that of the other dielectric layers.

9. The duplexer according to claim 1 or any one of claims 4 to 8, wherein at least one of the first stripline resonator and the second stripline resonator is formed on a dielectric layer different from a dielectric layer on which the coupling line is formed.

10. The duplexer according to claim 1 or any one of claims 4 to 9, wherein the coupling line comprises at least two striplines having different line widths, connected to each other.

11. The duplexer according to claim 1 or any one of claims 4 to 10, wherein the coupling line comprises a plurality of striplines, and the plurality of striplines are provided on different dielectric layers.

12. The duplexer according to claim 11, wherein at least one of the plurality of striplines has a line width different from that of the other striplines.

13. The duplexer according to claim 11, wherein the plurality of striplines are connected to each other by a via hole.

14. The duplexer according to claim 1 or any one of claims 4 to 13, further comprising a coupling capacitor provided so as to be overlapped with the coupling line and the stripline resonator with the dielectric layer interposed therebetween.

15. The duplexer according to claim 7, further comprising an adjusting capacitive electrode provided so as to be opposed to the first shield electrode with the first dielectric layer interposed therebetween.

20 16. The duplexer according to claim 1 or any one of claims 4 to 15, wherein the first filter and the second filter are provided so as to be opposed to each other with a shield electrode interposed therebetween.

25 17. The duplexer according to claim 16, wherein the laminate comprises a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, a fifth dielectric layer, and a sixth dielectric layer laminated successively, and the electrode layers include:

30 a first shield electrode placed on an upper surface of the first dielectric layer;  
 an interstage coupling capacitive electrode and an input/output coupling capacitive electrode comprising the first filter, placed between the first dielectric layer and the second dielectric layer;  
 a plurality of resonator electrodes comprising the first filter, placed between the second dielectric layer and the third dielectric layer;  
 a third shield electrode and a coupling line electrode comprising the matching circuit, placed between the third dielectric layer and the fourth dielectric layer;  
 a plurality of resonator electrodes and an input/output line electrode connected to the resonator electrodes comprising the second filter, placed between the fourth dielectric layer and the fifth dielectric layer;  
 an interstage coupling capacitive electrode comprising the second filter, placed between the fifth dielectric layer and the sixth dielectric layer;  
 a second shield electrode placed on a lower surface of the sixth dielectric layer;  
 at least three terminal electrodes connected to the input/output coupling capacitive electrode,

the input/output line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, the fifth dielectric layer, and the sixth dielectric layer; and  
 an end face electrode connecting the first shield electrode, the second shield electrode, and the third shield electrode to each other.

18. A duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately, comprising:

a first filter for transmitting and a second filter for receiving, which are provided in the laminate and have different pass band frequencies; and a matching circuit comprising a coupling line, provided between the first filter and the second filter,

wherein at least one of the first and second filters is a filter comprising a stripline resonator, having one end that is short-circuited, and a transmission line, having a band elimination characteristics, and

the transmission line and the coupling line are coupled to each other by electromagnetic field coupling.

19. The duplexer according to claim 18, wherein the laminate comprises a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, and a fifth dielectric layer laminated successively, and

the electrode layers include:

a first shield electrode placed on an upper surface of the first dielectric layer;

an interstage coupling capacitive electrode comprising the first filter, placed between the first dielectric layer and the second dielectric layer;

a plurality of resonator electrodes comprising the first filter and a coupling line electrode comprising the matching circuit, placed between the second dielectric layer and the third dielectric layer;

an input/output coupling capacitive electrode comprising the first filter, a transmission line electrode comprising the second filter, having band elimination characteristics, and a coupling line electrode comprising a matching circuit, placed between the third dielectric layer and the fourth dielectric layer;

a resonator electrode comprising the second filter, placed between the fourth dielectric layer and the fifth dielectric layer;

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a second shield electrode placed on a lower surface of the fifth dielectric layer;

at least three terminal electrodes connected to the input/output coupling capacitive electrode, the transmission line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, and the fifth dielectric layer; and

an end face electrode connecting the first shield electrode and the second shield electrode to each other.

20. The duplexer according to claim 18, wherein the laminate comprises a first dielectric layer, a second dielectric layer, a third dielectric layer, a fourth dielectric layer, and a fifth dielectric layer laminated successively, and

the electrode layers include:

a first shield electrode placed on an upper surface of the first dielectric layer;

an interstage coupling capacitive electrode comprising the first filter, placed between the first dielectric layer and the second dielectric layer;

a plurality of resonator electrodes comprising the first filter and a coupling line electrode comprising the matching circuit, placed between the second dielectric layer and the third dielectric layer;

an input/output coupling capacitive electrode comprising the first filter and a transmission line electrode comprising the second filter, having band elimination characteristics, placed between the third dielectric layer and the fourth dielectric layer;

a resonator electrode comprising the second filter, placed between the fourth dielectric layer and the fifth dielectric layer;

a second shield electrode placed on a lower surface of the fifth dielectric layer;

at least three terminal electrodes connected to the input/output coupling capacitive electrode, the transmission line electrode, and the coupling line electrode, respectively, provided on side surfaces of the first dielectric layer, the second dielectric layer, the third dielectric layer, the fourth dielectric layer, and the fifth dielectric layer; and

an end face electrode connecting the first shield electrode and the second shield electrode to each other,

a part of the transmission line electrode is overlapped with the coupling line electrode with the third dielectric layer interposed therebetween in a projection in a lamination direction, and

in the projection in the lamination direction, a width of the part of the transmission line electrode is the same as or different from that of the coupling line electrode.

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**21. Alamine-type high-frequency device, comprising:**

a duplexer comprising a laminate in which dielectric layers and electrode layers are laminated alternately; and

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a semiconductor chip and/or a surface acoustic wave device mounted on an upper surface of the laminate,

wherein, as the duplexer, the duplexer of any one of claims 1 to 20 is used.

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**22. Communication equipment, comprising:**

an antenna; and

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a duplexer for transmitting a frequency component output from a transmitting circuit to the antenna and transmitting a frequency component received from the antenna to a receiving circuit, comprising a laminate in which dielectric layers and electrode layers are laminated alternately,

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wherein, as the duplexer, the duplexer of any one of claims 1 to 20 is used.

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**23. Communication equipment according to claim 22, further comprising at least one selected from a semiconductor chip and a surface acoustic wave device mounted on an upper surface of the laminate.**

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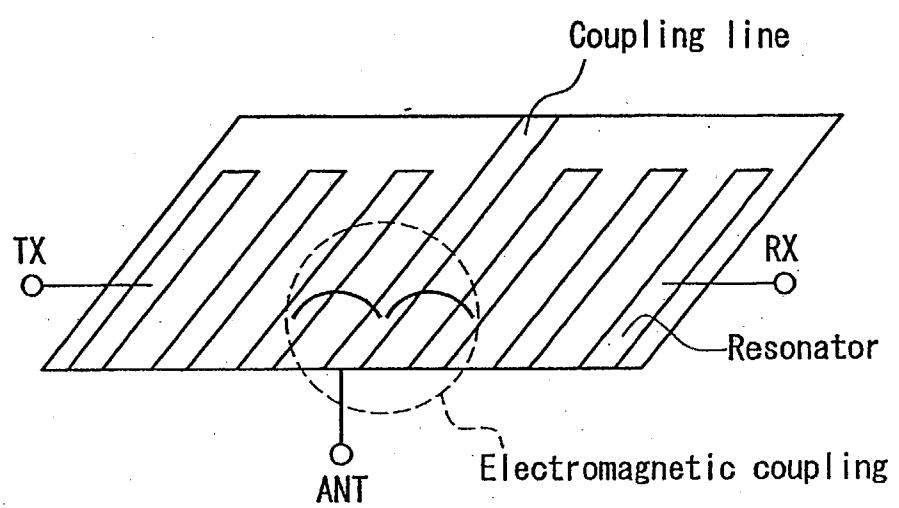


FIG. 1

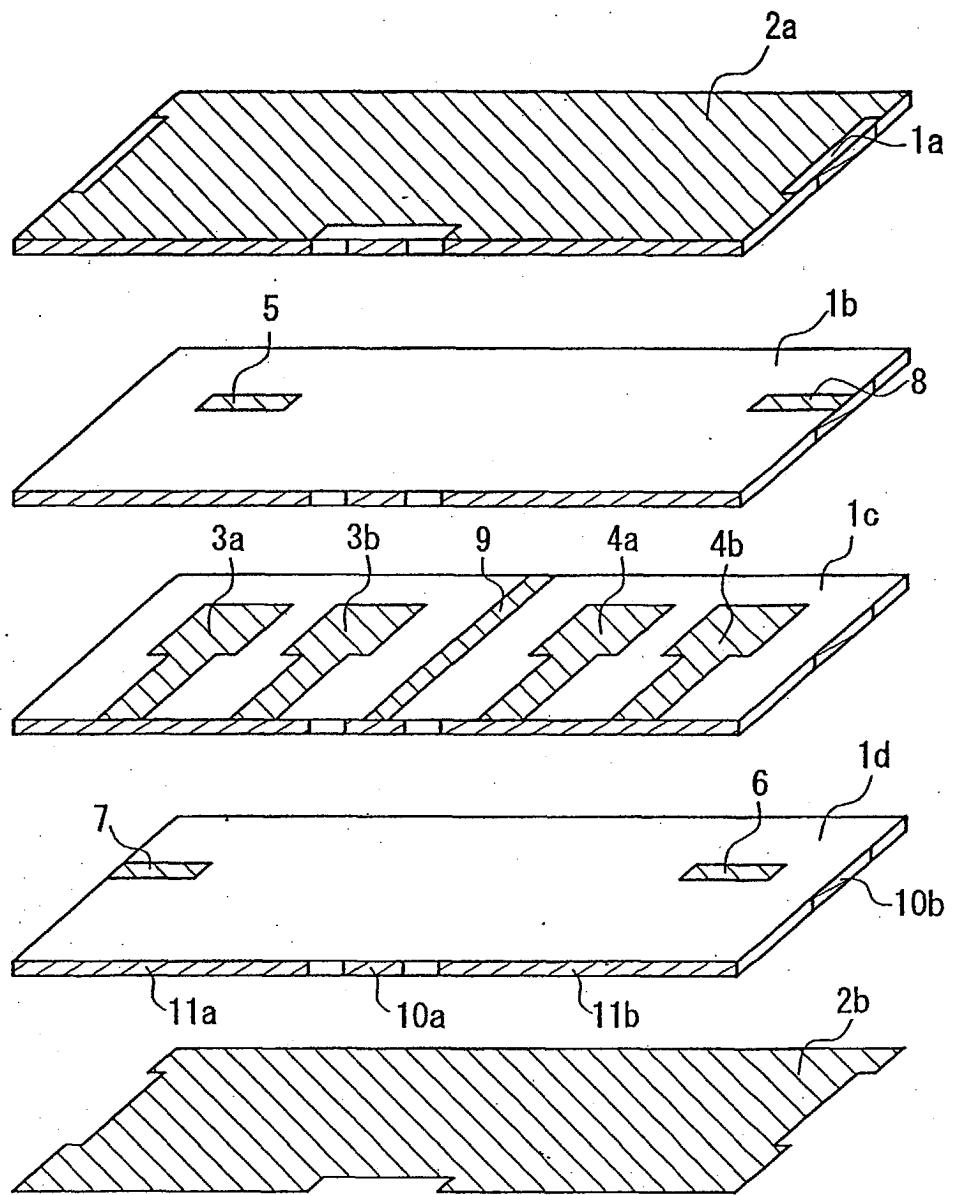


FIG. 2

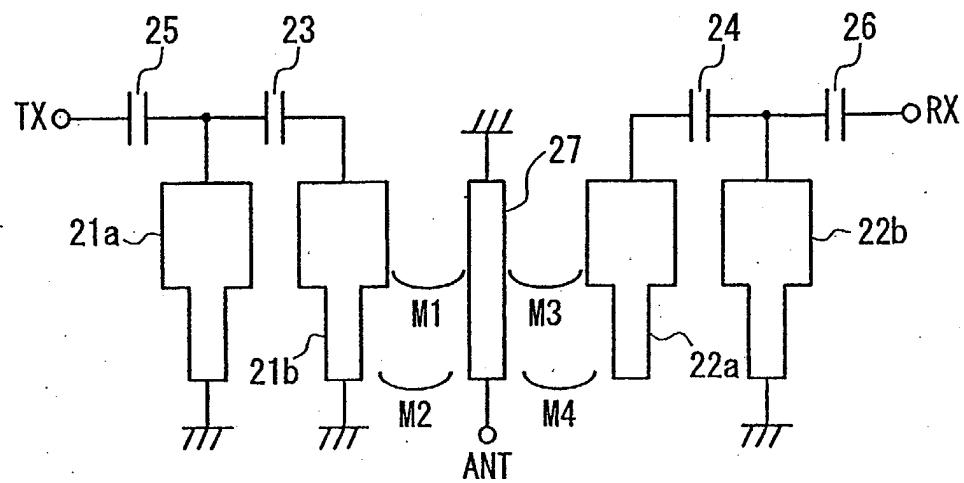


FIG. 3

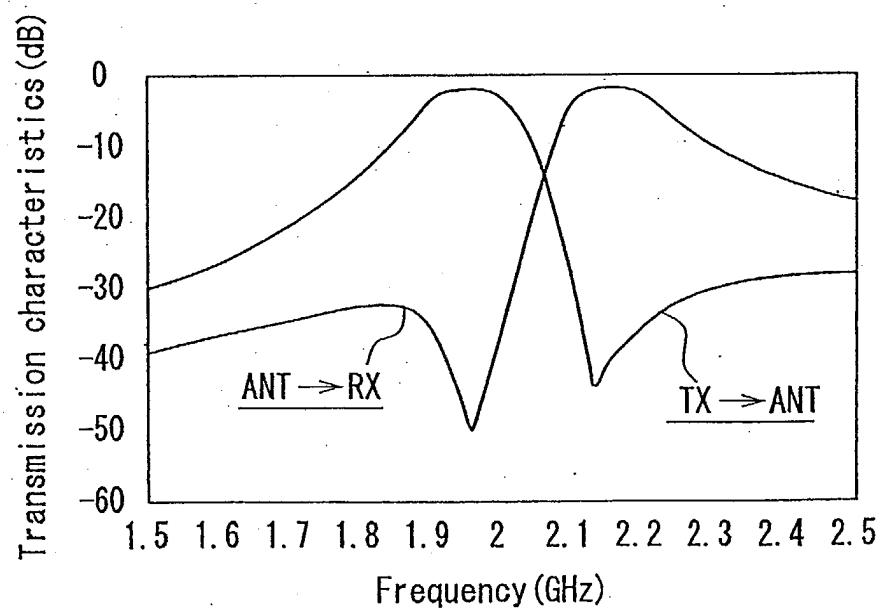


FIG. 4

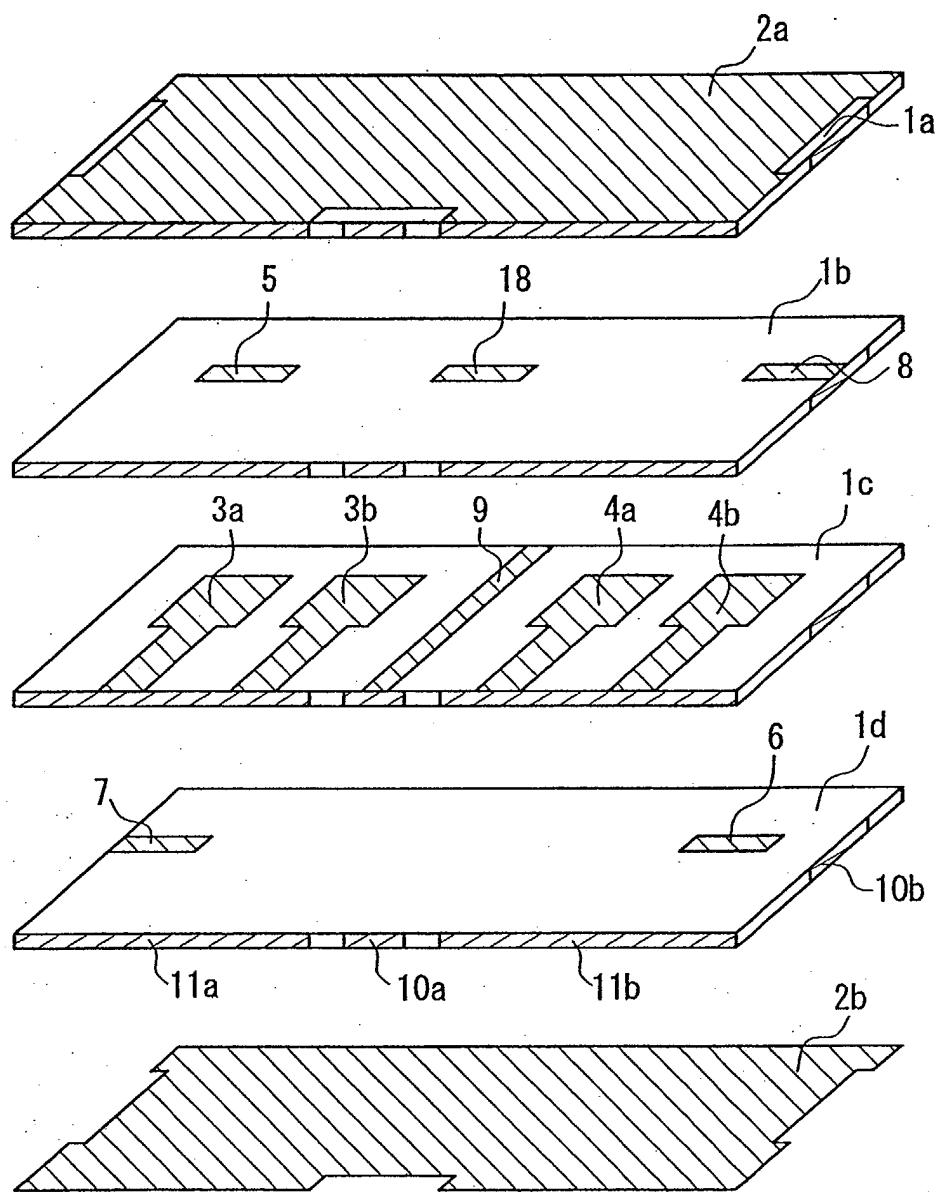


FIG. 5

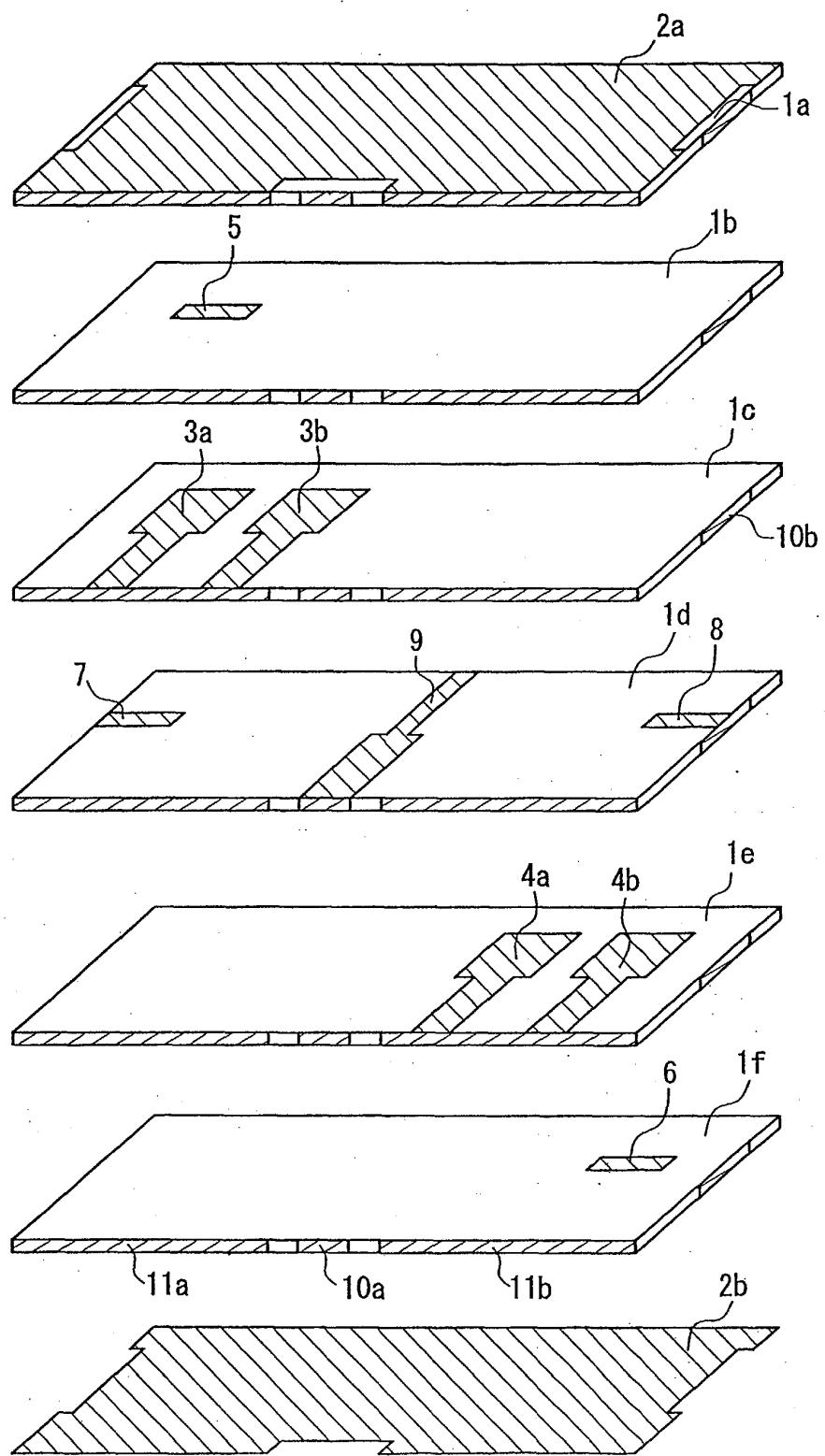


FIG. 6

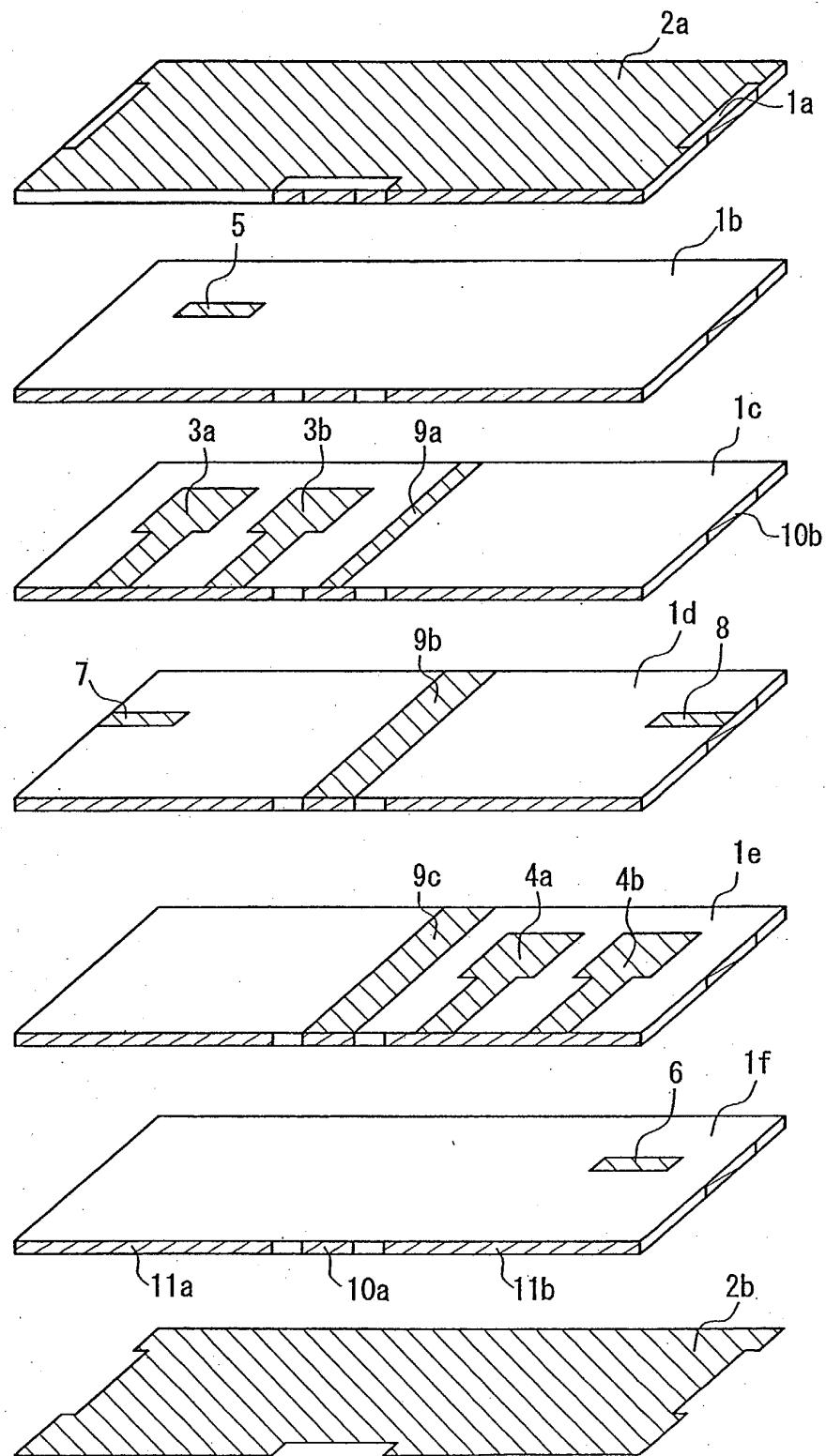


FIG. 7

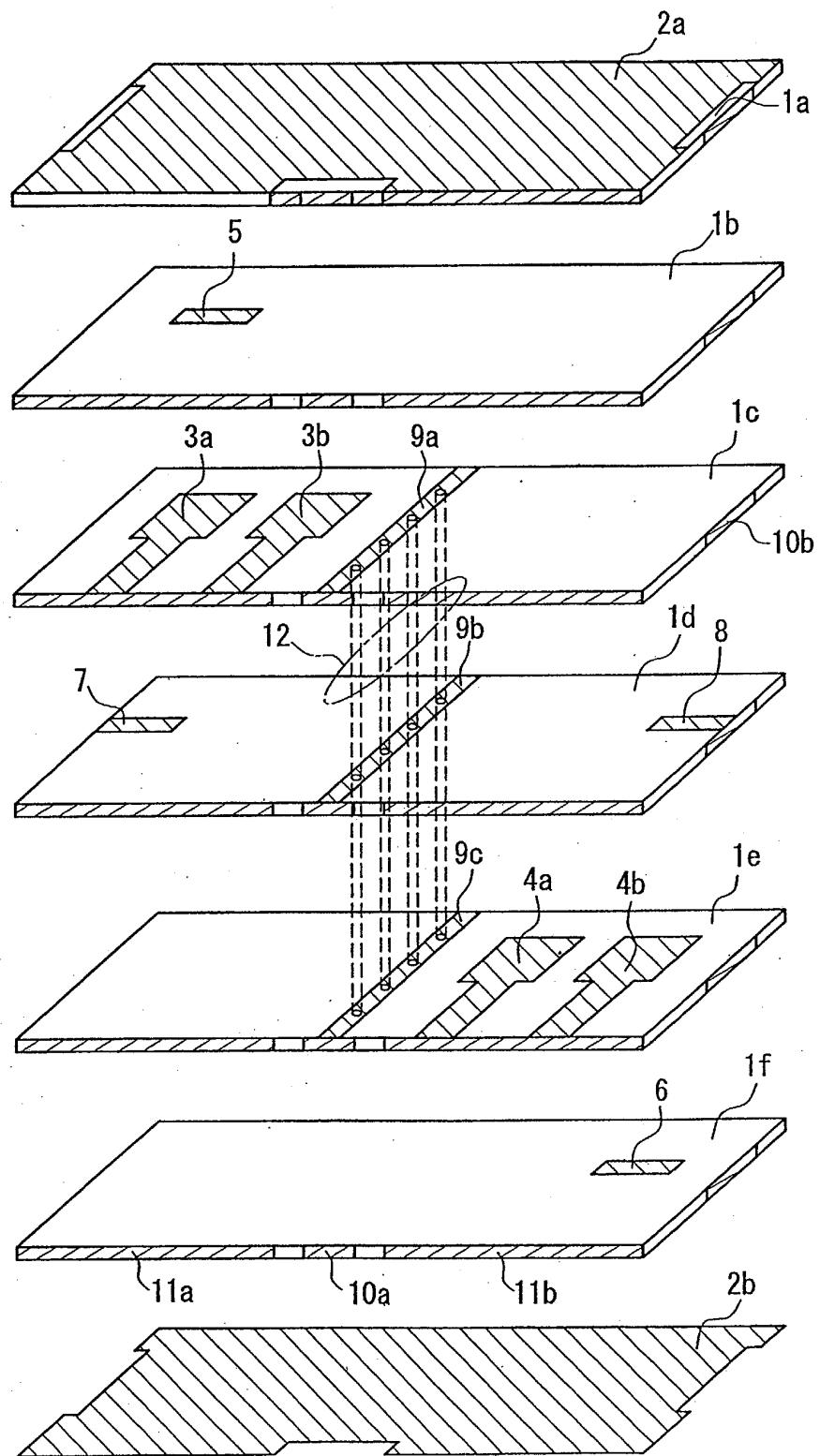


FIG. 8

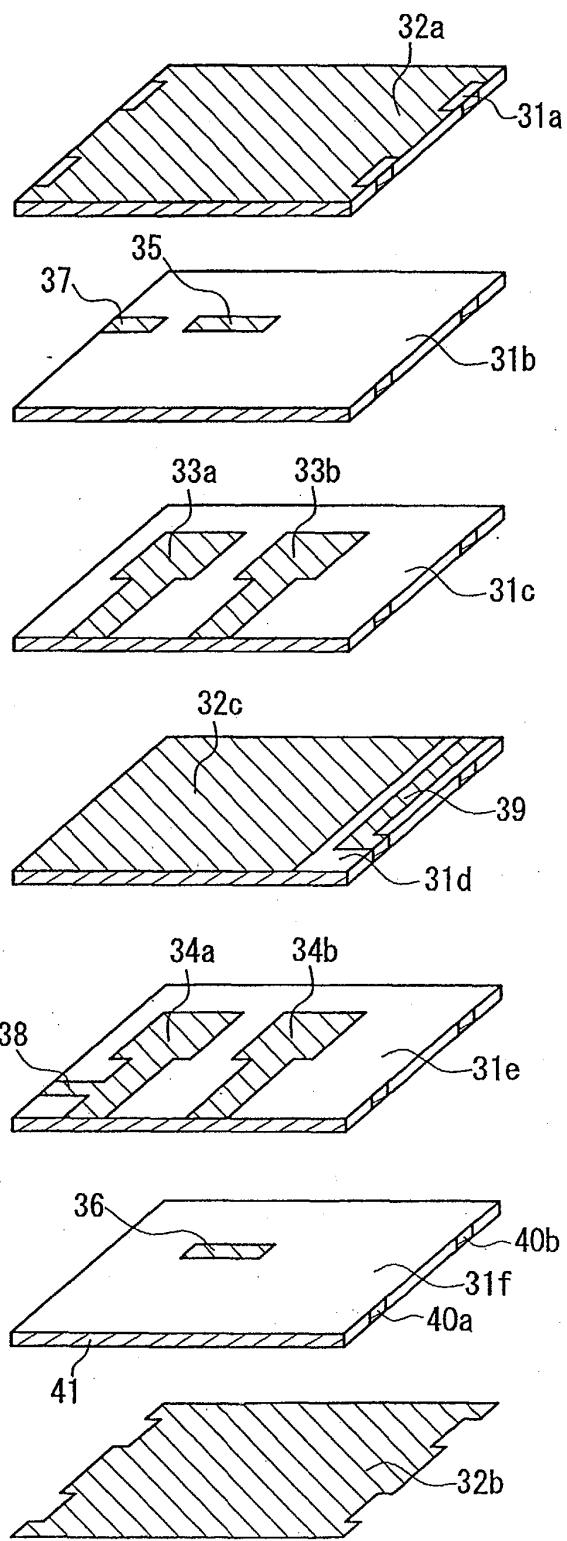


FIG. 9

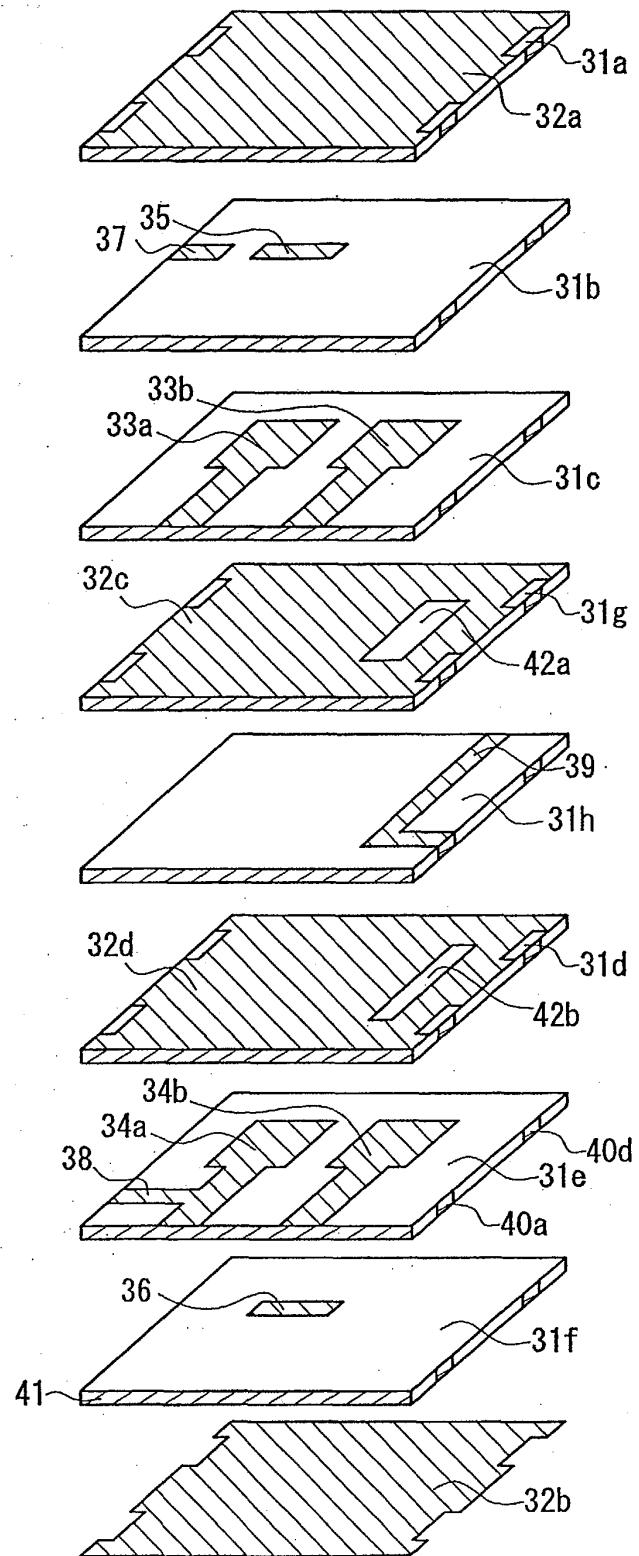


FIG. 10

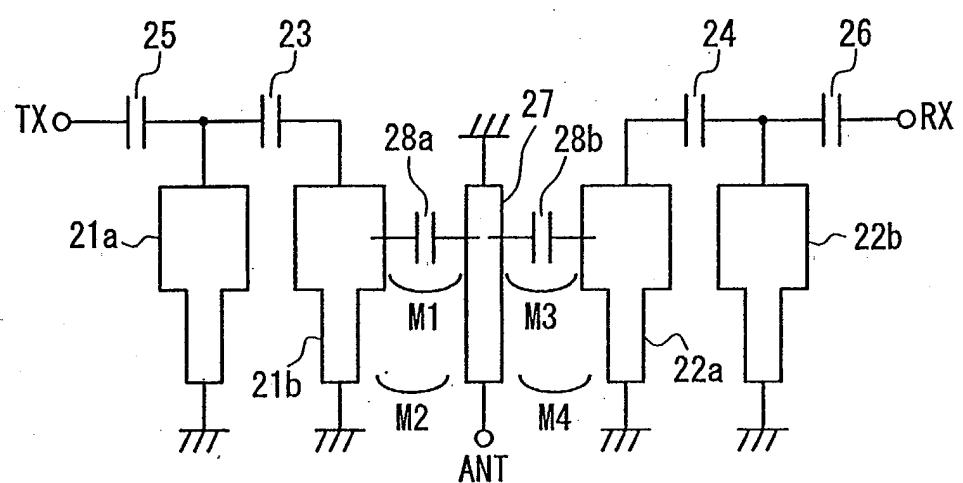


FIG. 11

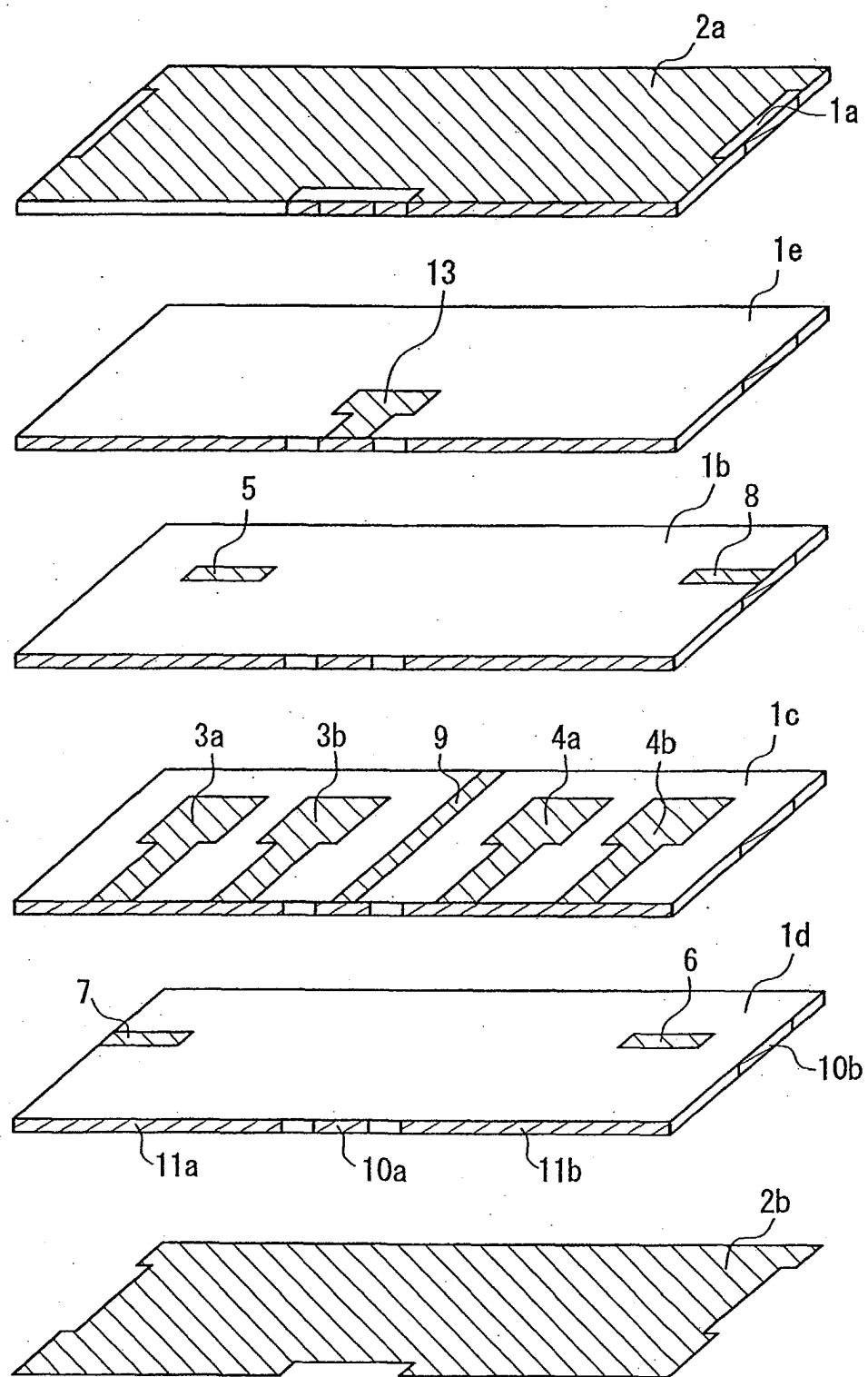


FIG. 12

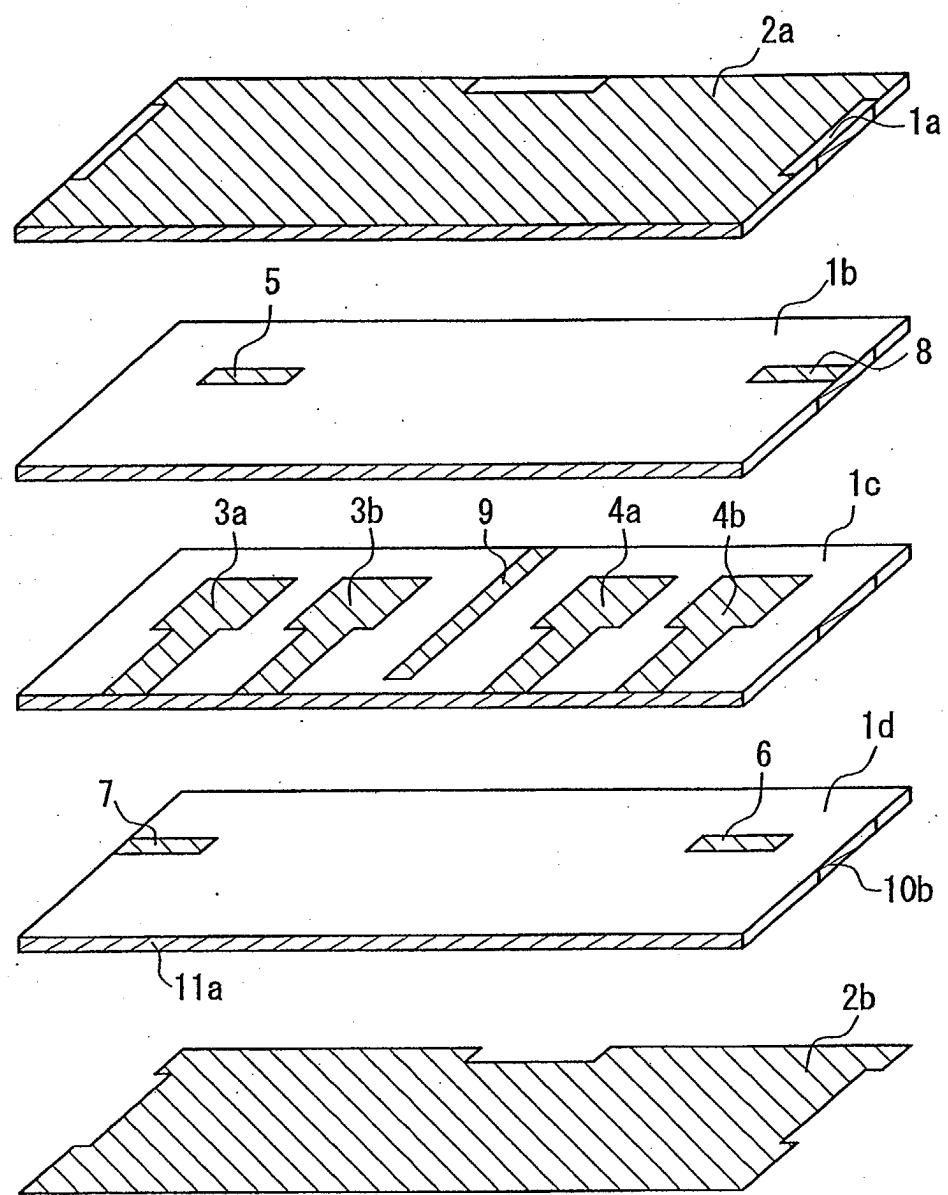


FIG. 13

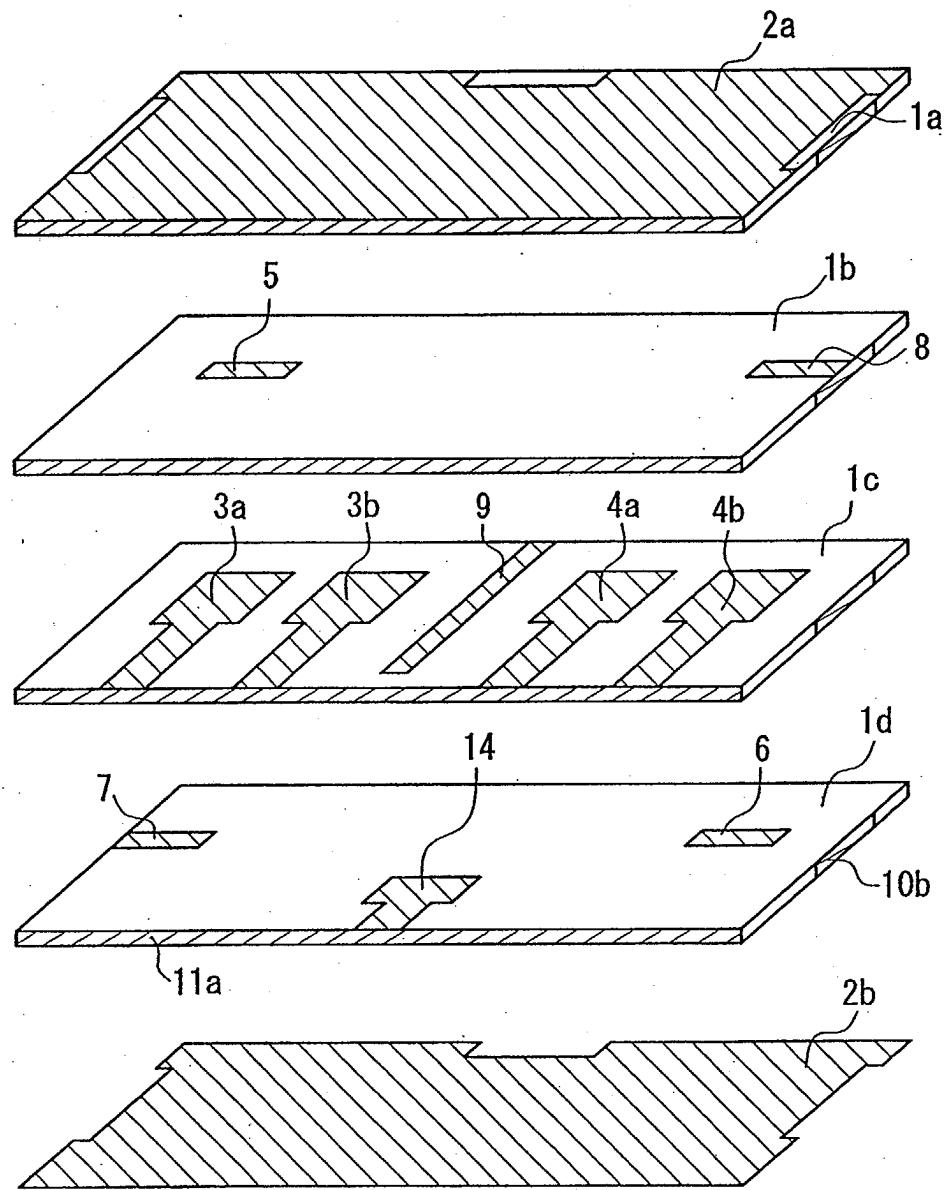


FIG. 14

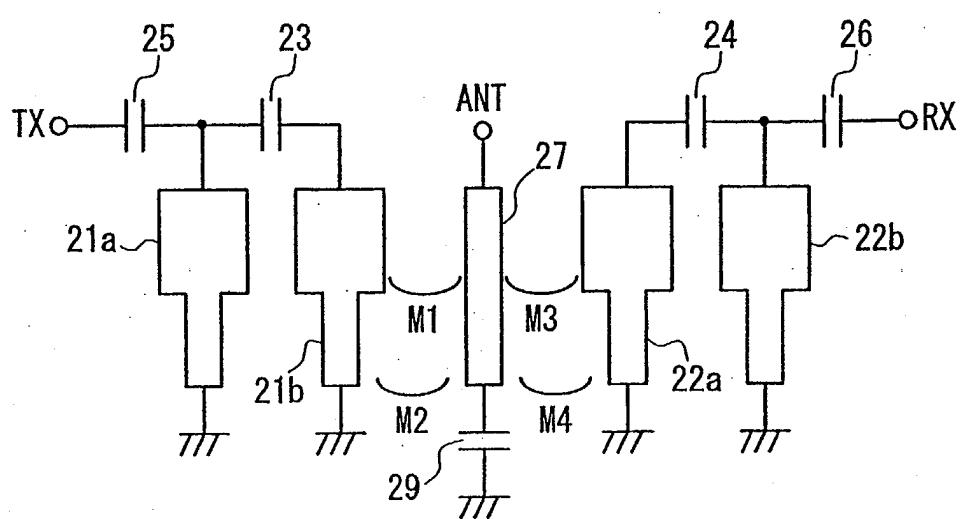


FIG. 15

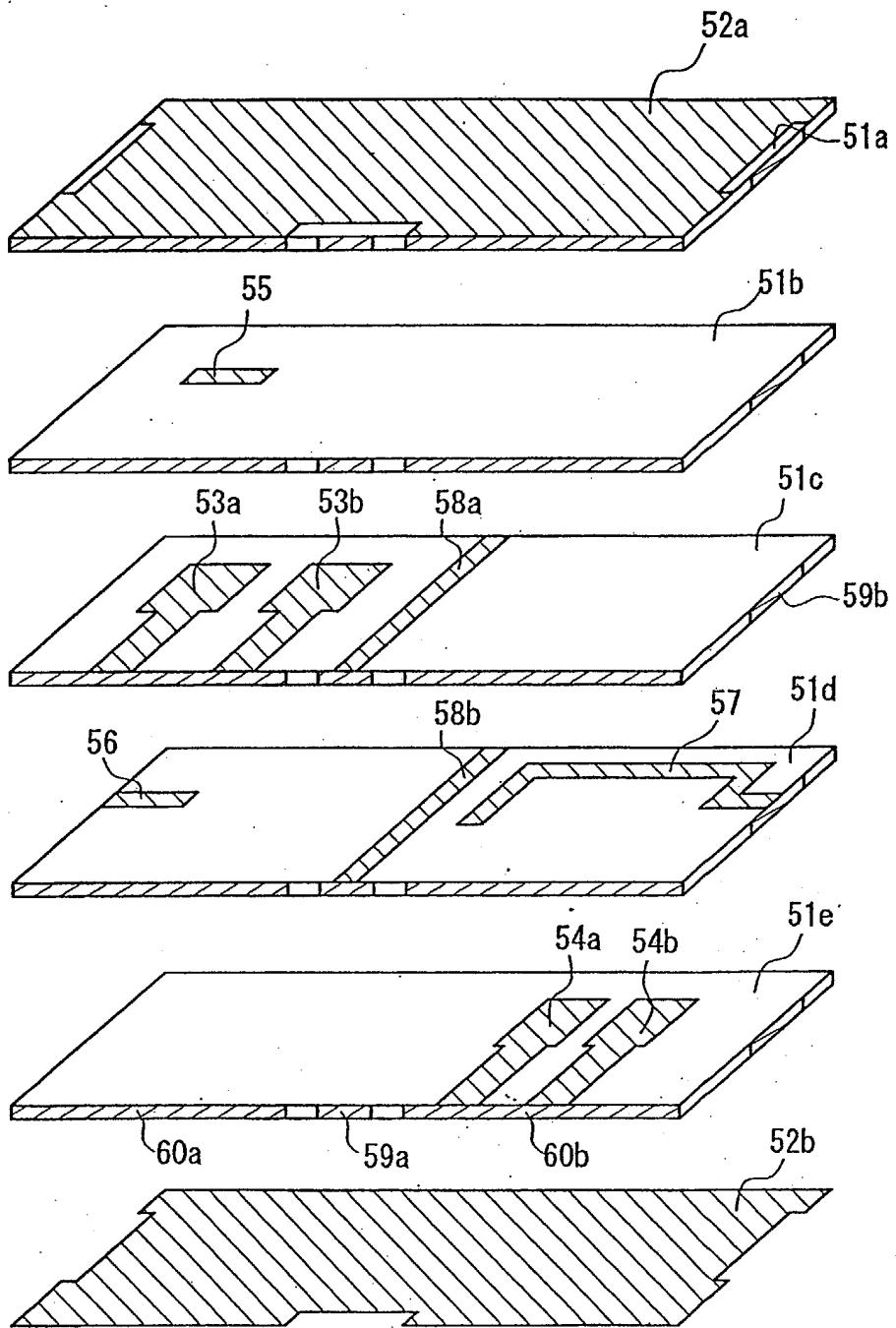


FIG. 16

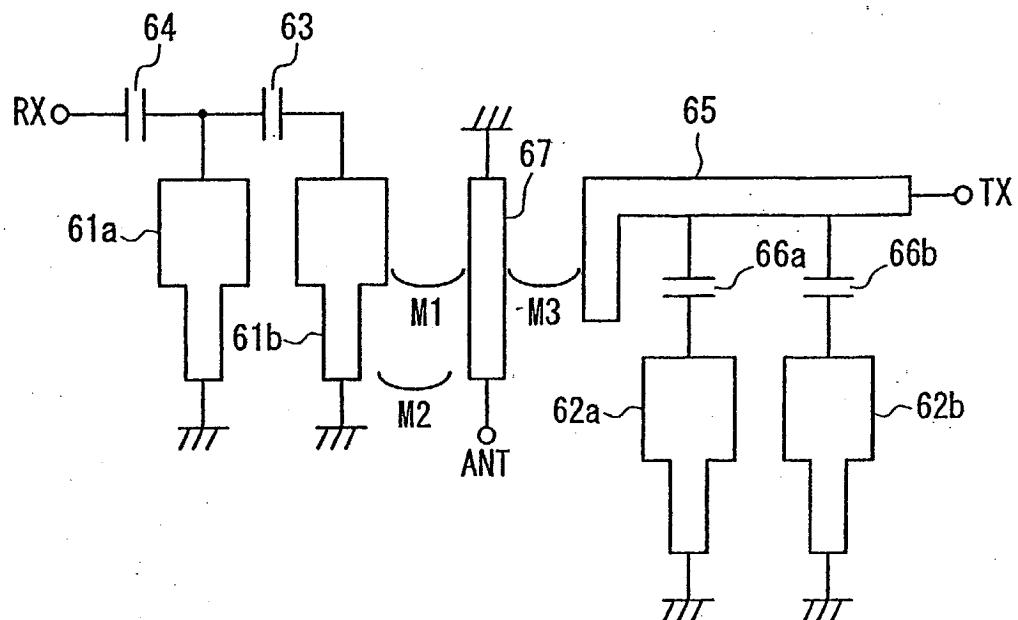


FIG. 17

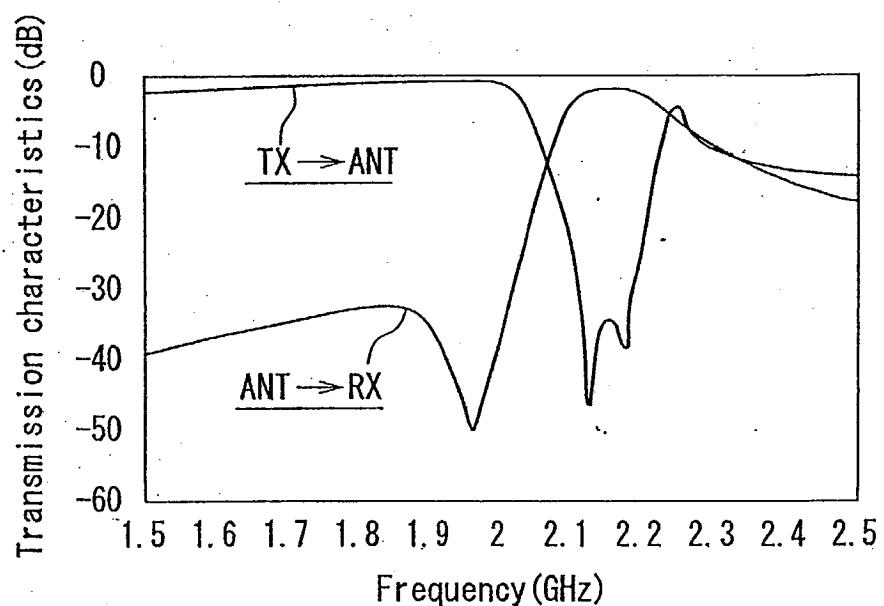


FIG. 18

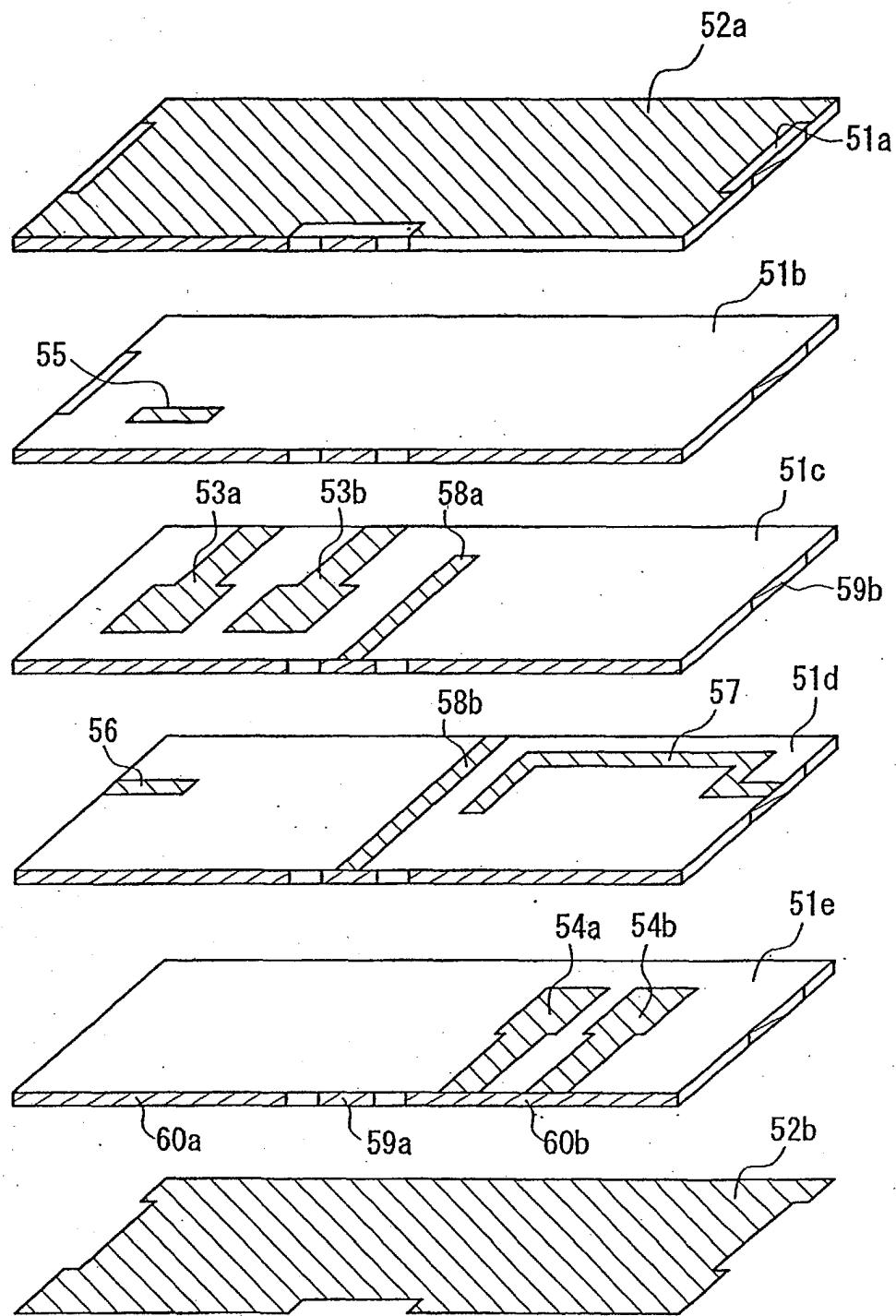


FIG. 19

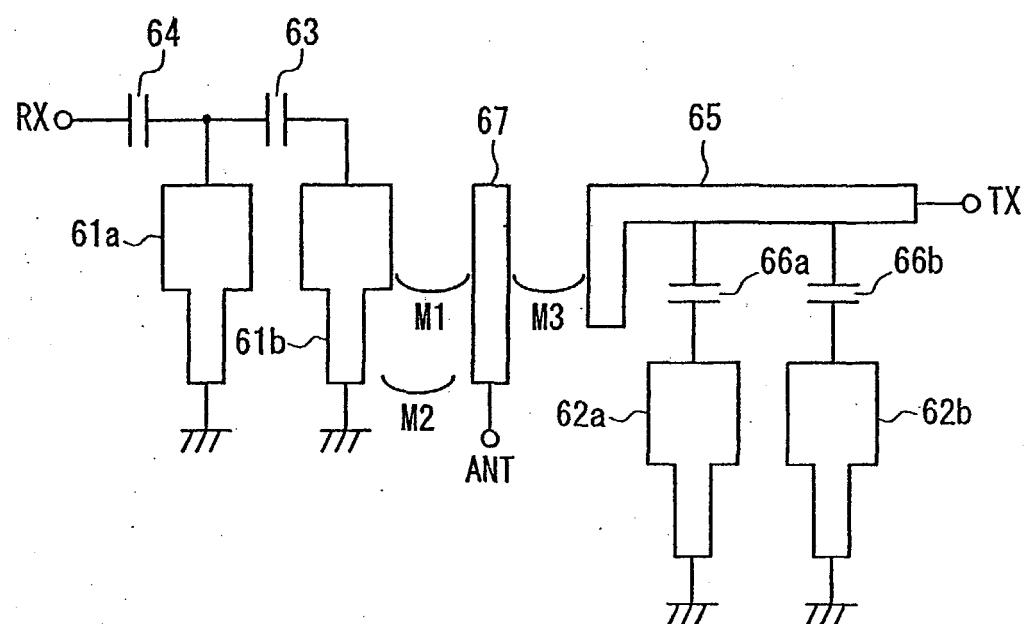


FIG. 20

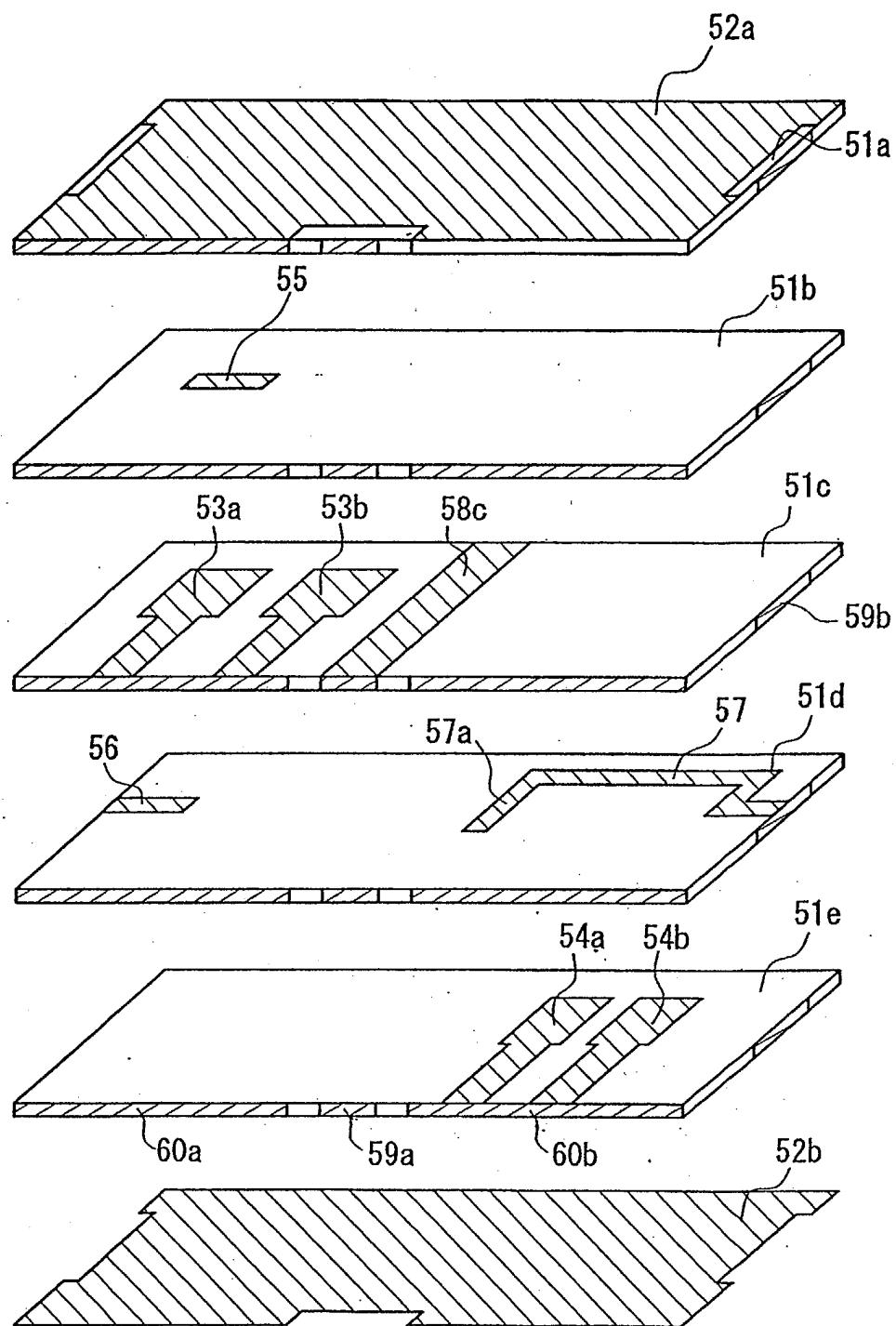


FIG. 21

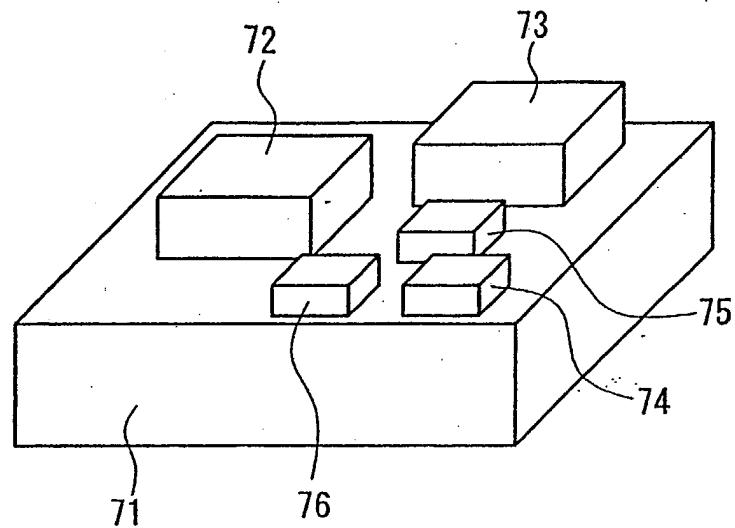


FIG. 22

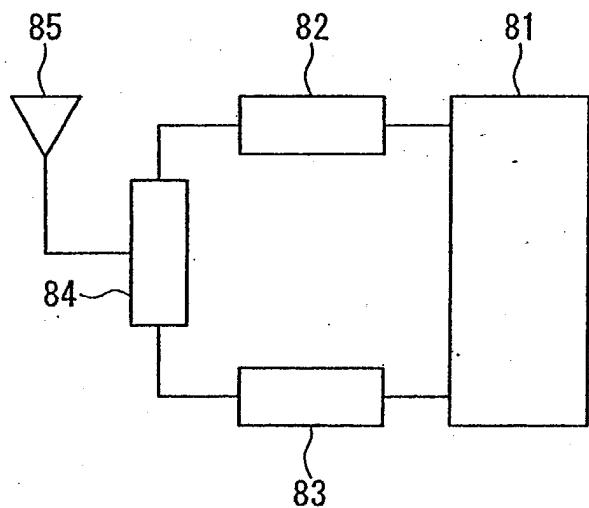


FIG. 23

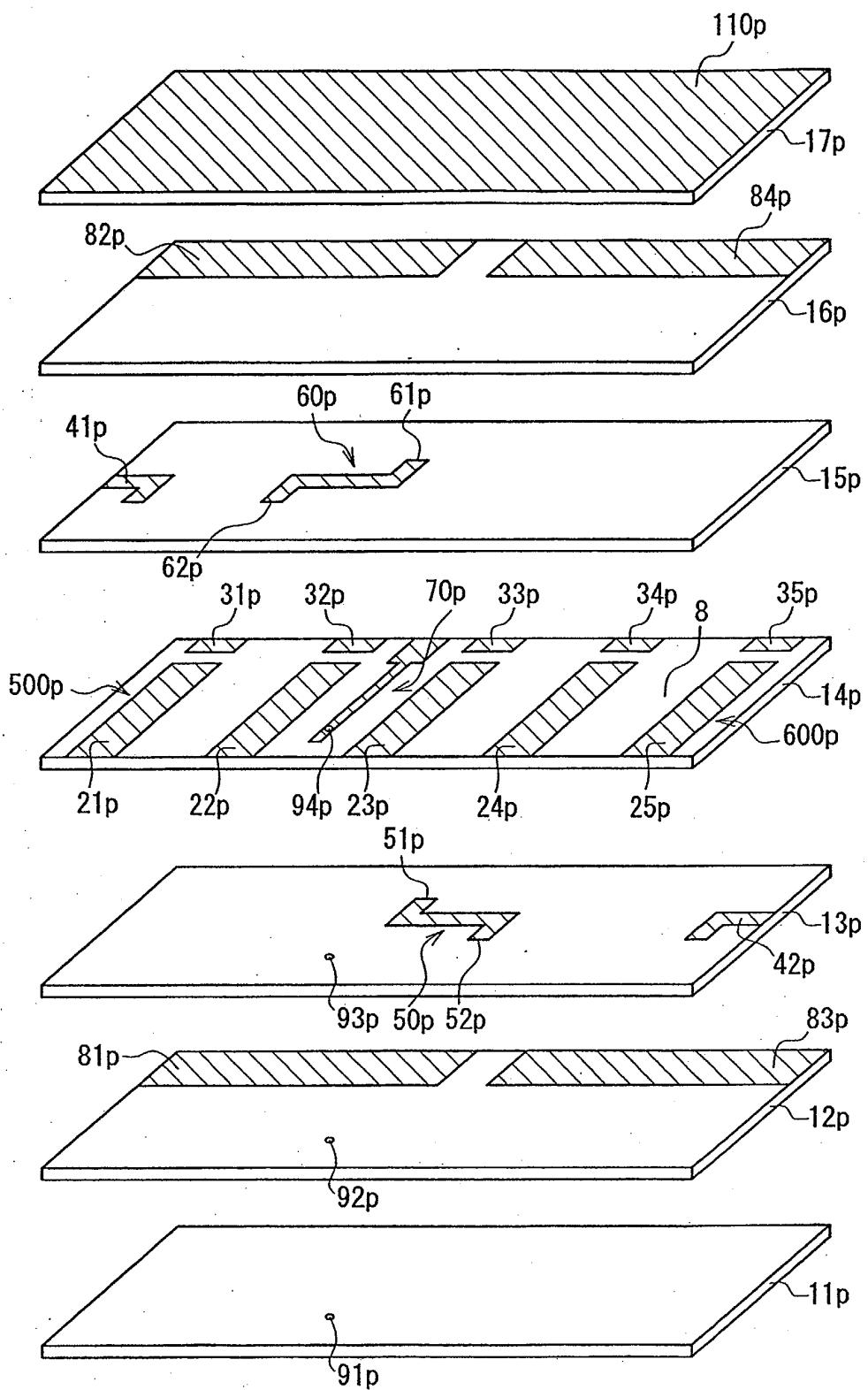


FIG. 24

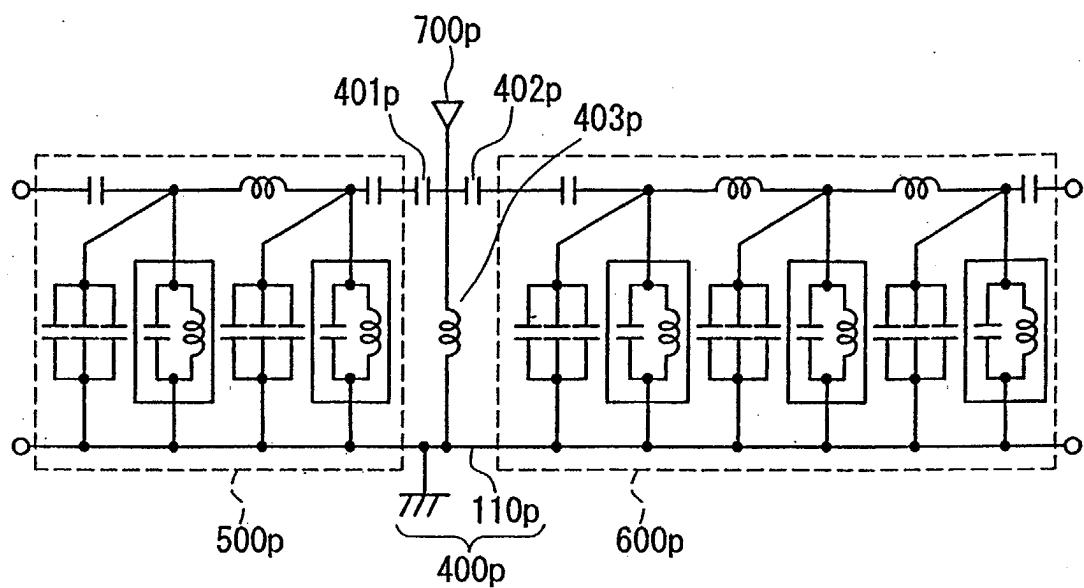


FIG. 25

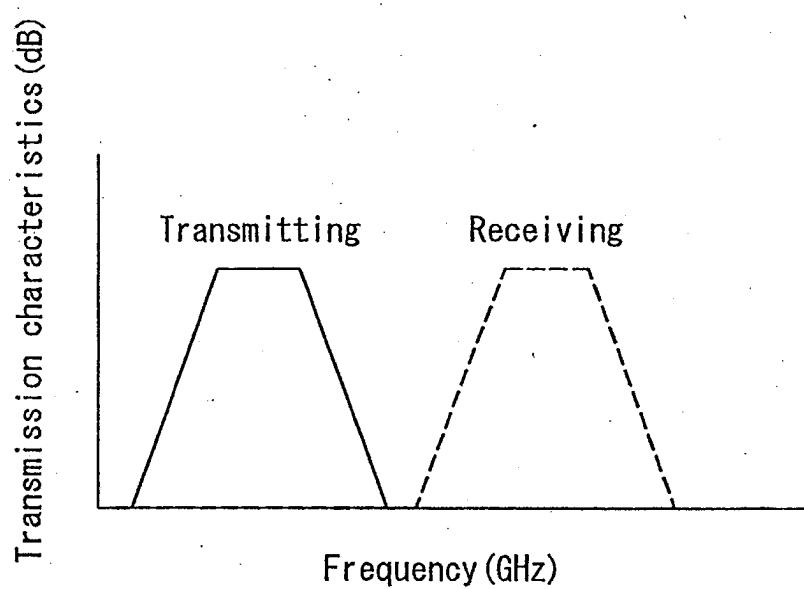


FIG. 26



European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 03 25 6268

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
Y	PATENT ABSTRACTS OF JAPAN vol. 2003, no. 01, 14 January 2003 (2003-01-14) -& JP 2002 271109 A (TAIYO YUDEN CO LTD), 20 September 2002 (2002-09-20) * abstract *	1,4,6, 18,22	H01P1/213
A	---	2	
Y	DE 100 36 977 A (MURATA MANUFACTURING CO) 13 June 2001 (2001-06-13) * column 10, line 21 - column 11, line 17; figure 5 *	1,6,18, 22	
Y	EP 0 840 390 A (MURATA MANUFACTURING CO) 6 May 1998 (1998-05-06) * column 9, line 21-46; figure 11 *	4	
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			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
			H01P
<p>The present search report has been drawn up for all claims</p>			
Place of search	Date of completion of the search		Examiner
THE HAGUE	6 January 2004		Den Otter, A
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			
T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document			

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 03 25 6268

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on. The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

06-01-2004

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