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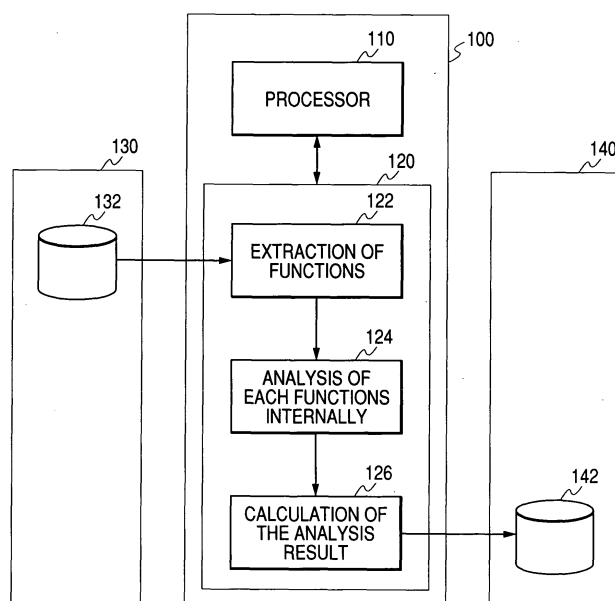
(54) System LSI design support apparatus and a system LSI design support method

(57) Adequately assigning features provided by the system to processing units having different architectures incorporated in a system LSI

An analysis unit is provided for counting the number of conditional branch statements and the number of loop control statements, the number of nestings of the con-

ditional branch statements and the number of nestings of the loop control statements, and the number of functions required to generate the conditions of the conditional branch statements and the number of repetitions of loop control statements described in each function of a program describing system features in a high-level language.

FIG. 1





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 04 00 4561

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
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A	TAE-WOO KIM ET AL: "Hardware cost estimation techniques for C-level description" VLSI AND CAD, 1999. ICVC '99. 6TH INTERNATIONAL CONFERENCE ON SEOUL, SOUTH KOREA 26-27 OCT. 1999, PISCATAWAY, NJ, USA, IEEE, US, 26 October 1999 (1999-10-26), pages 85-88, XP010370108 ISBN: 0-7803-5727-2 * page 85 - page 86 *	1-29	TECHNICAL FIELDS SEARCHED (Int.Cl.7) G06F
A	WO 01/90882 A (RADIOSCAPE LIMITED; FERRIS, GAVIN, ROBERT) 29 November 2001 (2001-11-29) * abstract * * page 2 * * page 4 - page 6 * * figure 1 *	1-29	
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 11 February 2005	Examiner Alonso Nogueiro, L
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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EPO FORM 1503 03.82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 04 00 4561

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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11-02-2005

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