



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:  
**27.10.2004 Bulletin 2004/44**

(51) Int Cl.7: **G09G 3/32**

(21) Application number: **03076206.6**

(22) Date of filing: **25.04.2003**

(84) Designated Contracting States:  
**AT BE BG CH CY CZ DE DK EE ES FI FR GB GR**  
**HU IE IT LI LU MC NL PT RO SE SI SK TR**  
Designated Extension States:  
**AL LT LV MK**

• **Thielemans, Robbie**  
**9810 Nazareth (BE)**

(74) Representative: **Bird, William Edward et al**  
**Bird Goen & Co.,**  
**Klein Dalenstraat 42A**  
**3020 Winksele (BE)**

(71) Applicant: **BARCO N.V.**  
**8500 Kortrijk (BE)**

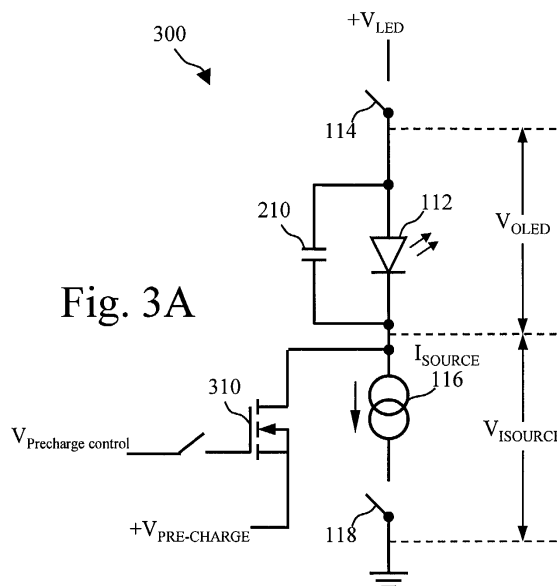
(72) Inventors:  
• **Thanghe, Gino**  
**8650 Merkem (BE)**

Remarks:

Amended claims in accordance with Rule 86 (2) EPC.

(54) **Organic light-emitting diode (Oled) pre-charge circuit for use in a large-screen display**

(57) The present invention is a pre-charge circuit integrated within the drive circuitry of a common anode, passive matrix, large-screen organic light-emitting diode (OLED) display device for overcoming the inherent capacitance characteristics  $C_{OLED}$  of the OLED devices therein. More specifically, a first pre-charge circuit of the present invention includes a MOSFET device integrated within the normal drive circuitry for applying a pre-charge voltage to the cathode of a given OLED device just prior to the desired "on" time, thereby overcoming  $C_{OLED}$  rapidly. A second pre-charge circuit of the present invention integrates within the normal drive circuitry a method of connecting the anode of a given OLED device to a positive voltage while concurrently connecting the cathode to ground just prior to the desired on time, thereby overcoming  $C_{OLED}$  rapidly. A third pre-charge circuit of the present invention includes an additional current source for supplying current over and above the normal operating current, which is activated just prior to the desired on time, thereby overcoming  $C_{OLED}$  rapidly. Finally, in a fourth pre-charge circuit of the present invention, a single current source is used that supplies a high current value just prior to the desired on time. Once the capacitor is charged, the output of this current source rapidly drops to the normal constant operating current.



**Fig. 3A**

## Description

### Technical field of the Invention

**[0001]** The present invention relates to the drive circuitry of a common anode, passive matrix large-screen organic light-emitting diode (OLED) display. More particularly, this invention relates to a pre-charge circuit for optimizing performance.

### Background of the Invention

**[0002]** Organic light-emitting diode (OLED) technology incorporates organic luminescent materials that, when sandwiched between electrodes and subjected to a DC electric current, produce intense light of a variety of colors. These OLED structures can be combined into the picture elements or pixels that comprise a display. OLEDs are also useful in a variety of applications as discrete light-emitting devices or as the active element of light-emitting arrays or displays, such as flat-panel displays in watches, telephones, laptop computers, pagers, cellular phones, calculators, and the like. To date, the use of light-emitting arrays or displays has been largely limited to small-screen applications such as those mentioned above.

**[0003]** Demands for large-screen display applications possessing higher quality and higher resolution has led the industry to turn to alternative display technologies that replace older LED and liquid crystal displays (LCDs). For example, LCDs fail to provide the bright, high light output, larger viewing angles, and high resolution and speed requirements that the large-screen display market demands. By contrast, OLED technology promises bright, vivid colors in high resolution and at wider viewing angles. However, the use of OLED technology in large-screen display applications, such as outdoor or indoor stadium displays, large marketing advertisement displays, and mass-public informational displays, is still in the development stage.

**[0004]** Several technical challenges exist relating to the use of OLED technology in a large-screen application. One such challenge is that OLED displays are expected to offer a wide dynamic range of colors, contrast, and light intensity depending on various external environmental factors including ambient light, humidity, and temperature. For example, outdoor displays are required to produce more white color contrast during the day and more black color contrast at night. Additionally, light output must be greater in bright sunlight and lower during darker, inclement weather conditions. The intensity of the light emission produced by an OLED device is directly proportional to the amount of current driving the device. Therefore, the more light output needed, the more current is fed to the pixel. Accordingly, less light emission is achieved by limiting the current to the OLED device.

**[0005]** A pixel, by definition, is a single point or unit of

programmable color in a graphic image. However, a pixel may include an arrangement of sub-pixels, for example, red, green, and blue sub-pixels. There are two basic circuit configurations for driving these sub-pixels, namely, a common cathode configuration and a common anode configuration. These configurations differ as to whether the three sub-pixels are addressed via a common cathode line or addressed via a common anode line, respectively. Accordingly, in the common cathode configuration, the cathodes of the three sub-pixels are electrically connected and addressed in common; in the common anode configuration, the anodes of the three sub-pixels are electrically connected and addressed in common.

**[0006]** Conventional OLED displays typically use the common cathode configuration. In a typical common cathode drive circuit, a current source is arranged between each individual anode and a positive power supply, while the cathodes are electrically connected in common to ground. Consequently, the current and voltage are not independent of one another, and small voltage variations result in fairly large current variations, having the further consequence of light output variations. Furthermore, in the common cathode configuration, the constant current source is referenced to the positive power supply, so again any small voltage variation will result in a current variation. For these reasons, the common cathode configuration makes precise control of light emission, which is dependent upon precise current control, more difficult.

**[0007]** By contrast, in a typical common anode drive circuit, a current source is arranged between each individual cathode and ground, while the anodes are electrically connected in common to the positive power supply. As a result, the current and voltage are completely independent of one another, and small voltage variations do not result in current variations, thereby eliminating the further consequence of light output variations. Furthermore, in the common anode configuration, the constant current source is referenced to ground, which does not vary, thereby eliminating any current variations due to its reference. For these reasons, the common anode configuration lends itself to the precise control of light emission needed in a large-screen display application.

**[0008]** Another consideration in a large-screen display application using OLED technology is the physical size of the pixel. A larger emission area is more visible and lends itself to achieving the required wide dynamic range of colors, contrast, and light intensity. Consequently, an OLED device structure having a larger area than OLED structures of conventional small-screen displays is desirable. In a small-screen application, the pixel pitch is typically 0.3 mm or less and the pixel area is, for example, only 0.1 mm<sup>2</sup>. By contrast, in a large-screen application, the pixel pitch may be 1.0 mm or greater, thereby allowing the pixel area to be as large as 0.3 to 50 mm<sup>2</sup> (pitch varies up to 10 mm or more with

fill factors of 50%). However, a consequence of the larger device area is the relatively high inherent capacitance ( $C_{\text{OLED}}$ ) of the larger OLED device as compared with small OLED structures. Due to this high inherent capacitance, in operation, an additional amount of charge time is required to reach the OLED device working voltage. This charge time limits the on/off rate of the device and thus adversely affects the overall display brightness and performance.

**[0009]** OLED pre-charge circuits have been developed and integrated into the existing drive circuitry to help overcome the capacitance characteristic of OLEDs within a graphics display device. For example, U.S. Patent No. 6,323,631, entitled, "Constant current driver with auto-clamped pre-charge function," describes a constant current driver with auto-clamped pre-charge function that includes a reference bias generator and a plurality of constant current driver cells, each being connected to the reference bias generator to form a respective current mirror. Each constant current driver cell has a switch transistor, a current output transistor, and a pre-charge transistor. When a constant current is output from the current output transistor for driving an OLED, the pre-charge transistor is turned on to provide a drain to source current as an additional large current for rapidly pre-charging the OLED until the gate to source voltage of the pre-charge transistor is smaller than the threshold voltage. While the pre-charge function of the '631 patent suitably serves to rapidly pre-charge the OLED devices and thereby optimize performance, the pre-charge function of the '631 patent is designed for use in a common cathode drive circuit and is therefore not suitable for use in the common anode drive circuit of a large-screen OLED display device. A further drawback of the pre-charge function of the '631 patent is that it is designed to handle the  $C_{\text{OLED}}$  value associated with a small pixel area, such as  $0.1 \text{ mm}^2$ , and is therefore not able to overcome the larger  $C_{\text{OLED}}$  value associated with a large pixel area.

**[0010]** It is therefore an object of the invention to provide a pre-charge circuit suitable for use in a large-screen OLED display arranged in a common anode configuration.

**[0011]** It is another object of this invention to provide a pre-charge circuit suitable to overcome the large  $C_{\text{OLED}}$  value associated with the large-area OLED device of a large-screen OLED display arranged in a common anode configuration, thereby optimizing performance.

**[0012]** It is yet another object of this invention to provide a pre-charge circuit that eliminates the effects of varying OLED device characteristics, such as capacitance, due to manufacturing process variations.

### Summary of the Invention

**[0013]** The present invention provides a drive circuitry for a common anode, passive matrix, organic light-emitting

diode (OLED) display comprising at least one OLED having an anode and a cathode, the cathode of the OLED being coupled in series to a first current source and a first switching means. The drive circuitry comprises means for pre-charging the at least one OLED before closing the switching means.

**[0014]** The means for pre-charging the at least one OLED may comprise a second switching means. The second switching means may comprise an active switch device, which may comprise a MOSFET. The MOSFET may be an NMOS transistor device having suitable voltage and current ratings for pre-charging the at least one OLED.

**[0015]** The second switching means may be coupled in a branch in parallel over the first current source. If the second switching means comprises a MOSFET, the MOSFET having a gate, the source may be electrically connected to a pre-charge voltage. The MOSFET may also have a drain which is electrically connected to the cathode of the OLED.

**[0016]** The second switching means may comprise a first switch device suitable for coupling the cathode of the OLED to the ground, and a second switch device suitable for coupling the anode of the OLED to a voltage supply substantially corresponding to the normal operating voltage of the OLED. The first switch device and the second switch device may be active switch devices. The active switch devices may be MOSFET transistors having suitable voltage and current ratings for pre-charging the at least one OLED.

**[0017]** The means for pre-charging the at least one OLED may furthermore comprise a second current source coupled in parallel over the first current source. The second current source may be suitable for supplying a current between 50 and 800 mA, preferably between 100 and 600 mA. The second current source may be substantially identical to the first current source, or it may be different, for example the second current source may be suitable for supplying a current between 2 and 4 times the current supplied by the first current source.

**[0018]** The first current source may be a current source device capable of modifying its output current by selecting either one of a first or a second current reference.

**[0019]** The present invention also provides an arrangement comprising an array of OLEDs, each OLED having an anode common with other OLED's of the array and a cathode, and drive circuitry according to the present invention.

**[0020]** The present invention furthermore provides a common anode, passive matrix, organic light-emitting diode (OLED) display comprising an array of OLEDs, each OLED having an anode and a cathode, the display comprising drive circuitry according to the present invention.

**[0021]** The present invention also provides a method for pre-charging an organic light-emitting diode (OLED) of a common anode, passive matrix OLED display prior

to a desired ON-time of the OLED, the method comprising charging the OLED immediately prior to the desired ON-time. The charging may be done by applying a pre-charge voltage to the cathode of the OLED prior to the desired ON-time. Alternatively, it may be done by applying a first voltage level to the anode of the OLED while pulling the cathode of the OLED to a second voltage level, the difference between the first and the second voltage being equal to a desired pre-charge voltage. In this latter case, the first voltage level may be equal to the desired pre-charge voltage, and the second voltage level may be the ground level. According to still another alternative embodiment, the charging may be done by supplying additional current to the OLED prior to the desired ON-time.

[0022] The pre-charge voltage may be substantially equal to a normal operating voltage of the OLED during ON-time. At low light output extra gray scales may be obtained by selectively switching two current sources.

[0023] These and other characteristics, features and advantages of the present invention will become apparent from the following detailed description, taken in conjunction with the accompanying drawings, which illustrate, by way of example, the principles of the invention. This description is given for the sake of example only, without limiting the scope of the invention. The reference figures quoted below refer to the attached drawings.

#### Brief description of the drawings

[0024]

**Figure 1** illustrates a schematic diagram of an OLED array circuit that is representative of a portion of a common anode, passive matrix, large-screen OLED array and associated drive circuit.

**Figure 2A** illustrates a schematic diagram of an OLED drive circuit that is representative of a drive circuit of a single OLED within the OLED array circuit of **Figure 1**.

**Figure 2B** shows a plot of  $V_{\text{SOURCE}}$ , thereby illustrating the operation of the OLED drive circuit of **Figure 2A**.

**Figure 3A** illustrates a schematic diagram of an OLED pre-charge circuit in accordance with a first and preferred embodiment of the present invention.

**Figure 3B** shows a plot of  $V_{\text{SOURCE}}$  vs.  $+V_{\text{PRE-CHARGE}}$ , thereby illustrating the operation of OLED the pre-charge circuits of **Figures 3A, 4, 5, and 6**.

**Figure 4** illustrates a schematic diagram of an OLED pre-charge circuit in accordance with a second embodiment of the present invention.

**Figure 5** illustrates a schematic diagram of an OLED pre-charge circuit in accordance with a third embodiment of the present invention.

**Figure 5A** illustrates the result on voltage and current in function or time, of using two current sources as in Fig. 5

**Figure 6** illustrates a schematic diagram of an OLED pre-charge circuit in accordance with a fourth embodiment of the present invention.

[0025] In the different figures, the same reference figures refer to the same or analogous elements.

#### Description of Illustrative Embodiments

[0026] The present invention will be described with respect to particular embodiments and with reference to certain drawings but the invention is not limited thereto but only by the claims. The drawings described are only schematic and are non-limiting. In the drawings, the size of some of the elements may be exaggerated and not drawn on scale for illustrative purposes.

[0027] Furthermore, the terms first, second, third and the like in the description and in the claims, are used for distinguishing between similar elements and not necessarily for describing a sequential or chronological order. It is to be understood that the terms so used are interchangeable under appropriate circumstances and that the embodiments of the invention described herein are capable of operation in other sequences than described or illustrated herein.

[0028] It is to be noticed that the term "comprising", used in the claims, should not be interpreted as being restricted to the means listed thereafter; it does not exclude other elements or steps. Thus, the scope of the expression "a device comprising means A and B" should not be limited to devices consisting only of components A and B. It means that with respect to the present invention, the only relevant components of the device are A and B.

[0029] The present invention will mainly be described with reference to a single display but the present invention is not limited thereto. For instance, the display may be extendable, e.g. via tiling, to form larger arrays. Hence, the present invention may also include assemblies of pixel arrays, e.g. they may be tiled displays and may comprise modules made up of tiled arrays which are themselves tiled into supermodules. Thus, the word display relates to a set of addressable pixels in an array or in groups of arrays. Several display units or "tiles" may be located adjacent to each other to form a larger display, i.e. multiple display element arrays are physically arranged side-by-side so that they can be viewed as a single image.

[0030] In one aspect of the present invention a pre-charge circuit is provided which may be integrated within

the drive circuitry of a common anode, passive matrix, OLED display device in order to overcome the inherent capacitance characteristic,  $C_{\text{OLED}}$ , of the OLED devices therein. The display may be a large-screen display. More specifically, in an aspect of the present invention a first pre-charge circuit of the present invention applies a pre-charge voltage to the cathode of a given OLED device just prior to the desired "on" time, thereby charging the OLED device rapidly. A second pre-charge circuit of the present invention applies a pre-charge voltage to the anode of a given OLED device while concurrently pulling the cathode to ground just prior to the desired on time, thereby charging the OLED device rapidly. A third pre-charge circuit of the present invention simply supplies additional current to the OLED device just prior to the desired on time, thereby charging the OLED device rapidly. Finally, a fourth pre-charge circuit of the present invention comprises a single current source device that is capable of modifying its output current rapidly by selecting either a low or high current reference, thus being able to rapidly charge the OLED device.

**[0031]** Figure 1 illustrates a schematic diagram of an OLED array circuit 100, which is representative of a portion of a typical common anode, passive matrix, large-screen OLED array and associated drive circuit. OLED array circuit 100 includes an OLED array 110 formed of a plurality of OLEDs 112 (each having an anode and cathode, as is well known) arranged in a matrix of rows and columns. For example, OLED array 110 is formed of OLEDs 112a, 112b, 112c, 112d, 112e, 112f, 112g, 112h, and 112j arranged in a 3x3 array, where the anodes of OLEDs 112a, 112b, and 112c are electrically connected to a ROW LINE 1, the anodes of OLEDs 112d, 112e, and 112f are electrically connected to a ROW LINE 2, and the anodes of OLEDs 112g, 112h, and 112j are electrically connected to a ROW LINE 3. Furthermore, the cathodes of OLEDs 112a, 112d, and 112g are electrically connected to a COLUMN LINE A, the cathodes of OLEDs 112b, 112e, and 112h are electrically connected to a COLUMN LINE B, and the cathodes of OLEDs 112c, 112f, and 112j are electrically connected to a COLUMN LINE C. Each OLED 112 represents a pixel in monochrome displays or a sub-pixel in a color display (typically red, green, or blue, however, any color variants are acceptable. Sub-pixels are geometrically grouped together to form single addressable full color pixels, for instance 112a-c may be respectively red, green and blue). An OLED emits light when forward biased in conjunction with an adequate current supply, as is well known.

**[0032]** A positive voltage ( $+V_{\text{LED}}$ ), typically ranging between 5 volts (i.e., normal working voltage across the OLED + voltage over current source, usually 0.7 V) and 15-20 volts, is electrically connected to each respective row line ROW LINE 1, ROW LINE 2, ROW LINE 3 via a plurality of switches 114a, 114b, 114c. Switches 114a, 114b, 114c are conventional active switch devices, such as MOSFET switches or transistors having suitable volt-

age and current ratings. More specifically, a positive voltage  $+V_{\text{LED}}$  is electrically connected to ROW LINE 1 via switch 114a, to ROW LINE 2 via switch 114b, and to ROW LINE 3 via switch 114c. Column lines COLUMN LINE A, COLUMN LINE B, and COLUMN LINE C are driven by separate constant current sources, i.e., a plurality of current sources ( $I_{\text{SOURCE}}$ ) 116a, 116b, 116c. More specifically, current source  $I_{\text{SOURCE}}$  116a drives COLUMN LINE A, current source  $I_{\text{SOURCE}}$  116b drives COLUMN LINE B, and current source  $I_{\text{SOURCE}}$  116c drives COLUMN LINE C. Connected in series between current source  $I_{\text{SOURCE}}$  116a and ground is a switch 118a. Connected in series between current source  $I_{\text{SOURCE}}$  116b and ground is a switch 118b. Connected in series between current source  $I_{\text{SOURCE}}$  116c and ground is a switch 118c. Current sources  $I_{\text{SOURCE}}$  116a, 116b, 116c are conventional current sources capable of supplying a constant current typically in the range of 5 to 50 mA. Examples of constant current devices include a Toshiba TB62705 (8-bit constant current LED driver with shift register and latch functions) and a Silicon Touch ST2226A (PWM-controlled constant current driver for LED displays). Switches 118a, 118b, 118c are normally included in the current source integrated circuit and consist of a conventional active switch device, such as MOSFET switches or transistors having suitable voltage and current ratings.

**[0033]** The matrix of OLEDs 112a-112j within OLED array circuit 100 are arranged in the common anode configuration. For each colour pixel for instance on ROW LINE 2, the anodes of each sub-pixel 112d-112f are all connected to the same row line. In this way the current source is referenced to the ground and the current and voltage are independent of one another providing better control of the light emission.

**[0034]** In operation, to activate (light up) any given OLED 112a-112j, its associated row line ROW LINE 1, ROW LINE 2, ROW LINE 3 and column line COLUMN LINE A, COLUMN LINE B, COLUMN LINE C are activated by simultaneously closing their associated switches 114a, 114b, 114c and 118a, 118b, 118c. In a first example, to light up OLED 112b, a positive voltage  $+V_{\text{LED}}$  is applied to ROW LINE 1 by closing switch 114a and, simultaneously, a constant current is supplied to COLUMN LINE B via current source  $I_{\text{SOURCE}}$  116b by closing switch 118b. In this way, OLED 112b is forward biased and current flows through OLED 112b. Once a device threshold voltage (of typically 1.5-2 volts) across the OLED 112b is reached, the OLED 112b starts emitting light. OLED 112b remains lit up as long as switch 114a and switch 118b remain closed. To deactivate OLED 112b, switch 118b is opened. In a second example, to light up OLED 112g, a positive voltage  $+V_{\text{LED}}$  is applied to ROW LINE 3 by closing switch 114c and, simultaneously, a constant current is supplied to COLUMN LINE A via current source  $I_{\text{SOURCE}}$  116a by closing switch 118a. In this way, OLED 112g is forward biased and current flows through OLED 112g. Once the device

threshold voltage (of typically 1.5-2 volts) across the OLED 112g is reached, OLED 112g starts emitting light. OLED 112g remains lit up as long as switch 114c and switch 118a remain closed. To deactivate OLED 112g, switch 118a is opened.

[0035] Along a given row line ROW LINE 1, ROW LINE 2, ROW LINE 3, any one or more OLEDs 112a-112j may be activated at any given time. By contrast, along a given column line COLUMN LINE A, COLUMN LINE B, COLUMN LINE C, only one OLED 112 may be activated at any given time. Thus, a complete image is built from sequentially or randomly selecting each row of OLED array 110, by closing its corresponding switch 114a-114c. In each row a current with a certain intensity and a certain duration is sent through the diodes 112a-112c, 112d-112f, 112g-112j on that row by current sources 116a, 116b, 116c by closing and opening switches 118a, 118b, 118c, such as to display the correct intensity in each pixel or sub-pixel. A switch 114a, 114b, 114c remains closed as long as its row is selected and opens when the next row is selected. All switches 118a, 118b, 118c open before the next row is selected. Further details of the operation of any given OLED 112a-112j is found in reference to Figures 2A and 2B below.

[0036] Figure 2A illustrates a schematic diagram of an OLED drive circuit 200, which is representative of a typical drive circuit of a single OLED 112 within OLED array circuit 100 of Figure 1. OLED drive circuit 200 includes switch 114, OLED 112, current source  $I_{SOURCE}$  116, and switch 118 all arranged in series between positive voltage  $+V_{LED}$  and ground as shown in Figure 2A. OLED drive circuit 200 further includes a capacitor 210 arranged in parallel with OLED 112. Capacitor 210 is representative of the device capacitance ( $C_{OLED}$ ) of OLED 112. Given the area of the structure of OLED 112, a typical value of  $C_{OLED}$  can be more than 500 pF, which is relatively high compared with a usual  $C_{OLED}$  value of 5 pF for a small OLED structure used in a small-screen OLED display application. The value of  $C_{OLED}$  along with any additional line capacitance of the physical package, which for the purpose of this description are assumed to be negligible, must be overcome in order to achieve satisfactory display performance. A voltage  $V_{OLED}$  represents the voltage potential across OLED 112 and a voltage  $V_{ISOURCE}$  represents the voltage potential across the series-connected current source  $I_{SOURCE}$  116 and switch 118.

[0037] Figure 2B shows a plot 250 of the voltage potential  $V_{ISOURCE}$  across the series-connected current source 116 and switch 118, from a time  $t_0$ , when switches 114 and 118 are closed, to a time  $t_2$ , when switch 118 is opened, thereby illustrating the operation of OLED drive circuit 200. At time  $t_0$ , the value of  $V_{ISOURCE}$  is equal to positive voltage  $+V_{LED}$  and begins to fall slowly towards the working voltage ( $V_{WORKING}$ ) of OLED 112 due to the relatively high capacitance value  $C_{OLED}$  of the OLED 112. The OLED starts lighting up a little bit as soon as the threshold level or threshold voltage is

reached (the threshold voltage of an OLED is the voltage across the OLED just enough to let it light up; the normal operating voltage or working voltage across the OLED is higher than this threshold voltage).  $V_{ISOURCE}$  reaches the working voltage of OLED 112 at a time  $t_1$ . The period between  $t_0$  and  $t_1$  represents the charge time  $T_{CHARGE}$  of capacitor 210 of OLED 112. The voltage transition from  $t_0$  to  $t_1$  is linear because the current output of current source  $I_{SOURCE}$  116 is constant. At time  $t_1$ , OLED 112 begins to emit its full light and continues to emit light for a predetermined period of time which is the OLED emission time  $T_{ON}$  as long as switches 114 and 118 remain closed. OLED 112 is deactivated by opening switch 118, and subsequently  $V_{ISOURCE}$  returns sharply to the  $+V_{LED}$  value. OLED 112 remains off for a period from  $t_2$  to the next  $t_0$ , i.e., OLED off time or period  $T_{OFF}$ . Therefore, a cycle time  $T_{CYCLE}$  is represented by  $T_{CHARGE} + T_{ON} + T_{OFF}$ . As shown in plot 250,  $T_{CHARGE}$  represents time wasted when switches 114 and 118 are closed and capacitor 210 is charging but OLED 112 is not yet emitting light at the desired emission level. This results in an extended  $T_{CYCLE}$ , thereby decreasing the achievable  $T_{ON}/T_{OFF}$  rate and limiting the achievable performance of OLED drive circuit 200.

Figures 3A, 3B, 4, 5, and 6 that follow illustrate ways to minimize or eliminate the  $T_{CHARGE}$  time by performing a pre-charge operation on capacitor 210, thereby minimizing  $T_{CYCLE}$ .

[0038] Figure 3A illustrates a schematic diagram of an OLED pre-charge circuit 300 in accordance with a first and preferred embodiment of the invention. OLED pre-charge circuit 300 is identical to OLED drive circuit 200 of Figure 2A except for the addition of a MOSFET 310 arranged in parallel with current source  $I_{SOURCE}$  116. More specifically, the drain of MOSFET 310 is electrically connected directly to the cathode of OLED 112, the source of MOSFET 310 is connected to a pre-charge voltage  $+V_{PRE-CHARGE}$ , and the gate of MOSFET 310 is electrically connected to a precharge control voltage  $V_{PRECHARGE-CONTROL}$ . MOSFET 310 may be any conventional NMOS transistor device having suitable voltage and current ratings for this application. However, MOSFET 310 is representative of any suitable active switch device.

[0039] Figure 3B shows a plot 350 of  $V_{ISOURCE}$  vs.  $+V_{PRE-CHARGE}$  from a time  $t_0$ , when the pre-charge operation begins, to a time  $t_2$ , when switch 118 is opened, thereby illustrating the operation of OLED pre-charge circuit 300. (It is to be noted that the plot of  $V_{ISOURCE}$  vs.  $+V_{PRE-CHARGE}$  is not drawn to scale in relation to one another along the voltage axis. Plot 350 is intended to illustrate only general voltage transitions and timing.) At time  $t_0$ , MOSFET 310 is switched on by applying a voltage  $V_{PRECHARGE-CONTROL}$  to its gate, which voltage  $V_{PRECHARGE-CONTROL}$  is positive enough referred to the source of MOSFET 310 (voltage =  $+V_{PRECHARGE}$ ) to saturate MOSFET 310. MOSFET 310 connects a source that is able to sink typically 100 to

600 ma of current. In addition, at time  $t_0$  switch **114** is closed, while switch **118** remains open. As a result, current begins to flow through OLED **112** for a short period of time via the electrical path created by MOSFET **310**. This time period must be long enough to build up a voltage across capacitor **210** of OLED **112** that approaches the working voltage of OLED **112**. Once this voltage has built up across capacitor **210** of OLED **112**, MOSFET **310** is switched off, i.e., the  $+V_{\text{PRE-CHARGE}}$  voltage at the cathode of the OLED is removed, and switch **118** is simultaneously closed, thereby allowing the normal operating current from current source  $I_{\text{SOURCE}}$  **116** to flow through OLED **112**, causing light to be emitted.

**[0040]** Plot **350** of **Figure 3B** shows that the value of  $V_{\text{ISOURCE}}$  is equal to  $+V_{\text{LED}}$  at  $t_0$  when  $V_{\text{PRECHARGE-CONTROL}}$  is applied (MOSFET **310** on), switch **114** is closed, and switch **118** is open. Subsequently,  $V_{\text{ISOURCE}}$  falls sharply toward the working voltage of OLED **112** due to  $+V_{\text{PRE-CHARGE}}$  rapidly charging capacitor **210**. At  $t_1$ ,  $V_{\text{PRECHARGE-CONTROL}}$  is removed and switch **118** is closed. The period between  $t_0$  and  $t_1$  represents the charge time  $T_{\text{CHARGE}}$  of capacitor **210** of OLED **112**. At time  $t_1$ , OLED **112** begins normal light emission and continues to emit light for a predetermined period of time  $T_{\text{ON}}$  as long as switches **114** and **118** remain closed. Thus, switch **114** is closed for a period of time equal to at least charge time  $T_{\text{CHARGE}}$  + OLED emission time  $T_{\text{ON}}$ , while switch **118** is closed for a period of time equal to the OLED emission time  $T_{\text{ON}}$ . OLED **112** is deactivated by opening switch **118**, and subsequently  $V_{\text{ISOURCE}}$  returns sharply to the  $+V_{\text{LED}}$  value. OLED **112** remains off for a period from  $t_2$  to the next  $t_0$ , i.e., OLED off time  $T_{\text{OFF}}$ . Therefore, a cycle time  $T_{\text{CYCLE}}$  is represented by  $T_{\text{CHARGE}} + T_{\text{ON}} + T_{\text{OFF}}$ .

**[0041]** With reference to plot **250** of **Figure 2A** and plot **350** of **Figure 3A**, it is illustrated that charge time  $T_{\text{CHARGE}}$  of OLED pre-charge circuit **300**, which is typically in the range of 12 ns to 50 ns, is significantly minimized compared with the charge time  $T_{\text{CHARGE}}$  of OLED drive circuit **200**, which is typically in the range of 25 ns to 65 s. As a result,  $T_{\text{CYCLE}}$  of OLED pre-charge circuit **300** is allowed to be significantly shorter than  $T_{\text{CYCLE}}$  of OLED drive circuit **200** while achieving equivalent  $T_{\text{ON}}$  time. Consequently, the achievable  $T_{\text{ON}}/T_{\text{OFF}}$  rate of OLED **112** within OLED pre-charge circuit **300** is increased compared with the achievable on/off rate of OLED **112** within OLED drive circuit **200**, thereby enhancing overall performance.

**[0042]** Is it important to balance the operation of OLED pre-charge circuit **300** such that charge time  $T_{\text{CHARGE}}$  is minimized while also ensuring that pre-charging stops when the working voltage of OLED **112** has been reached. Consequently, if the timing of the pre-charging is too long, it will result in an excessive current through OLED **112**, which results in excessive light emission. As a precaution, it may be desirable to end

the pre-charge operation just short of  $V_{\text{ISOURCE}}$  reaching the normal operating voltage of 0.7 volts. For example,  $V_{\text{PRECHARGE CONTROL}}$  can be removed when  $V_{\text{ISOURCE}}$  reaches 1.5 volts. This would result in a slightly longer charge time  $T_{\text{CHARGE}}$ , as the voltage transition from 1.5 to 0.7 volts is due to the current supplied by current source  $I_{\text{SOURCE}}$  **116** only, without the help of  $+V_{\text{PRE-CHARGE}}$ .

**[0043]** Furthermore, when the OLED emission time  $T_{\text{ON}} = 0$ , the pre-charge operation must not occur, so that OLED **112** is not lighted up when it is unwanted. In this case, both switch **118** and MOSFET **310** remain open. If the pre-charge operation was allowed when OLED emission time  $T_{\text{ON}} = 0$ , OLED **112** could possibly begin lighting up because  $+V_{\text{PRE-CHARGE}}$  reaches a level just high enough to slightly light up OLED **112**. Thus, unwanted lighting of OLED **112** should be avoided by eliminating the pre-charge operation if OLED emission time  $T_{\text{ON}} = 0$ .

In summary, and in reference to **Figures 3A** and **3B**, just prior to the desired  $T_{\text{ON}}$  time, a pre-charge voltage ( $+V_{\text{PRE-CHARGE}}$ ) from a source that is able to sink a suitable amount of current is applied to the OLED cathode via MOSFET **310**. Thus, capacitor **210** is charged rapidly, not via the normal current source ( $I_{\text{SOURCE}}$  **116**), but instead via a high current through MOSFET **310**.

**[0044]** **Figure 4** illustrates a schematic diagram of an OLED pre-charge circuit **400** in accordance with a second embodiment of the invention. OLED pre-charge circuit **400** is identical to OLED drive circuit **200** of **Figure 2** except that a voltage  $+V_{\text{OLED}}$  may be electrically connected to the anode of OLED **112** via a switch **410**, and the cathode of OLED **112** may be electrically connected to ground via a switch **412**. Switches **410** and **412** are conventional active switch devices, such as MOSFET switches or transistors having suitable voltage and current ratings.

**[0045]** In operation, just prior to the desired OLED emission time  $T_{\text{ON}}$  (see **Figure 3B**) the anode of OLED **112** is forced to the normal operating voltage across the OLED **112** ( $+V_{\text{OLED}}$ ) for a short time by closing switch **410**, while concurrently shorting the cathode of OLED **112** to ground by closing switch **412**. In this way, a charge is built up across capacitor **210** rapidly. After a predetermined amount of time (i.e., charge time  $T_{\text{CHARGE}}$  of **Figure 3B**), switches **114** and **118** are closed and switch **412** is opened, thereby applying  $+V_{\text{LED}}$  to the anode of OLED **112** and supplying the normal operating current via current source  $I_{\text{SOURCE}}$  **116**. As a result, OLED **112** begins its normal operation (i.e.,  $T_{\text{ON}}$  of **Figure 3B**).

**[0046]** Similar to OLED pre-charge circuit **300** of **Figure 3A**,  $T_{\text{CHARGE}}$  of OLED pre-charge circuit **400**, which is typically in the range of 12 ns to 50 ns, is significantly minimized compared with charge time  $T_{\text{CHARGE}}$  of OLED drive circuit **200**, which is typically in the range of 25 ns to 65 s. As a result,  $T_{\text{CYCLE}}$  of OLED pre-charge circuit **400** is allowed to be significantly

shorter than  $T_{\text{CYCLE}}$  of OLED drive circuit **200** while achieving equivalent OLED emission time  $T_{\text{ON}}$ . Consequently, the achievable  $T_{\text{ON}}/T_{\text{OFF}}$  rate of OLED **112** within OLED pre-charge circuit **400** is increased compared with the achievable on/off rate of OLED **112** within OLED drive circuit **200**, thereby enhancing overall performance.

**[0047]** In summary, and in reference to **Figure 4**, just prior to the desired OLED emission time  $T_{\text{ON}}$ , a pre-charge voltage ( $+V_{\text{OLED}}$ ) is applied to the anode of OLED **112** while, concurrently, the cathode of OLED **112** is pulled to ground; thus, capacitor **210** is charged rapidly, not via the normal current source ( $I_{\text{SOURCE 116}}$ ), but instead via  $+V_{\text{OLED}}$  and the straight connection of the cathode to the ground.

**[0048]** **Figure 5** illustrates a schematic diagram of an OLED pre-charge circuit **500** in accordance with a third embodiment of the invention. OLED pre-charge circuit **500** is identical to OLED drive circuit **200** of **Figure 2**, except that an additional current source (i.e., a current source  $I_{\text{SOURCE 510}}$  with an associated series-connected switch **512**) is connected in parallel with current source  $I_{\text{SOURCE 116}}$ , as shown in **Figure 5**. Current source  $I_{\text{SOURCE 510}}$  is a conventional current source capable of supplying a constant current typically in the range of 100 to 600 mA. Switch **512** is a conventional active switch device, such as a MOSFET switch or transistor having suitable voltage and current ratings.

**[0049]** In operation, just prior to the desired  $T_{\text{ON}}$  time (see **Figure 3B**) switches **114**, **118** and **512** are all closed, thus additional current is available via current source  $I_{\text{SOURCE 510}}$  along with the normal current supplied via current source  $I_{\text{SOURCE 116}}$ . As a result of the additional current availability, the charge time (i.e.,  $T_{\text{CHARGE}}$  of **Figure 3B**) of capacitor **210** is reduced. In this way, a charge is built up across capacitor **210** rapidly. After a predetermined amount of time (i.e.,  $T_{\text{CHARGE}}$  of **Figure 3B**), switch **512** is opened, thereby allowing only the normal operating current via current source  $I_{\text{SOURCE 116}}$ . As a result, OLED **112** begins its normal operation (i.e.,  $T_{\text{ON}}$  of **Figure 3B**).

**[0050]** Similar to OLED pre-charge circuit **300** of **Figure 3A** and OLED pre-charge circuit **400** of **Figure 4**, charge time  $T_{\text{CHARGE}}$  of OLED pre-charge circuit **500**, which is typically in the range of 12 ns to 50 ns, is significantly minimized compared with charge time  $T_{\text{CHARGE}}$  of OLED drive circuit **200**, which is typically in the range of 25 ns to 65 s. As a result,  $T_{\text{CYCLE}}$  of OLED pre-charge circuit **400** is allowed to be significantly shorter than  $T_{\text{CYCLE}}$  of OLED drive circuit **200** while achieving equivalent OLED emission time  $T_{\text{ON}}$ . Consequently, the achievable  $T_{\text{ON}}/T_{\text{OFF}}$  rate of OLED **112** within OLED pre-charge circuit **500** is increased compared with the achievable on/off rate of OLED **112** within OLED drive circuit **200**, thereby enhancing overall performance.

**[0051]** In summary, and in reference to **Figure 5**, just prior to the desired  $T_{\text{ON}}$  time, capacitor **210** is charged

rapidly, not via the normal current source ( $I_{\text{SOURCE 116}}$ ) only, but with the additional current available to OLED **112** via current source  $I_{\text{SOURCE 510}}$ .

**[0052]** The charge time  $T_{\text{CHARGE}}$  used for pre-charge, greatly influences the performance of a display. Longer pre-charge times  $T_{\text{CHARGE}}$  limit the maximum light output while compensating by increasing the current level increases the lowest light output and thus eliminates gray scales. High quality displays need a large number of gray scales, thus requiring a high digital resolution or number of possible output values or current sources operating at high clock speeds. A single current pulse (one clock cycle) will only generate light if the threshold is reached within that pulse, for instance in half the time of a clock cycle. If  $f_c$  is the clock frequency, then the shortest  $t_2-t_0$  is  $1/f_c$ . For example a 40 MHz clock, the pre-charge time  $T_{\text{CHARGE}}$  would then have to be as short as 12 ns. For OLED diodes typically operating in the range of 9-15V and a large  $C_{\text{OLED}}$  of 500 pF, a pre-charge current of at least 375 mA ( $C_{\text{OLED}} \cdot dV/dt$ ) is required, which is quite high. However, the requirement of reaching the pre-charge state within a clock pulse period may be overcome by using two current sources **116**, **510** as in **Figure 5**.

**[0053]** **Figure 5A**, demonstrates the possible result of using two current sources **116** and **510** as in **Figure 5**. Current source **510** is for instance capable of delivering twice the current of current source **116**. That means that  $V_{\text{ISOURCE}}$  of current source **510** reaches the threshold voltage in half the time of  $V_{\text{ISOURCE}}$  of current source **116**. Consequently, when both current sources **510**, **116** operate simultaneously,  $V_{\text{ISOURCE}}$  will reach the threshold in a third of the time. In the lower part of **Figure 5A**, corresponding currents  $I_{\text{OLED}}$  through the OLED **112** are shown for a  $t_2-t_0$  equal to the time required for the two current sources **510**, **116** to together reach the threshold. The surface under the current curve is a measure for the emitted light. As can be seen, even though  $V_{\text{ISOURCE}}$  for each current source **510**, **116** separately does not necessarily reach the threshold within the on time, three possible light output values are generated as long as the reached  $V_{\text{OLED}}$  (not drawn in **Figure 5A**) is high enough for the diode **112** to start emitting light. Expanding on this principle, at low light output values highly precise gray scales can be obtained by varying the on time of one or two current sources **510**, **116**. Additionally, high current can be obtained at high light output by switching on both current sources **510**, **116**.

**[0054]** **Figure 6** illustrates a schematic diagram of an OLED pre-charge circuit **600** in accordance with a fourth embodiment of the invention. OLED pre-charge circuit **600** is identical to OLED drive circuit **200** of **Figure 2**, except that current source  $I_{\text{SOURCE 116}}$  is replaced with a current source  $I_{\text{SOURCE 610}}$ , which is a single current source device that is capable of modifying its output current rapidly by selecting either a low or high current reference via a switch **612** and a switch **614**, respectively.



Switches **612** and **614** are conventional active switch devices, such as MOSFET switches or transistors having suitable voltage and current ratings.

**[0055]** In operation and with reference to **Figures 3B** and **6**, during charge time  $T_{\text{CHARGE}}$  switches **114**, **118** and **612** are closed and switch **614** is open, thereby supplying the high current reference to current source  $I_{\text{SOURCE}}$  **610** and thus charging capacitor **210** rapidly. Once the pre-charge operation is complete, switch **612** is opened and switch **614** is closed, thereby supplying the low current reference to current source  $I_{\text{SOURCE}}$  **610**. As a result, current source  $I_{\text{SOURCE}}$  **610** rapidly drops to the normal constant operating current. Switches **114**, **118**, and **614** remain closed for the duration of OLED emission time  $T_{\text{ON}}$  and normal operation occurs. At time  $t_2$  switch **118** is opened, thus ending OLED emission time  $T_{\text{ON}}$ .

**[0056]** Lastly, because the pre-charge circuits of the present invention overcome the adverse performance effects due to  $C_{\text{OLED}}$ , any process variations affecting  $C_{\text{OLED}}$  do not factor into the OLED overall display performance. Thus, the pre-charge circuits of the present invention eliminate the effects of varying OLED device characteristics, such as capacitance, due to manufacturing process variations.

## Claims

1. Drive circuitry for a common anode, passive matrix, organic light-emitting diode (OLED) display comprising at least one OLED (112) having an anode and a cathode, the cathode of the OLED (112) being coupled in series to a first current source (116) and a first switching means (118), wherein the drive circuitry comprises means for pre-charging the at least one OLED (112) before closing the switching means (118).
2. Drive circuitry according to claim 1, wherein the means for pre-charging the at least one OLED comprises a second switching means.
3. Drive circuitry according to claim 2, wherein the second switching means comprises an active switch device.
4. Drive circuitry according to claim 3, wherein the active switch device comprises a MOSFET.
5. Drive circuitry according to claim 4, wherein the MOSFET is an NMOS transistor device having suitable voltage and current ratings for pre-charging the at least one OLED (112).
6. Drive circuitry according to any of claims 2 to 5, wherein the second switching means is coupled in a branch in parallel over the first current source (116).
7. Drive circuitry according to claim 6 in combination with any of claims 4 or 5, the MOSFET (310) having a gate, wherein the source is electrically connected to a pre-charge voltage.
8. Drive circuitry according to claim 7, the MOSFET (310) having a drain, wherein the drain of the MOSFET (310) is electrically connected to the cathode of the OLED (112).
9. Drive circuitry according to any of claims 2 to 5, wherein the second switching means comprises a first switch device (412) suitable for coupling the cathode of the OLED (112) to the ground, and a second switch device (410) suitable for coupling the anode of the OLED (112) to a voltage supply substantially corresponding to the normal operating voltage of the OLED (112).
10. Drive circuitry according to claim 9, wherein the first switch device (412) and the second switch device (410) are active switch devices.
11. Drive circuitry according to claim 10, wherein the active switch devices are MOSFET transistors having suitable voltage and current ratings for pre-charging the at least one OLED (112).
12. Drive circuitry according to any of claims 2 to 5, wherein the means for pre-charging the at least one OLED (112) furthermore comprises a second current source (510) coupled in parallel over the first current source.
13. Drive circuitry according to claim 12, wherein the second current source (510) is suitable for supplying a current between 50 and 800 mA, preferably between 100 and 600 mA.
14. Drive circuitry according to any of claims 12 or 13, wherein the second current source (510) is substantially identical to the first current source (116).
15. Drive circuitry according to any of claims 12 or 13, wherein the second current source (510) is suitable for supplying a current between 2 and 4 times the current supplied by the first current source.
16. Drive circuitry according to any of claims 2 to 5, wherein the first current source (610) is a current source device capable of modifying its output current by selecting either one of a first or a second current reference.
17. An arrangement comprising:

an array of OLEDs (112), each OLED (112) having an anode common with other OLED's of the array and a cathode, and  
drive circuitry according to any of the claims 1 to 16.

5

18. A common anode, passive matrix, organic light-emitting diode (OLED) display comprising an array of OLEDs (112), each OLED (112) having an anode and a cathode, the display comprising drive circuitry according to any of the claims 1 to 16.

10

19. A method for pre-charging an organic light-emitting diode (OLED) of a common anode, passive matrix OLED display prior to a desired ON-time of the OLED, the method comprising charging the OLED immediately prior to the desired ON-time.

15

20. A method according to claim 19, wherein the charging is done by applying a pre-charge voltage to the cathode of the OLED prior to the desired ON-time.

20

21. A method according to claim 19, wherein the charging is done by applying a first voltage level to the anode of the OLED while pulling the cathode of the OLED to a second voltage level, the difference between the first and the second voltage being equal to a desired pre-charge voltage.

25

22. A method according to claim 21, wherein the first voltage level is equal to the desired pre-charge voltage, and the second voltage level is the ground level.

30

23. A method according to claim 19, wherein the charging is done by supplying additional current to the OLED prior to the desired ON-time.

35

24. A method according to any of claims 20 to 22, wherein the pre-charge voltage is substantially equal to a normal operating voltage of the OLED (112) during ON-time.

40

25. A method for pre-charging according to claim 24 and where at low light output extra gray scales are obtained by selectively switching two current sources.

45

#### Amended Claims under Rule 86 (2) EPC

50

1. **(Amended)** Drive circuitry for a common anode, passive matrix, organic light-emitting diode (OLED) display comprising at least one OLED (112) having an anode and a cathode, the cathode of the OLED (112) being coupled in series to a first current source (116) and a first switching means (118), and the anode of the OLED (112) being coupled in

55

series to a second switching means (114), wherein the drive circuitry comprises means for pre-charging the at least one OLED (112) to a strictly positive voltage level before closing the said first switching means (118).

2. **(Amended)** Drive circuitry according to claim 1, wherein the means for pre-charging the at least one OLED comprises a **third** switching means.

3. **(Amended)** Drive circuitry according to claim 2, wherein the **third** switching means comprises an active switch device.

4. **(Original)** Drive circuitry according to claim 3, wherein the active switch device comprises a MOSFET.

5. **(Original)** Drive circuitry according to claim 4, wherein the MOSFET is an NMOS transistor device having suitable voltage and current ratings for pre-charging the at least one OLED (112).

6. **(Amended)** Drive circuitry according to any of claims 2 to 5, wherein the **third** switching means is coupled in a branch in parallel over the first current source (116).

7. **(Amended)** Drive circuitry according to claim 6 in combination with any of claims 4 or 5, the MOSFET (310) having a **source**, wherein the source is electrically connected to a pre-charge voltage.

8. **(Original)** Drive circuitry according to claim 7, the MOSFET (310) having a drain, wherein the drain of the MOSFET (310) is electrically connected to the cathode of the OLED (112).

9. **(Amended)** Drive circuitry according to any of claims 2 to 5, wherein the **third** switching means comprises a first switch device (412) suitable for coupling the cathode of the OLED (112) to the ground, and a second switch device (410) suitable for coupling the anode of the OLED (112) to a voltage supply substantially corresponding to the normal operating voltage of the OLED (112).

10. **(Original)** Drive circuitry according to claim 9, wherein the first switch device (412) and the second switch device (410) are active switch devices.

11. **(Original)** Drive circuitry according to claim 10, wherein the active switch devices are MOSFET transistors having suitable voltage and current ratings for pre-charging the at least one OLED (112).

12. **(Original)** Drive circuitry according to any of claims 2 to 5, wherein the means for pre-charging

the at least one OLED (112) furthermore comprises a second current source (510) coupled in parallel over the first current source.

**13. (Original)** Drive circuitry according to claim 12, wherein the second current source (510) is suitable for supplying a current between 50 and 800 mA, preferably between 100 and 600 mA. 5

**14. (Original)** Drive circuitry according to any of claims 12 or 13, wherein the second current source (510) is substantially identical to the first current source (116). 10

**15. (Original)** Drive circuitry according to any of claims 12 or 13, wherein the second current source (510) is suitable for supplying a current between 2 and 4 times the current supplied by the first current source. 15

**16. (Amended)** Drive circuitry according to any of claims 2 to 5, wherein the first current source (610) is a current source device capable of modifying its output current by selecting either one of a first or a second current reference, where the **first current reference is selected during the pre-charge operation and the second current reference is selected during the normal operation.** 20 25

**17. (Original)** An arrangement comprising: 30  
an array of OLEDs (112), each OLED (112) having an anode common with other OLED's of the array and a cathode, and  
drive circuitry according to any of the claims 1 to 16. 35

**18. (Original)** A common anode, passive matrix, organic light-emitting diode (OLED) display comprising an array of OLEDs (112), each OLED (112) having an anode and a cathode, the display comprising drive circuitry according to any of the claims 1 to 16. 40

**19. (Amended)** A method for pre-charging an organic light-emitting diode (OLED) of a common anode, passive matrix OLED display prior to a desired ON-time of the OLED, the method comprising charging the OLED to a strictly positive voltage level immediately prior to the desired ON-time. 45 50

**20. (Original)** A method according to claim 19, wherein the charging is done by applying a pre-charge voltage to the cathode of the OLED prior to the desired ON-time. 55

**21. (Original)** A method according to claim 19, wherein the charging is done by applying a first voltage level to the anode of the OLED while pulling the

cathode of the OLED to a second voltage level, the difference between the first and the second voltage being equal to a desired pre-charge voltage.

**22. (Original)** A method according to claim 21, wherein the first voltage level is equal to the desired pre-charge voltage, and the second voltage level is the ground level.

**23. (Original)** A method according to claim 19, wherein the charging is done by supplying additional current to the OLED prior to the desired ON-time.

**24. (Original)** A method according to any of claims 20 to 22, wherein the pre-charge voltage is substantially equal to a normal operating voltage of the OLED (112) during ON-time.

**25. (Original)** A method for pre-charging according to claim 24 and where at low light output extra gray scales are obtained by selectively switching two current sources.

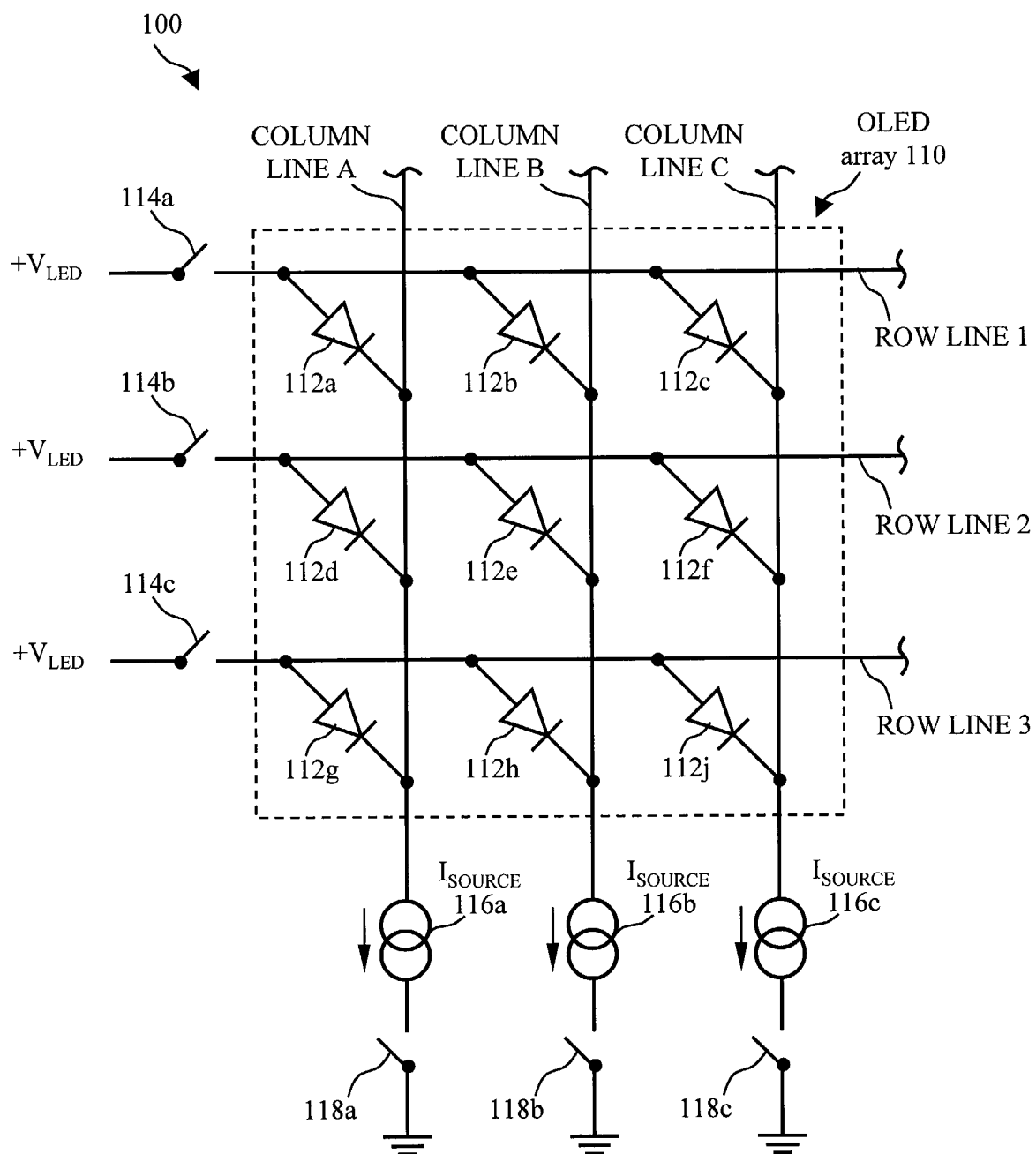
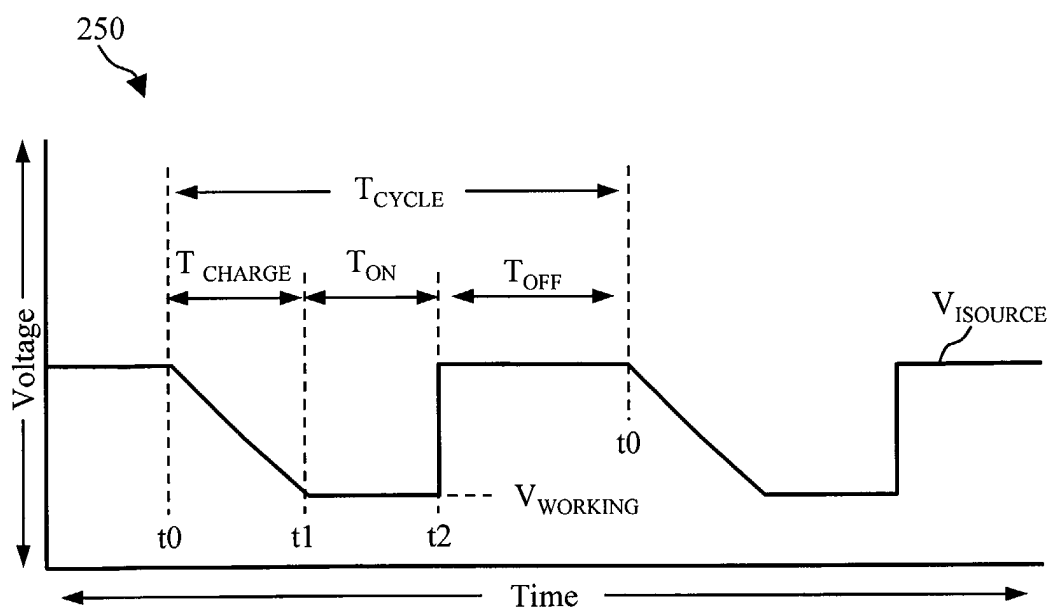
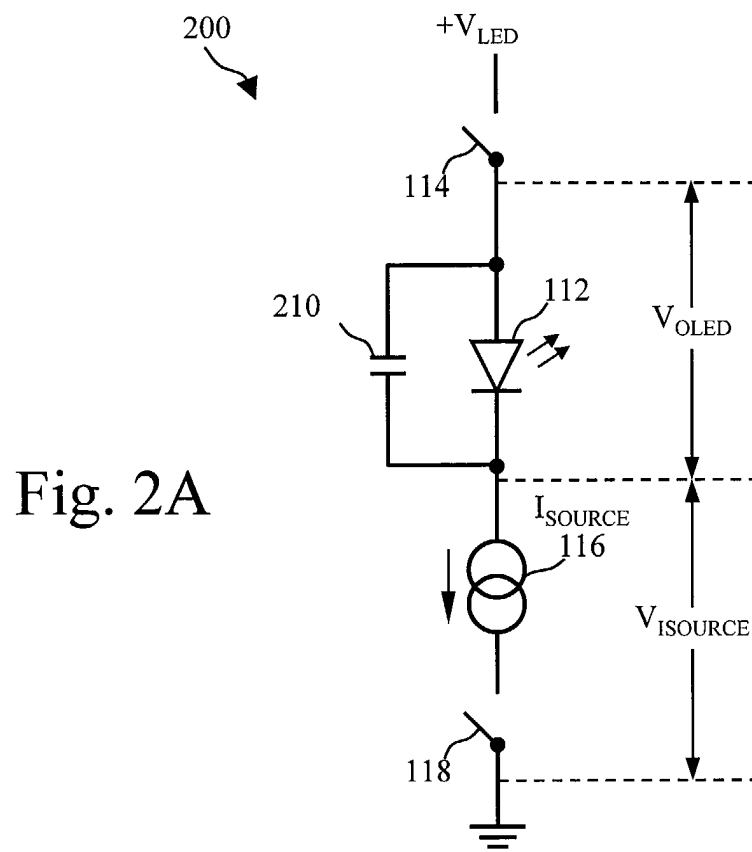


Fig. 1



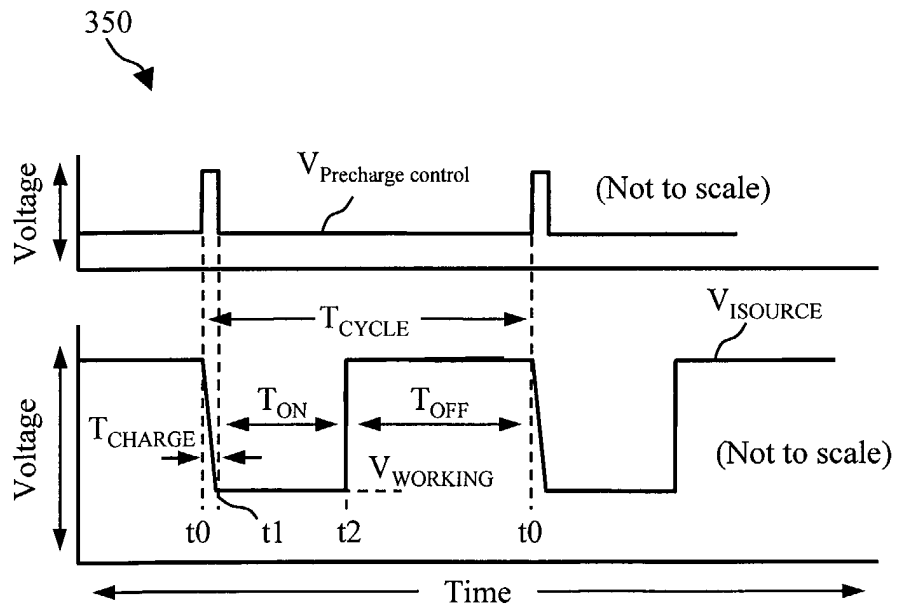
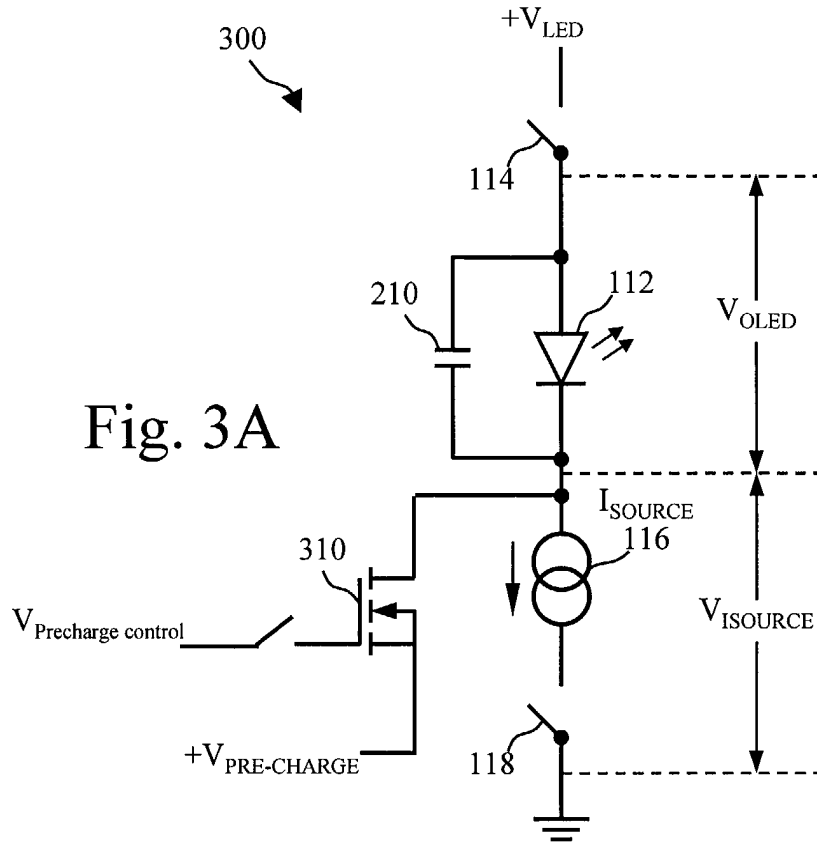


Fig. 3B

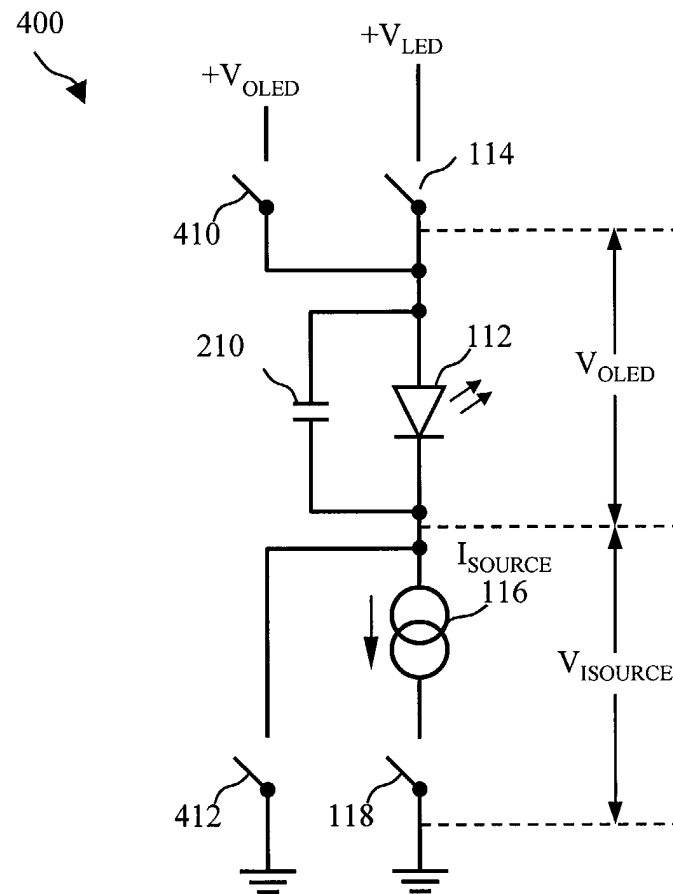


Fig. 4

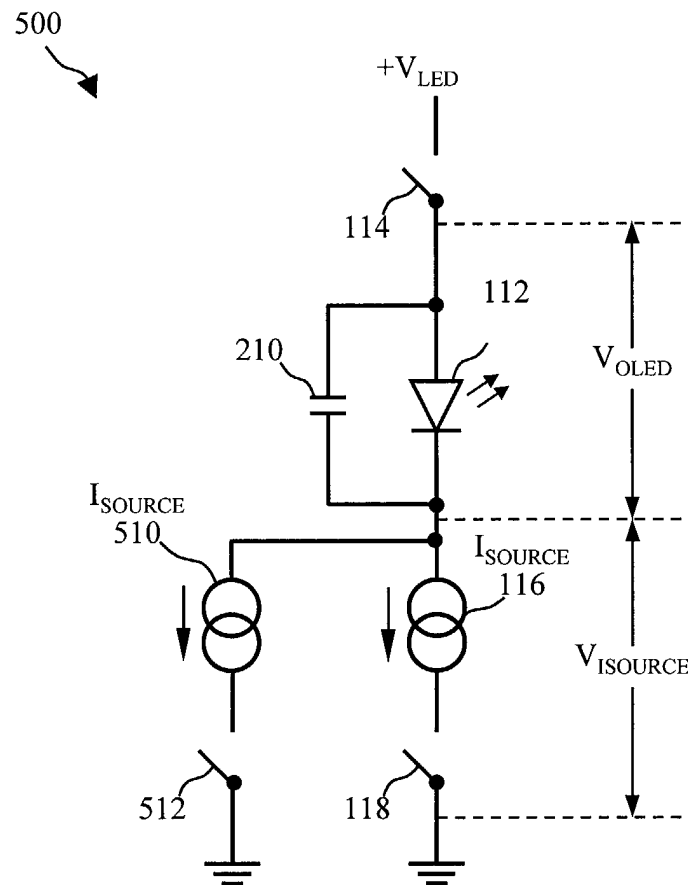


Fig. 5



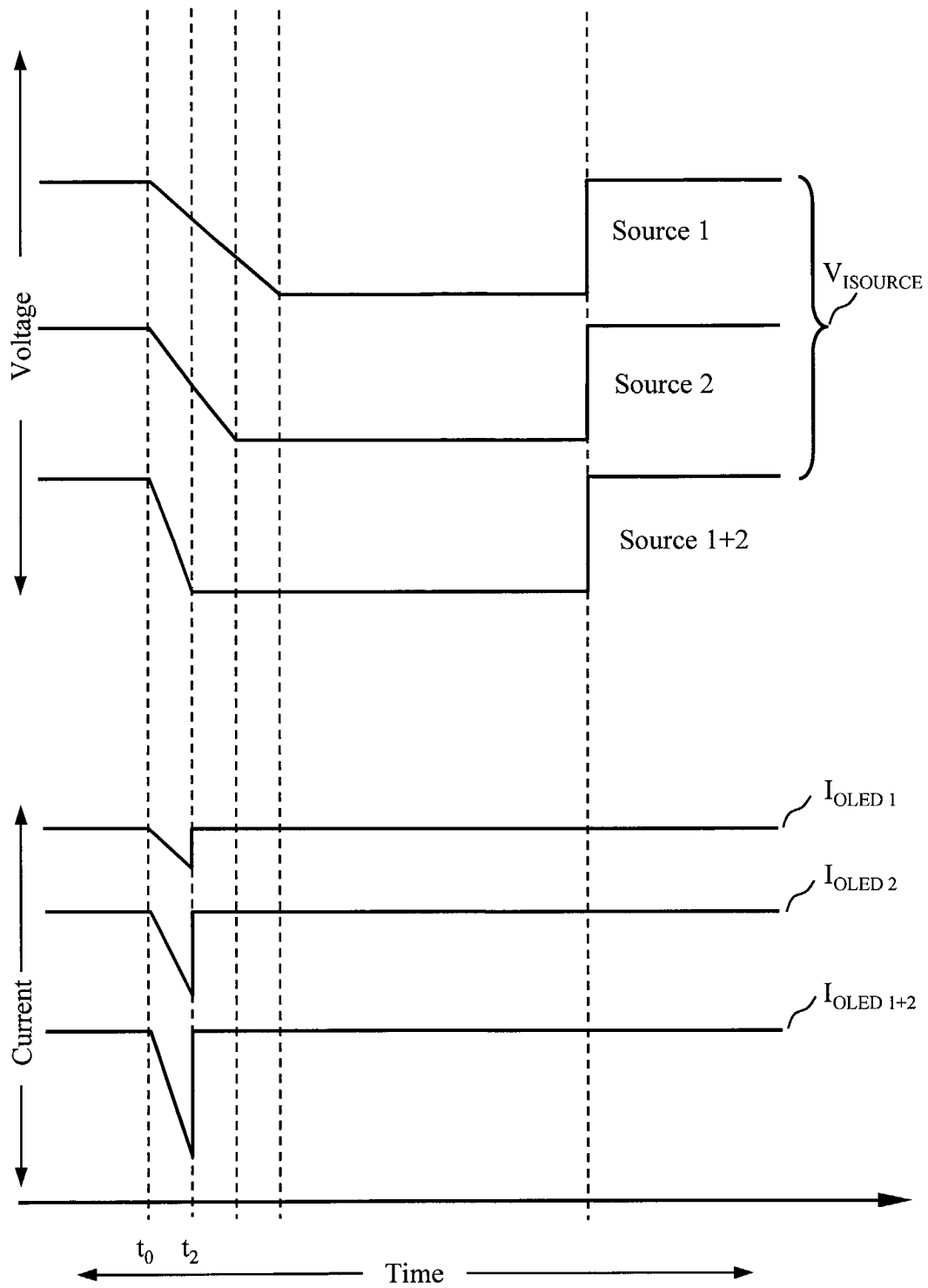


Fig. 5A

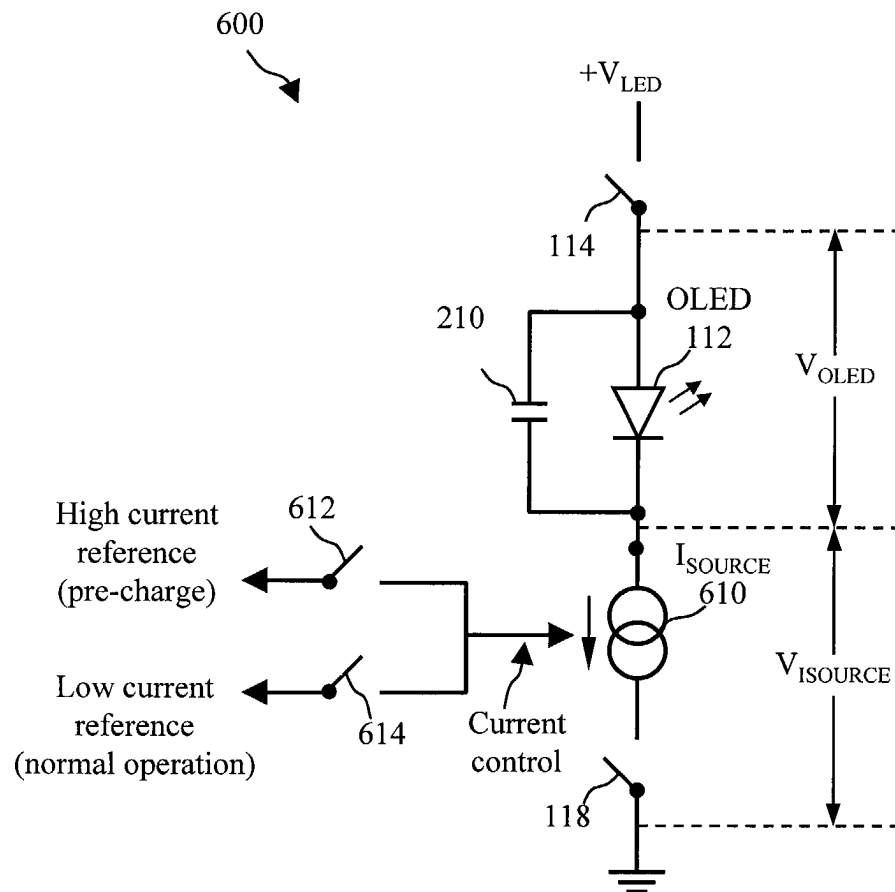


Fig. 6



European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 03 07 6206

| DOCUMENTS CONSIDERED TO BE RELEVANT                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |                                                                                                            |                                                       |                                                 |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------|-------------------------------------------------------|-------------------------------------------------|
| Category                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | Citation of document with indication, where appropriate, of relevant passages                              | Relevant to claim                                     | CLASSIFICATION OF THE APPLICATION (Int.Cl.7)    |
| X                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | US 6 486 607 B1 (YEUAN JIAN-JONG)<br>26 November 2002 (2002-11-26)                                         | 1-5,<br>9-11,<br>16-21,<br>23,24                      | G09G3/32                                        |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | * the whole document *                                                                                     | 6-8,12,<br>14,15,<br>22,25                            |                                                 |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | ---<br>US 6 310 589 B1 (KAWASHIMA SHINGO ET AL)<br>30 October 2001 (2001-10-30)<br>* the whole document *  | 6                                                     |                                                 |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | ---<br>US 6 366 116 B1 (JUANG DAR-CHANG)<br>2 April 2002 (2002-04-02)<br>* the whole document *            | 6-8                                                   |                                                 |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | ---<br>EP 1 274 065 A (LG ELECTRONICS INC)<br>8 January 2003 (2003-01-08)<br>* the whole document *        | 12,14,<br>15,25                                       |                                                 |
| Y                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | ---<br>US 6 473 064 B1 (ISHIZUKA SHINICHI ET AL)<br>29 October 2002 (2002-10-29)<br>* the whole document * | 12,14,<br>15,22                                       | TECHNICAL FIELDS<br>SEARCHED (Int.Cl.7)<br>G09G |
| The present search report has been drawn up for all claims                                                                                                                                                                                                                                                                                                                                                                                                                                                                             |                                                                                                            |                                                       |                                                 |
| Place of search<br>MUNICH                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                                                                                                            | Date of completion of the search<br>10 September 2003 | Examiner<br>Harke, M                            |
| CATEGORY OF CITED DOCUMENTS<br>X : particularly relevant if taken alone<br>Y : particularly relevant if combined with another document of the same category<br>A : technological background<br>O : non-written disclosure<br>P : intermediate document<br>T : theory or principle underlying the invention<br>E : earlier patent document, but published on, or after the filing date<br>D : document cited in the application<br>L : document cited for other reasons<br>& : member of the same patent family, corresponding document |                                                                                                            |                                                       |                                                 |

EPO FORM 1503 03/02 (P04001)

**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 03 07 6206

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.  
The members are as contained in the European Patent Office EDP file on  
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

10-09-2003

| Patent document<br>cited in search report |    | Publication<br>date | Patent family<br>member(s) | Publication<br>date |
|-------------------------------------------|----|---------------------|----------------------------|---------------------|
| US 6486607                                | B1 | 26-11-2002          | NONE                       |                     |
| US 6310589                                | B1 | 30-10-2001          | JP 3102411 B2              | 23-10-2000          |
|                                           |    |                     | JP 11045071 A              | 16-02-1999          |
|                                           |    |                     | TW 381249 B                | 01-02-2000          |
|                                           |    |                     | US 2001048410 A1           | 06-12-2001          |
| US 6366116                                | B1 | 02-04-2002          | GB 2371399 A               | 24-07-2002          |
| EP 1274065                                | A  | 08-01-2003          | KR 2003004774 A            | 15-01-2003          |
|                                           |    |                     | CN 1402215 A               | 12-03-2003          |
|                                           |    |                     | EP 1274065 A2              | 08-01-2003          |
|                                           |    |                     | JP 2003043997 A            | 14-02-2003          |
|                                           |    |                     | US 2003006713 A1           | 09-01-2003          |
| US 6473064                                | B1 | 29-10-2002          | JP 11231834 A              | 27-08-1999          |
|                                           |    |                     | US 2002196215 A1           | 26-12-2002          |