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(12) CORRECTED EUROPEAN PATENT APPLICATION

Note: Bibliography reflects the latest situation

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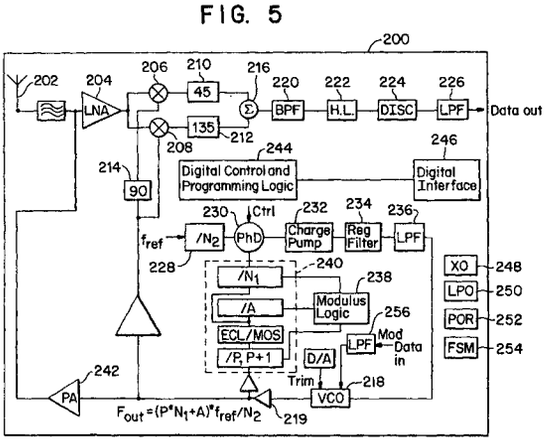
Remarks:

This application was filed on 25-11-2004 as a divisional application to the application mentioned under INID code 62.

(54) Radio transceiver on a chip

(57) A radio for receiving and transmitting high frequency signals. The radio comprises a down-conversion section (206, 208) arranged to down-convert a received first high frequency signal to an intermediate frequency signal; an up-conversion section arranged to up-convert an information signal to a second high frequency signal; a variable voltage controlled oscillator (218) connected to both the up-conversion section and the down-conversion section for operation in a time division duplex mode; and a detector (224) for information recovery. The up-conversion section, the down-conversion section and the variable controlled oscillator are integrated into a single IC chip. The intermediate frequency signal is a low intermediate frequency signal. A filter is connected to the down-conversion section and is tuned for passing the low intermediate frequency signal. The filter is integrated on the single IC chip.

FIG. 5



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