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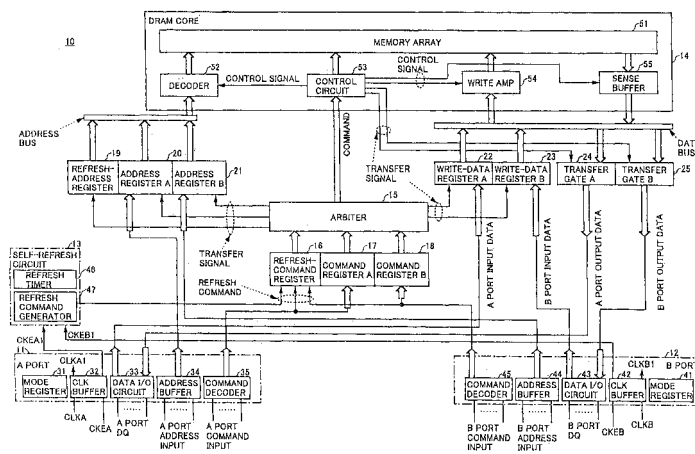
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(54) **Multi-port memory based on DRAM core**

(57) A semiconductor memory device includes a plurality of N external ports, each of which receives com-

mands, and an internal circuit which performs at least N access operations during a minimum interval of the commands that are input into one of the external ports.

FIG.4





European Patent
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EUROPEAN SEARCH REPORT

Application Number
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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	US 5 659 711 A (SUGITA MITSURU) 19 August 1997 (1997-08-19) * column 11, line 54 - column 16, line 15; figure 1 *	1	G11C8/16
A	-----	2-40	
			TECHNICAL FIELDS SEARCHED (IPC)
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The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 30 January 2006	Examiner Gaertner, W
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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