



(11) **EP 1 564 749 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:
Corrected version no 1 (W1 B1)
Bibliography INID code(s) 84

(51) Int Cl.:
G11C 8/16 (2006.01)

(48) Corrigendum issued on:
18.02.2009 Bulletin 2009/08

(45) Date of publication and mention
of the grant of the patent:
12.11.2008 Bulletin 2008/46

(21) Application number: **05010996.6**

(22) Date of filing: **02.10.2001**

(54) **Multi-port memory based on DRAM core**

Multiportspeicher auf Basis von DRAM

Mémoire multiportes basée sur une DRAM

(84) Designated Contracting States:
DE FR

(30) Priority: **20.12.2000 JP 2000387891**
27.12.2000 JP 2000398893
27.12.2000 JP 2000399052
09.02.2001 JP 2001034361
14.02.2001 JP 2001037547

(43) Date of publication of application:
17.08.2005 Bulletin 2005/33

(62) Document number(s) of the earlier application(s) in
accordance with Art. 76 EPC:
01308424.9 / 1 220 226

(73) Proprietor: **Fujitsu Microelectronics Limited**
Tokyo 163-0722 (JP)

(72) Inventors:
• **Matsuzaki, Yasurou**
Fujitsu Limited
Kawasaki-shi
Kanagawa 211-8588 (JP)

- **Suzuki, Takaaki**
Fujitsu Limited
Kawasaki-shi
Kanagawa 211-8588 (JP)
- **Yamazaki, Masafumi**
Fujitsu Limited
Kawasaki-shi
Kanagawa 211-8588 (JP)
- **Kawasaki, Kenichi**
Fujitsu Limited
Kawasaki-shi
Kanagawa 211-8588 (JP)
- **Kamata, Shinnosuke**
Fujitsu Limited
Kawasaki-shi
Kanagawa 211-8588 (JP)

(74) Representative: **Mohun, Stephen John**
Haseltine Lake
Lincoln House
300 High Holborn
London WC1V 7JH (GB)

(56) References cited:
EP-A- 0 496 391 **US-A- 5 497 353**
US-A- 5 587 957 **US-A- 5 946 262**

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).