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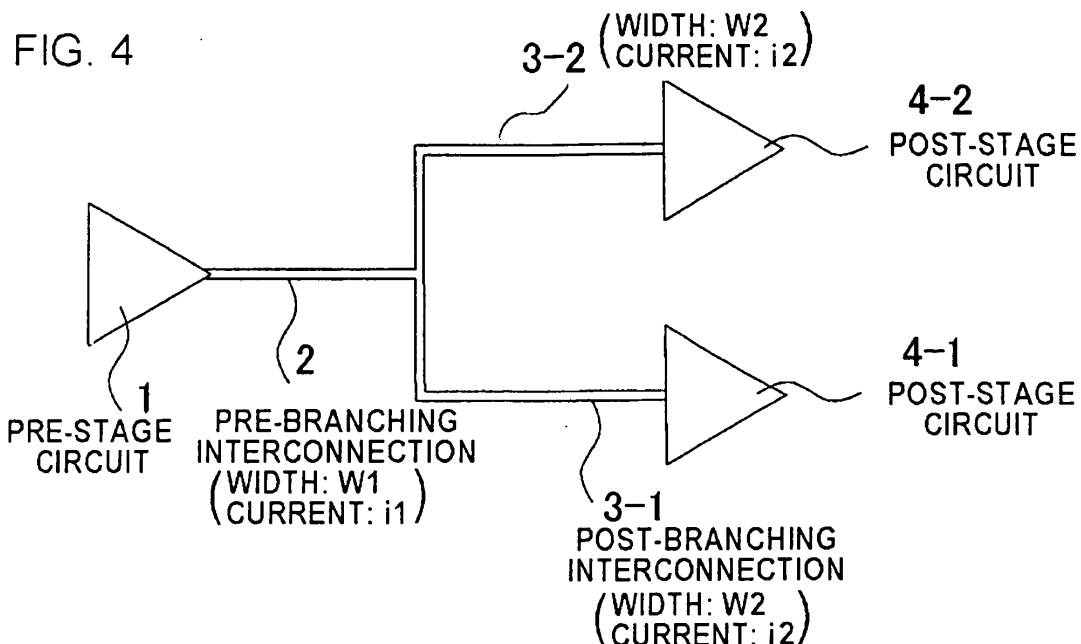
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(54) Semiconductor integrated circuit, method for designing semiconductor integrated circuit and system for designing semiconductor integrated circuit

(57) The semiconductor integrated circuit capable of reducing an interconnection width as compared with conventional one while suppressing electromigration effectively. An input unit 101 stores interconnection information in an interconnection information storage unit 104. An arithmetic operation unit 102 acquires the interconnection information upon accessing the interconnection

information storage unit 104 and acquires an arithmetic operation parameter while accessing the arithmetic operation parameter storage unit 105 to determine a width W of the interconnection based on these values. That is, the width W of the interconnection is made to determine upon multiplying $(\text{current } i)^{1/3}$ by the arithmetic operation parameter (constant), which current is caused to flow through the interconnection.





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
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			TECHNICAL FIELDS SEARCHED (IPC)
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The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 15 March 2006	Examiner Cortes Rosa, Joao
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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