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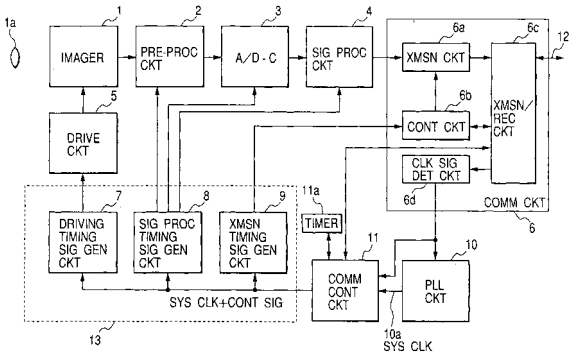
(54) An imaging apparatus with video data transmission

(57) A communication circuit transmits the digital video signal to a transmission line and receives a clock signal and timing data transmitted through the transmission line. The timing data indicates a communication timing. A timing signal generation circuit generates timing signals from the detected clock signal to control the imager, the a/d converting circuit, the signal processing circuit, and the communication circuit. A communication control circuit detects a communication timing from the timing data, judges whether the communication timing is detected within a predetermined condition (communication error), and controls the timing signal generation circuit to stop transmitting the digital video signal when the communication timing is not detected within the predetermined condition to prevent a fail in transmitting the video data. A shutter interval of the imager is further controlled toward a constant shutter interval in response to the communication error. The video signal is stored in a memory in response to the communication error and read if the communication error eliminated. The communication error is displayed. A system control signal indicative of data transmission period is generated in response to a received cycle start packet in accordance with the obtained data rate and channel timing.

FIG. 1



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