



(12) **CORRECTED EUROPEAN PATENT APPLICATION**

published in accordance with Art. 158(3) EPC

Note: Bibliography reflects the latest situation

(15) Correction information:

Corrected version no 1 (W1 A1)
INID code(s) 87

(51) Int Cl.:

H04N 7/26 (2006.01)

(48) Corrigendum issued on:

02.08.2006 Bulletin 2006/31

(86) International application number:

PCT/JP2005/012772

(43) Date of publication:

31.05.2006 Bulletin 2006/22

(87) International publication number:

WO 2006/006564 (19.01.2006 Gazette 2006/03)

(21) Application number: **05758294.2**

(22) Date of filing: **11.07.2005**

(84) Designated Contracting States:

**AT BE BG CH CY CZ DE DK EE ES FI FR GB GR
HU IE IS IT LI LT LU LV MC NL PL PT RO SE SI
SK TR**

Designated Extension States:

AL BA HR MK YU

(72) Inventors:

- **SUZUKI, Teruhiko**
SONY CORPORATION
Tokyo 1410001 (JP)
- **ISHIGAYA, Kazuhiro**
c/o SONY CORPORATION
Tokyo 1410001 (JP)
- **YAGASAKI, Yoicho**
c/o SONY CORPORATION
Tokyo 1410001 (JP)

(30) Priority: **12.07.2004 JP 2004204313**

24.01.2005 JP 2005015284

(74) Representative: **Keston, Susan Elizabeth et al**

D Young & Co
120 Holborn
London EC1 2DY (GB)

(71) Applicant: **SONY CORPORATION**

Tokyo 141-0001 (JP)

(54) **ENCODING METHOD, ENCODING DEVICE, DECODING METHOD, DECODING DEVICE, AND PROGRAM THEREOF**

(57) A sub block generation circuit 52 divides coefficients of orthogonal transformation by 8×8 to sub block data, and a two-dimensional reversible coding circuit 54 selects transformation table data for assigning an encod-

ing code having a short bit length to non-0 coefficient quantity data indicating "0" in non-0 coefficient quantity data "TotalCoeff" of a sub block being away from direct current components, and performs encoding.

FIG. 3

