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(54) Plasma display apparatus and driving method thereof

(57) The present invention relates to a plasma display apparatus. The plasma display apparatus according to the present invention comprises a plasma display panel comprising a scan electrode, a set-up driver that applies a rising pulse to the scan electrode during a reset period, a set-down driver that applies a first ramp-down pulse before the rising pulse is applied to the scan elec-

trode and applies a second ramp-down pulse after the rising pulse is applied to the scan electrode, and a scan pulse driver that applies a scan pulse having an amount, which is substantially the same as that of the rising pulse, to the scan electrode during an address period subsequent to the reset period.



EUROPEAN SEARCH REPORT

Application Number EP 06 00 3144

<u>'</u>	DOCUMENTS CONSIDI		1	
Category	Citation of document with in of relevant passa	dication, where appropriate, ges	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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Х	US 2004/012547 A1 (22 January 2004 (20 * paragraphs [0065] 7,8,9a-9e *	04-01-22)	1-17	
A	JP 2001 013910 A (F 19 January 2001 (20 * figure 3 *	 UJITSU LTD) 01-01-19)	1-17	
				TECHNICAL FIELDS SEARCHED (IPC)
				G09G
			-	
	The present search report has k	een drawn up for all claims		
	Place of search	Date of completion of the search		Examiner
	The Hague	12 June 2009	Váz	quez del Real, S
X : parti Y : parti docu	ATEGORY OF CITED DOCUMENTS cularly relevant if taken alone cularly relevant if combined with anothment of the same category nological background	L : document cited for	cument, but publice n the application or other reasons	
0	-written disclosure	& : member of the sa		



Application Number

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CLAIMS INCURRING FEES
The present European patent application comprised at the time of filing claims for which payment was due.
Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):
No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.
LACK OF UNITY OF INVENTION
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:
see sheet B
All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims: 1-17
The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).



LACK OF UNITY OF INVENTION SHEET B

Application Number

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-17

refer to a scan driver of a plasma display applying a pre-reset waveform, a reset waveform, an addressing waveform and a sustain waveform wherein the voltages values applied in several points of the drawing waveforms are the same.

2. claim: 18

refer to the a scan driver of a plasma display comprising an energy recovery circuit and several switches wherein only one block transistor is found in the current path.

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 06 00 3144

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

12-06-2009

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