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(54) **Method and apparatus for generating gate control signal of liquid crystal display**

(57) A liquid crystal display is disclosed, which includes a panel having an array of pixels, a timing controller outputting image data and source control signals, a series of source drivers and a gate driver. One of the source drivers is selected to generate gate control signals by reference to an identification signal of the source control signals and transmitted to the gate driver. Thus, the

gate driver along with a selected one of the source drivers can drive the panel pixels.

Each source driver comprises transceivers and can operate in one of two modes. In a first mode, control and image data are received by the driver. In the second mode control and image data are transmitted to a next source driver.

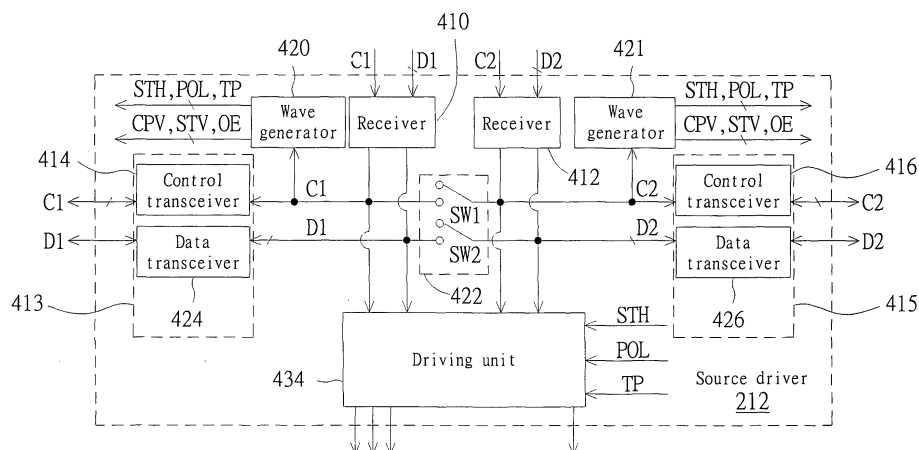


FIG. 5A



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EUROPEAN SEARCH REPORT

Application Number
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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
The Hague		10 July 2007	Husselin, Stephane
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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