



(12) **EUROPEAN PATENT APPLICATION**

(88) Date of publication A3:
08.08.2007 Bulletin 2007/32

(51) Int Cl.:
G06F 17/10 (2006.01)

(43) Date of publication A2:
21.02.2007 Bulletin 2007/08

(21) Application number: **06024665.9**

(22) Date of filing: **26.05.1995**

(84) Designated Contracting States:
DE FR GB

(30) Priority: **28.05.1994 JP 13805494**
28.05.1994 JP 13805594

(62) Document number(s) of the earlier application(s) in accordance with Art. 76 EPC:
95303586.2 / 0 685 800

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(54) **Digital signal processing**

(57) In processing a digital information signal (a digital video signal etc), an improvement in the accuracy of the signal process can be made by integrating the time region process and the frequency region process. In one embodiment, an input digital video signal is supplied to a DCT circuit 3. The DCT circuit 3 converts the input video digital signal into coefficient data. A categorizing circuit 5 separates a flat component 6a and an impulse component 6b in a frequency region from the input signal. The flat component 6a is supplied to an inverse DCT circuit 7. The inverse DCT circuit 7 converts the input signal into a signal on a time axis. A class categorizing adaptive processing circuit 9 compensates a resolution in a time region. The impulse component 6a is supplied to a gain converting circuit 10. The gain converting circuit 10 compensates the high band in a frequency region. The output signal of the gain converting circuit 10 is supplied to an inverse DCT circuit 11. The inverse DCT circuit 11 converts the input signal into a signal on a time axis. A mix circuit 14 mixes signals that have been processed. Thus, a video signal compensated the resolution is obtained from an output terminal 15. The adaptive processing circuit 9 and the gain converting circuit 10 use pre-learned predicted coefficients, predicted values, and gain converting ratio.

Fig. 5A

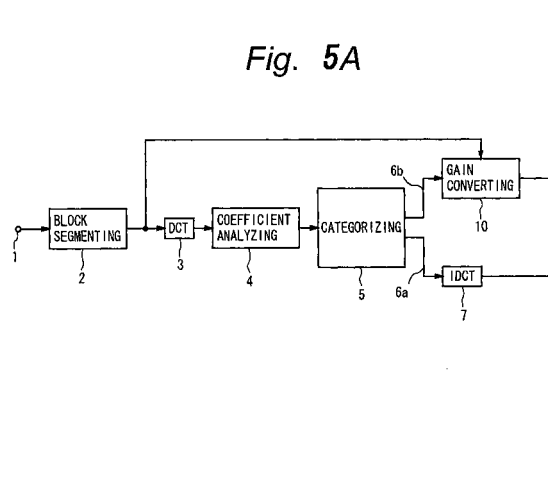
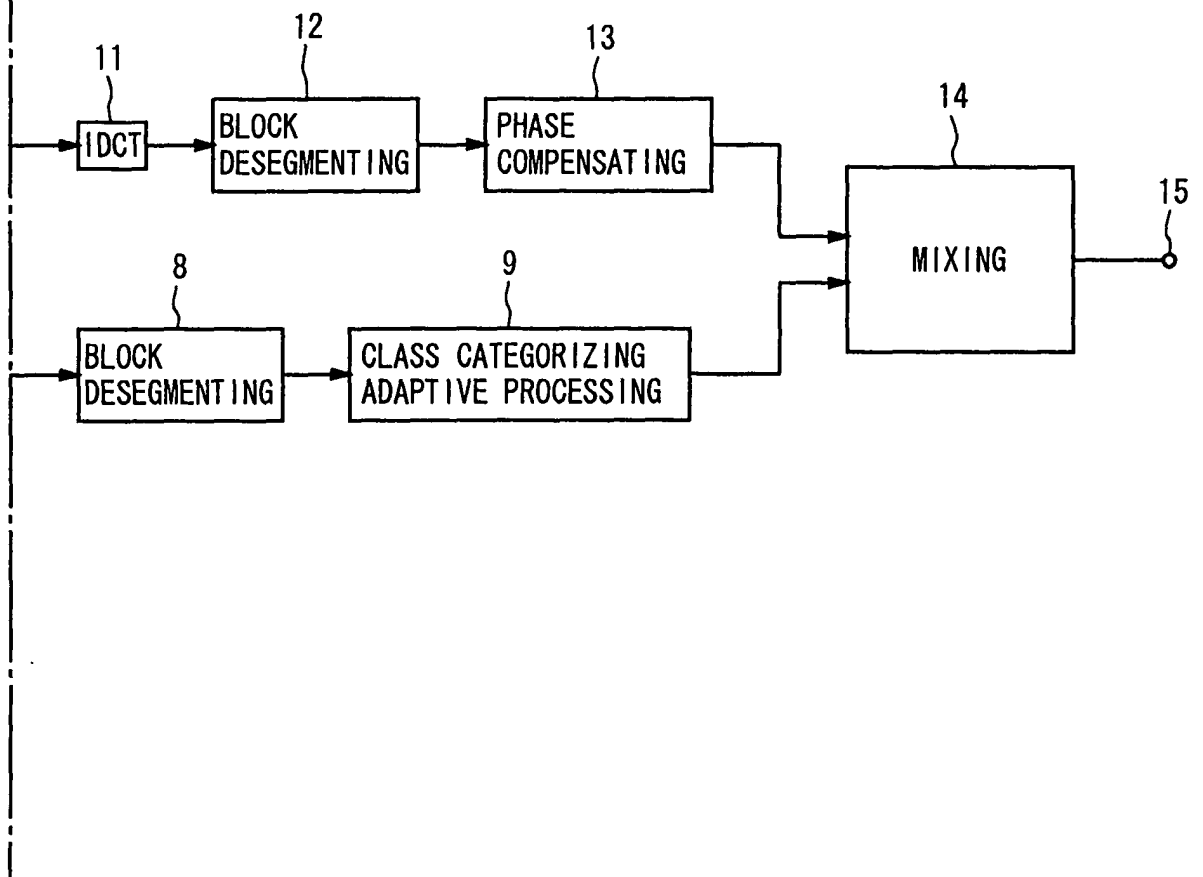


Fig. 5B





European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 06 02 4665

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The present search report has been drawn up for all claims			
Place of search		Date of completion of the search	Examiner
Munich		2 July 2007	PIERFEDERICI, A
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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EPO FORM 1503 03.82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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02-07-2007

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