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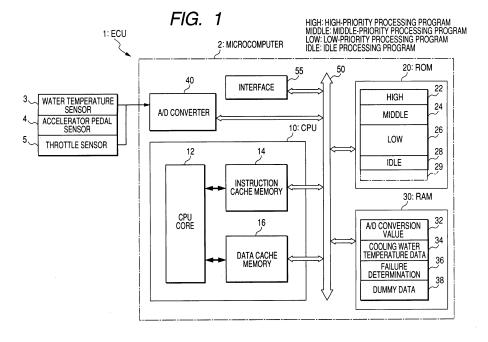
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(54) Microcomputer provided with instruction cache memory

(57) In a microcomputer provided with an instruction cache memory, in which a plurality of programs are executed, the execution time of each of the programs is ensured not be long. After dummy execution of high-priority processing in a time zone when a CPU of the microcomputer is in the process of executing idle process-

ing, a high-priority processing program is stored in the instruction cache memory. The time for executing the high-priority processing afterwards with normal timing can be reduced because the high-priority processing program has already been stored in the instruction cache memory, whereby the hit rate is increased.





EUROPEAN SEARCH REPORT

Application Number EP 06 02 6784

-	DOCUMENTS CONSIDE			
Category	Citation of document with ind of relevant passag		Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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				SEARCHED (IPC)
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Place of search The Hague		Date of completion of the search 22 February 2008	Brandt, Jan	
X : part Y : part docu A : tech	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone icularly relevant if combined with anothe ument of the same category inological background -written disclosure rmediate document	T : theory or principle E : earlier patent doc after the filing date r D : document cited in L : document cited in	underlying the ument, but publi the application r other reasons	invention shed on, or

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22-02-2008

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