



(12) **EUROPEAN PATENT APPLICATION**
published in accordance with Art. 158(3) EPC

(43) Date of publication:
30.07.2008 Bulletin 2008/31

(51) Int Cl.:
H05B 41/282 (2006.01) H05B 41/24 (2006.01)

(21) Application number: **06832450.8**

(86) International application number:
PCT/JP2006/322389

(22) Date of filing: **09.11.2006**

(87) International publication number:
WO 2007/055289 (18.05.2007 Gazette 2007/20)

(84) Designated Contracting States:
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IS IT LI LT LU LV MC NL PL PT RO SE SI SK TR

(72) Inventor: **WANG, Guo-Hua**
Yamatokohriyama-shi, Nara 6391032 (JP)

(30) Priority: **14.11.2005 JP 2005328981**
10.10.2006 JP 2006276501

(74) Representative: **Steil, Christian et al**
Witte, Weller & Partner
Postfach 10 54 62
70047 Stuttgart (DE)

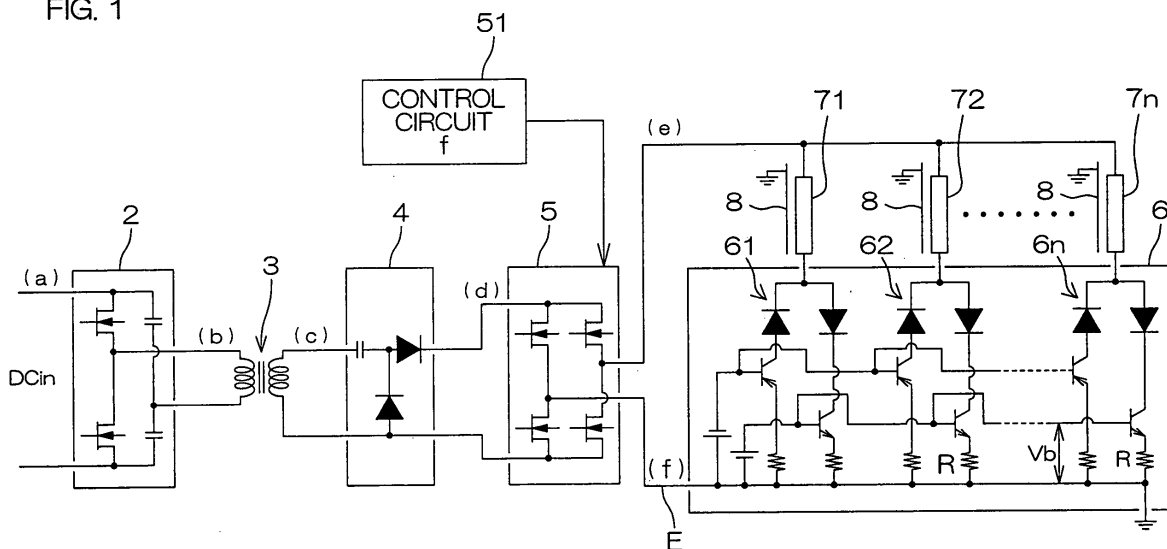
(71) Applicant: **Nitta Corporation**
Osaka-shi,
Osaka 556-0022 (JP)

(54) **FLUORESCENT LAMP OPERATION DEVICE**

(57) A fluorescent lamp lighting device includes: a polarity switching circuit (5) for performing polarity switching with a low frequency (f) not greater than 20 kHz on DC input; a plurality of fluorescent lamps (71, ..., 7n) connected to the output line of the polarity switching circuit (5); a uniform flow circuit (6) formed by constant current circuits (61, ..., 6n) connected to the other end of the flu-

orescent lamps (71, ..., 7n) and having a transistor for supplying identical current to the respective fluorescent lamps (71, ..., 7n). Since the fluorescent lamps are turned on with the low frequency (f) not greater than 20 kHz, they are hardly affected by stray capacitance, the fluorescent lamps (71, ..., 7n) can be arranged near a chassis (8) of a display device and it is possible to prolong wiring. (FIG. 1)

FIG. 1



Description

TECHNICAL FIELD

[0001] The present invention relates to a fluorescent lamp lighting device capable of simultaneously turning on a plurality of fluorescent lamps.

BACKGROUND ART

[0002] A type of fluorescent lamp called a cold cathode fluorescent tube is used for backlights of various display devices, such as a liquid crystal display. Conventionally, a high-frequency AC lighting system using an inverter has been adopted to turn on and drive the cold cathode fluorescent tube.

[0003] FIG. 15 is a circuit diagram showing a conventional high-frequency AC lighting device. The high-frequency AC lighting device comprises an inverter circuit 21 for supplying high-frequency AC power source of several tens kHz, a main transformer 31, a plurality of cold cathode fluorescent tubes 70 of which one ends are connected to an output line of the main transformer 31, and a uniform flow circuit 601 composed of a plurality of transformers, connected to the other ends of the above-mentioned plurality of cold cathode fluorescent tubes 70, for supplying uniform current to the respective cold cathode fluorescent tubes 70.

[0004] FIG. 16 shows a waveform at each part of the high frequency lighting device in FIG. 15, (a) showing a DC input voltage inputted into an inverter 2, (b) showing a waveform of an output voltage of the inverter 2, that is, a primary-side voltage of the main transformer 31, and (c) showing a waveform of a high-frequency voltage on a secondary side of the main transformer 31.

Patent Publication 1: Japanese Unexamined Patent Publication No. 2000-294391

DISCLOSURE OF THE INVENTION

Problems to be Solved by the Invention

[0005] In such high frequency lighting device, the uniform flow circuit 601 includes a plurality of transformers, thereby increasing the size thereof.

[0006] In the conventional high-frequency AC lighting device, the main transformer 31 occupies the largest portion of the device from the viewpoints of size and price, so that the miniaturization of the main transformer 31 is also desired.

[0007] There also arises a problem that since stray capacitance exists between a fluorescent lamp and a chassis and the like therearound, when the fluorescent lamp is turned on with a high-frequency AC power supply at several tens of kHz, a high frequency current flows into the chassis through the stray capacitance, thereby reducing efficiency during lighting.

[0008] It is an object of the present invention to provide a fluorescent lamp lighting device capable of using a small uniform flow circuit, effectively turning on a fluorescent lamp, and generally miniaturizing its size.

Means for Solving the Problem

[0009] According to the present invention, a fluorescent lamp lighting device includes a polarity switching circuit which outputs a low-frequency driving voltage of a predetermined frequency (f) by switching a polarity at the predetermined frequency (f) on DC input, a plurality of fluorescent lamps of which one ends are connected to an output line of the polarity switching circuit, and a uniform flow circuit formed by a constant current circuit connected to the other ends of the plurality of fluorescent lamps and having a transistor for supplying identical current to the respective fluorescent lamps.

[0010] With the fluorescent lamp lighting device, the uniform flow circuit is formed by a constant current circuit including a transistor, thereby achieving miniaturization of the uniform flow circuit. Therefore, the entire high-frequency AC lighting device can be minimized.

[0011] Since the uniform flow circuit is formed by a constant current circuit including a transistor, if a driving frequency of a fluorescent lamp is high as in a conventional device, the uniform flow circuit is prone to produce significant losses and to increase heat generation. Therefore, it is preferable that the above-mentioned polarity switching circuit has a frequency (f) as low as possible, which is greater than 0 Hz, but not greater than 10 kHz. More preferably, the frequency (f) is greater than 0 Hz, but not greater than 1 kHz.

[0012] As described above, the frequency (f) of the polarity switching circuit is lowered from the conventional frequency, so that the fluorescent lamp lighting device is less affected by stray capacitance between the fluorescent lamp or wiring thereof and the chassis (ground potential). For this reason, the fluorescent lamp can be directly mounted to the chassis of a display device, and the wiring can also be extended. The direct mounting of the fluorescent lamp to the chassis thereby achieves reduction in thickness of the display device. The extension of the wiring allows a display device having a large screen to be readily manufactured.

[0013] Conventionally, DC (0 Hz) lighting has been tried with a fluorescent lamp. Such DC lighting can eliminate the effect of stray capacitance. However, when a fluorescent lamp is turned on with DC, "cataphoresis phenomenon or dark end effect" occurs in which mercury in the fluorescent lamp moves by an electric field, thereby causing the near-by portion of the anode to darken with time. Further, "sputter phenomenon" also occurs in which only one of the electrodes is worn. Hence, screen luminance cannot be uniform.

[0014] Therefore, the present invention can provide a fluorescent lamp lighting device having both advantages of DC lighting and the conventional high frequency light-

ing by performing low frequency lighting as described above.

[0015] When the frequency of the above-mentioned polarity switching circuit is set so that a frequency (f_1) at the start of lighting a fluorescent lamp is higher than a frequency (f) during the lighting, the fluorescent lamp can be easily turned on. Because it may be difficult to start lighting at a low frequency (f) in some cases, especially under low ambient temperature. Therefore, when the high frequency (f_1) is used only at the start of lighting, the fluorescent lamp can be easily turned on even at low temperatures. After the start of lighting, the frequency is switched to the low frequency (f), so that the fluorescent lamp lighting device is not affected by stray capacitance.

[0016] A time (T) for controlling the frequency so as to be higher than the above-mentioned predetermined frequency (f) may be regarded as a time required to start lighting a fluorescent lamp. It is desirable to set the time according to ambient temperature conditions or the like. For example, the time is set in the range of 1 second to 10 seconds.

[0017] Alternatively, an arrangement in which the frequency (f) is not switched, but in which a high-frequency voltage having a higher frequency (f_2) is superimposed on a low-frequency driving voltage may be adopted. More specifically, a high-frequency voltage superimposing circuit for superimposing a high-frequency voltage having the frequency (f_2) higher than the above-mentioned predetermined frequency (f) on the above-mentioned low-frequency driving voltage is further provided in a fluorescent lamp.

[0018] With this arrangement, a high-frequency voltage superimposed by the high-frequency voltage superimposing circuit is used to start lighting a fluorescent lamp. After the start of lighting, the fluorescent lamp can be continuously lit using the low-frequency driving voltage outputted from the polarity switching circuit. Even during the lighting, the high-frequency current superimposed by the high-frequency voltage superimposing circuit continues to flow. However, if an amplitude of the high-frequency voltage is set smaller than that of the low-frequency driving voltage, the fluorescent lamp lighting device is less affected by stray capacitance between the fluorescent lamp or wiring thereof, and the chassis, which causes no problem. Of course, the high-frequency voltage superimposing circuit may be turned off to prevent the high-frequency current from being supplied during the lighting.

[0019] As the above-mentioned high-frequency voltage superimposing circuit, a high frequency superimposing transformer having a high frequency power supply connected to a primary side thereof and being capable of taking a high-frequency voltage from a secondary side thereof may be used. The use of the high frequency superimposing transformer advantageously enables easy insulation between the fluorescent lamp and the high frequency power supply.

[0020] There may be an arrangement in which a center

tap is provided on a secondary-side winding of the above-mentioned high frequency superimposing transformer, an output line of the above-mentioned polarity switching circuit is connected to the center tap, the above-mentioned plurality of fluorescent lamps are divided into two groups, and one ends of the fluorescent lamps belonging to each group are connected to each ends of the above-mentioned secondary-side winding. This allows the high frequency superimposing transformer to be made even smaller.

[0021] There may also be an arrangement in which the secondary-side winding of the above-mentioned high frequency superimposing transformer is connected to one output line of the above-mentioned polarity switching circuit through one capacitor, and is connected to the other output line thereof through another capacitor. This allows the high frequency superimposing transformer to be smaller, and balanced lighting can also be achieved using low-frequency driving voltages in the same phase from both ends of each fluorescent lamp. Therefore, uneven luminance can be further prevented.

[0022] An LC resonant circuit having an inductor connected in series and a capacitor connected in parallel may be connected between the above-mentioned polarity switching circuit and the above-mentioned plurality of fluorescent lamps. This enables a high-frequency voltage to be produced from a low-frequency driving voltage by self-excitation.

[0023] A DC power supply circuit for producing the above-mentioned DC input may have a main transformer for converting AC voltage, and a rectifier circuit which rectifies output of the main transformer. A desired DC voltage required to turn on a fluorescent lamp can be obtained by setting a turns ratio of the main transformer.

[0024] If the above-mentioned rectifier circuit is a voltage doubler rectifier circuit, voltage can be increased with the voltage doubler rectifier circuit, so that the load of a voltage step-up ratio on the main transformer can be reduced. Thus, the turns ratio of the main transformer can be reduced, thereby advantageously miniaturizing the main transformer.

[0025] It is preferable that an AC voltage supplied to the above-mentioned main transformer is produced by an inverter which obtains a high frequency output having a predetermined frequency from DC. Thus, the high frequency generated by the inverter can be made high to advantageously improve conversion efficiency of the main transformer. In general, the higher the frequency is, the more the conversion efficiency of the transformer increases. This reduces the number of turns in the main transformer, thereby allowing the size of the main transformer to be sufficiently small.

[0026] In the conventional high-frequency AC lighting device shown in FIG. 15, as the frequency of the inverter increases, the main transformer can be miniaturized. However, the higher the frequency thereof becomes, the larger the effect of stray capacitance between the fluorescent lamp and the chassis becomes. When the stray

capacitance is larger, the wiring distance cannot be extended, thereby limiting the arrangement of fluorescent lamps in the device. Further, wattless current becomes larger, illumination of fluorescent lamps falls, and uniform display luminance becomes difficult to achieve.

[0027] Thus, the frequency of the inverter has not yet been able to be increased to a frequency suitable for the main transformer.

[0028] However, according to the present invention, a high frequency output from an inverter is once rectified to DC by a rectifier circuit, and is then supplied to a fluorescent lamp via a polarity switching circuit. That is, the frequency of the inverter and the driving frequency of the fluorescent lamp can be separately set up. Therefore, the frequency of the inverter can be made sufficiently high so that the conversion efficiency of the main transformer can be advantageously improved. This facilitates miniaturization of the main transformer, and at the same time, the driving frequency f of the fluorescent lamp can be lowered as described above. Thus, the fluorescent lamp and wiring thereof are no longer affected by stray capacitance.

[0029] These and other features, advantages and effects of the present invention will be more fully apparent from the following detailed description set forth below when taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

[0030]

FIG. 1 is a circuit diagram of a fluorescent lamp lighting device according to the present invention;

FIG. 2 is a graph showing a waveform at each part of the lighting device of FIG. 1;

FIG. 3 is a graph showing that a faster frequency f_1 is set only at a start of lighting a fluorescent lamp and is then switched to a slower frequency f after the lighting-up;

FIG. 4 is a graph showing that a faster frequency f_1 is set only at a start of lighting a fluorescent lamp and is then switched to a slower frequency f after the lighting-up;

FIG. 5 is a circuit diagram of a fluorescent lamp lighting device according to the present invention, in which a high-frequency voltage superimposing circuit is provided between a polarity switching circuit 5 and a plurality of cold cathode fluorescent tubes (71, ..., 7n);

FIG. 6A is a waveform chart showing an output voltage waveform of the polarity switching circuit 5;

FIG. 6B is a waveform chart showing an output voltage waveform obtained after a high-frequency voltage is superimposed by the high-frequency voltage superimposing circuit;

FIG. 7 is a concrete circuit diagram of a high frequency power supply 9 two transistors Q1, Q2;

FIG. 8 is a circuit diagram of a fluorescent lamp lighting device according to the present invention, comprising a high-frequency voltage superimposing circuit according to a variation;

FIG. 9 is a circuit diagram of a fluorescent lamp lighting device according to the present invention, further comprising a high-frequency voltage superimposing circuit according to another variation;

FIG. 10 is a circuit diagram of a fluorescent lamp lighting device using a self-excitation type high-frequency voltage superimposing circuit;

FIG. 11A is a chart showing an output voltage waveform of the polarity switching circuit 5;

FIG. 11B is a chart showing an output voltage waveform obtained after a high-frequency voltage is superimposed;

FIG. 12 is a circuit diagram of a major portion of a transformerless lighting device, from which a main transformer 3 is omitted;

FIG. 13 is a circuit diagram of a major portion showing an arrangement in which an inverter circuit and a transformer of a device 10 in which the lighting device is incorporated remain in use, from which an inverter circuit 2 and a main transformer 3 are omitted;

FIG. 14 is a circuit diagram showing another example of a uniform flow circuit;

FIG. 15 is a circuit diagram showing a conventional high-frequency AC lighting device; and

FIG. 16 is a graph showing a waveform at each part of the high frequency lighting device of FIG. 15.

EXPLANATION OF REFERENCE NUMERALS

[0031]

1, 1'	High-frequency voltage superimposing circuit
2	Inverter
3	Main transformer
4	Voltage doubler rectifier circuit
5	Polarity switching circuit
6	Uniform flow circuit
61, ..., 6n	Constant current circuits
71, ..., 7n	Cold cathode tubes
8	Chassis
9	High frequency power supply
10	Apparatus
12	Voltage superimposing transformer
51	Control circuit

BEST MODE FOR CARRYING OUT THE INVENTION

[0032] The embodiments of the present invention will be described in detail below while referring to the accompanying drawings.

[0033] FIG. 1 is a circuit diagram of a fluorescent lamp lighting device according to the present invention.

[0034] The lighting device includes an inverter 2 which converts DC input into AC, a main transformer 3 which increases AC voltage from the inverter 2, a voltage doubler rectifier circuit 4 which doubles and rectifies the AC voltage outputted from the main transformer 3, a polarity switching circuit 5 which switches the polarity of the rectified DC voltage, a plurality (n pieces) of cold cathode fluorescent tubes 71, ..., 7n connected to output lines (e), (f) of the polarity switching circuit 5, and a uniform flow circuit 6 connected to the other ends of the above-mentioned plurality of cold cathode fluorescent tubes (71, ..., 7n), for supplying identical current to the respective cold cathode fluorescent tubes (71, ..., 7n).

[0035] The operation of the device will be described. A DC input is first converted into an AC frequency suitable for the main transformer 3 by switching a switching transistor included in an inverter 2. The "AC frequency suitable for the main transformer 3" refers to a frequency at which the main transformer 3 can obtain sufficient conversion efficiency, that is usually in the range of several tens of kHz to several hundreds of kHz. When the frequency is excessively lower than this range, the main transformer 3 needs to be increased in size, and thus the entire device becomes larger and heavier. When the frequency is higher than this range, the effect of parallel capacitance generated within the main transformer 3 is increased, so that resonance occurs to reduce the conversion efficiency.

[0036] Next, the above-mentioned AC voltage is increased at a predetermined voltage step-up ratio by the main transformer 3 having a predetermined number of turns and a number of turns ratio. Further, the voltage doubler rectifier circuit 4 performs rectification and voltage step-up. As a result of this, DC voltage required to turn on the cold cathode fluorescent tubes (71, ..., 7n) can be obtained. The "DC voltage required to turn on the cold cathode fluorescent tubes (71, ..., 7n)" is on the order of 1000 V to 2000 V.

[0037] The DC voltage is converted into AC voltage by turning on/off the switching transistor in the polarity switching circuit 5. A control circuit 51 is used to control the turning on/off of the switching transistor. The control circuit 51 supplies an on/off signal to a gate of each switching transistor, thereby controlling the turning on/off of each of the switching transistors.

[0038] A frequency f of the on/off signal may be in the range of greater than 0 Hz but not greater than 20 kHz. The frequency f is preferably greater than 0 Hz but not greater than 10 kHz, or more preferably greater than 0 Hz but not greater than 1 kHz.

[0039] The uniform flow circuit 6 includes electronic circuits (referred to as constant current circuits) 61, ..., 6n which obtain constant current by using electric current flowing through between a collector and an emitter of a transistor, and the number of the electronic circuits correspond to the number of the cold cathode fluorescent tubes (71, ..., 7n). The cold cathode fluorescent tubes (71, ..., 7n) and the constant current circuits (61, ..., 6n)

are connected in series to each other, respectively. The series circuits (the number thereof is equal to the number of cold cathode fluorescent tubes (71, ..., 7n)) of the cold cathode fluorescent tubes (71, ..., 7n) and the constant current circuits (61, ..., 6n) are connected to two output lines (e), (f) of the polarity switching circuit 5.

[0040] As shown in FIG. 1, transistors of NPN type and PNP type are connected in parallel with each other in the respective constant current circuits (61, ..., 6n). As for the NPN transistor, a resistance R is connected between an emitter and a ground. A base of the transistor is commonly connected to the respective transistors. A voltage at this common base is indicated as Vb. Since a voltage across the resistance R is substantially equal to the voltage Vb, and the voltage Vb is commonly applied to the respective transistors, the voltage across the resistance R is substantially equal in each of the constant current circuits (61, ..., 6n). Therefore, electric currents flowing through the respective constant current circuits in one direction (a direction from the collector to the emitter of the NPN transistor) are substantially equal, whereby constant current can be obtained. Electric currents flowing in the reverse direction (a direction from the emitter to the collector of the PNP transistor) are also equalized by the same action of the PNP transistor, whereby constant current can be obtained.

[0041] Thus, constant current is realized using the constant current circuits (61, ..., 6n) having the transistors, so that miniaturization and weight reduction can be realized as compared to the uniform flow circuit 601 conventionally utilizing a plurality of transformers.

[0042] FIG. 2 is a graph showing a waveform at each part of the lighting device of FIG. 1.

[0043] FIG. 2 (a) shows a DC input voltage to be inputted into the inverter 2, FIG. 2 (b) shows a waveform of an output voltage of the inverter 2, that is, a primary-side voltage of the main transformer, and FIG. 2 (c) shows a waveform of a secondary-side voltage of the main transformer 3. The secondary-side voltage of the main transformer 3 is increased with respect to the primary-side voltage thereof, according to the number of turns ratio of the main transformer 3. FIG. 2 (d) shows an output waveform of the voltage doubler rectifier circuit 4. This output voltage is more increased than the secondary-side voltage of the main transformer 3, and is also rectified into pulsating current. FIG. 2 (e) shows an output waveform of the polarity switching circuit 5. A DC output rectified into the pulsating current is switched by the polarity switching circuit 5 so as to alternately become positive and negative. The output current thus switched is supplied to the respective cold cathode fluorescent tubes (71, ..., 7n) through the uniform flow circuit 6.

[0044] It is preferable that the switching frequency of the polarity switching circuit 5 is set faster only at the start of lighting, and the details thereof will be described below. Although it has been described above that the switching frequency f of the polarity switching circuit 5 is in the range of greater than 0 Hz but not greater than 20 kHz,

it may be set to an even faster frequency f_1 ($f_1 \geq f$) only at the start of lighting, and then switched to a slower frequency f within the range of greater than 0 Hz but not greater than 20 kHz after the lighting-up.

[0045] FIG. 2 (e) shows an example in which an on/off frequency of the control circuit 51 is set to the faster frequency f_1 only at the start of lighting, and is then switched to the slower frequency f after the lighting-up.

[0046] FIG. 3 is a graph showing a relationship between time and frequency, in which the frequency is set to the faster frequency f_1 only at a start of lighting and is then switched to the slow frequency f after a predetermined period of time T passes. For example, at the start of lighting, $f_1 = 1$ kHz, and after the lighting-up, $f = 120$ Hz which is slower than the above. The predetermined time T is set according to ambient temperature, and is set in the range of about 1 second to 10 seconds at normal temperature. Under low temperature environment in which lighting hardly starts, the predetermined time T is set longer, while under high temperature environment, the predetermined time T is set shorter.

[0047] As shown in FIG. 4, the faster frequency f_1 may be set only at the start of lighting, and as the predetermined time T passes from the start, the frequency f_1 may be gradually lowered to converge on the frequency f . The setting range of the predetermined time T is generally the same as that in the case of FIG. 3.

[0048] In this manner, when the switching frequency of the polarity switching circuit 5 is increased only at the start of lighting, the start of lighting of the cold cathode fluorescent tubes (71, ..., 7n) can be facilitated using stray capacitance between the cold cathode fluorescent tubes (71, ..., 7n) and wiring connected thereto, and a chassis 8 that supports the cold cathode fluorescent tubes (71, ..., 7n). In particular, it is advantageous to start thereof at low temperatures.

[0049] With the above-described arrangement of the lighting device according to the present invention, the plurality of cold cathode fluorescent tubes (71, ..., 7n) can be turned on at a low frequency with the frequency f .

[0050] Here, "turned on at a low frequency" means that lighting-up is performed while DC is switched at the frequency f lower than conventional frequencies by the polarity switching circuit 5. As described above, the low frequency f is in the range of greater than 0 Hz but not greater than 20 kHz. Preferably, it ranges greater than 0 Hz but not greater than 10 kHz, or more preferably greater than 0 Hz but not greater than 1 kHz.

[0051] Conventionally, the cold cathode fluorescent tubes (71, ..., 7n) have been turned on by supplying high frequency current of several tens kHz thereto with the inverter 2. As compared to this, the cold cathode fluorescent tubes (71, ..., 7n) are now turned on with electric current having such low frequency f . Therefore, the effect of stray capacitance generated between the cold cathode fluorescent tubes (71, ..., 7n) and the chassis 8 and between wiring connected thereto and the chassis 8 can be reduced, thereby enabling uniformity of screen lumi-

nance to be nearly ideal. The lower the switching frequency f of the polarity switching circuit 5 becomes, the more advantageous the effect of maintaining the uniformity of screen luminance becomes.

[0052] Further, since the main transformer 3 can be driven at a high frequency independent of the switching frequency f of the polarity switching circuit 5, the size of the main transformer 3 can be made smaller.

[0053] Therefore, if the lighting device of the present invention is used for backlights of liquid crystal display devices, the entire lighting device can be minimized. Moreover, since the effect of stray capacitance can be reduced by turning on the cold cathode fluorescent tubes with a low frequency, the cold cathode fluorescent tubes and the chassis supporting them can be brought as close as possible to each other without limitation. Thus, the thickness of the liquid crystal display device can be made thinner.

[0054] Next will be described an example of a circuit provided with a high-frequency voltage superimposing circuit for superimposing a high-frequency voltage having a frequency f_2 higher than the above-mentioned low frequency f on the above-mentioned low-frequency driving voltage.

[0055] In the case of superimposing the voltage having the frequency f_2 , not only at the start of lighting but for the entire period during the lighting, the voltage having the frequency f_2 is superimposed on the voltage having the frequency f .

[0056] FIG. 5 is a circuit diagram of a fluorescent lamp lighting device in which an inductor L1, a capacitor C1, a high frequency power supply 9, and a voltage superimposing transformer 12 are provided between the polarity switching circuit 5 and the plurality of cold cathode fluorescent tubes (71, ..., 7n). These inductor L1, capacitor C1, high frequency power supply 9, and voltage superimposing transformer 12 form a high-frequency voltage superimposing circuit 1.

[0057] The inductor L1 is connected in series between the polarity switching circuit 5 and the cold cathode tubes (71, ..., 7n), and prevents backflow of a superimposed high frequency current into the polarity switching circuit 5. The capacitor C1 is provided in order to prevent short circuit of a low-frequency driving voltage. The voltage superimposing transformer 12 is composed of a primary-side winding T1 and a secondary-side winding T2, and the high frequency power supply 9 is connected to the primary-side winding T1. A frequency of the high frequency power supply 9 is indicated as " f_2 ." It is preferable that the frequency f_2 is set to a frequency on the order of 5 to 50 times of the above-mentioned low frequency f . For example, if $f = 100$ Hz, $f_2 = 500$ Hz to 5 kHz.

[0058] FIG. 6A shows an output voltage waveform of the polarity switching circuit 5. This waveform, which is the same as in FIG. 2 (e), is drawn without the pulsating current omitted. FIG. 6B shows an output voltage waveform in which a high-frequency voltage having the frequency f_2 is superimposed by the high-frequency voltage

superimposing circuit 1.

[0059] Here, the relationship of amplitude will be described. When an amplitude of the output voltage of the polarity switching circuit 5 is referred to as "a" and an amplitude of the high-frequency voltage of the frequency f2 is referred to as "b", b is desirably on the order of 0.1 to 0.5 times of a. If b is lower than 0.1 times of a, the effect of superimposing the high frequency f2 becomes small, leading to generally the same result as the case of starting lighting only at the low frequency f. If b is higher than 0.5 times of a, the stray capacitance affects to increase power loss during the lighting.

[0060] Thus, since a high-frequency voltage of the frequency f2 is superimposed on the output voltage waveform of the polarity switching circuit 5, the high-frequency voltage is used to turn on the cold cathode fluorescent tubes (71, ..., 7n), so that the lighting can be readily started. Especially, it is effective at the start of lighting at low temperatures.

[0061] Further, the brightness of the cold cathode fluorescent tubes (71, ..., 7n) during the lighting can be controlled by adjusting the amplitude "b" of the high-frequency voltage of the frequency f2. The larger the "b" is, the brighter the cold cathode fluorescent tubes (71, ..., 7n) can be. Conversely, the smaller the "b" is, the darker they can be.

[0062] The brightness of the cold cathode fluorescent tubes (71, ..., 7n) during the lighting can be adjusted by selecting a value of the frequency f2. That is, when there is a leakage magnetic flux from the secondary-side winding T2 in the voltage superimposing transformer 12, higher frequency f2 allows the cold cathode fluorescent tubes (71, ..., 7n) to be dimmed, and lower frequency f2 allows the cold cathode fluorescent tubes (71, ..., 7n) to be brighter. Conversely, when there is no leak magnetic flux from the secondary-side winding T2, higher frequency f2 allows the cold cathode fluorescent tubes (71, ..., 7n) to be brighter, and lower frequency f2 allows the cold cathode fluorescent tubes (71, ..., 7n) to be dimmed.

[0063] FIG. 7 is a concrete circuit diagram of the high frequency power supply 9 described above. The high frequency power supply 9 is a circuit connecting two transistors Q1, Q2 in series, in which an AC switching circuit 11 which alternately outputs high/low voltages at the frequency f2 is connected to the base of the transistors Q1, Q2. While the AC switching circuit 11 is outputting a high voltage, the transistor Q1 is turned on and an electric current is charged to the primary-side coil T1 through a capacitor C3 from the power supply F. While the AC switching circuit 11 is outputting a low voltage, the transistor Q2 is turned on and the electric current thus charged to the primary-side coil T1 discharges the capacitor C3. Thus, a high frequency current can be supplied to the primary-side coil T1.

[0064] Next, variations of the high-frequency voltage superimposing circuit 1 will be described with FIG. 8 and FIG. 9.

[0065] In FIG. 8, a transformer having the secondary-

side winding T2 with a tap is used as the voltage superimposing transformer 12, and the output line (e) from the polarity switching circuit 5 is connected to this tap. FIG. 8 is a diagram showing the following circuit arrangement: A plurality of cold cathode fluorescent tubes are divided into two groups, the cold cathode fluorescent tubes in each group are connected in parallel, and the connecting ends of the cold cathode fluorescent tubes are connected to both ends (g), (h) of the secondary-side winding T2, respectively. Cold cathode fluorescent tubes in one group are denoted as 71, 72, ..., and those in the other group are denoted as 81, 82, As in FIG. 5, the high frequency power supply 9 is connected to the primary-side coil T1 in the voltage superimposing transformer 12. The frequency f2 of the high frequency power supply 9 is set to a frequency on the order of 5 to 50 times of the above-mentioned low frequency f.

[0066] With this circuit, a low-frequency voltage outputted from the polarity switching circuit 5 is divided at the tap and is then applied from both the ends (g), (h) of the secondary-side winding T2 to the respective groups, thereby turning on and driving the cold cathode fluorescent tubes in each group. The low-frequency voltage outputted from the polarity switching circuit 5 is in the same phase at both the ends (g), (h) of the secondary-side winding T2. A high-frequency voltage outputted from the high frequency power supply 9 is applied in the opposite phase from both the ends of the secondary-side winding T2 to start lighting the cold cathode fluorescent tubes in each group. That is, the high-frequency voltage outputted from the high frequency power supply 9 is in the opposite phase at both the ends (g), (h) of the secondary-side winding T2. With this circuit, the capacitor C1 as seen in the circuit of FIG. 5 is no longer required. Further, since the secondary-side winding T2 having the center tap is used, the DC magnetization of the core is offset. Therefore, core saturation is less likely to occur, thereby allowing to use a small transformer with a small core.

[0067] FIG. 9 shows an example of a circuit in which the capacitors C1, C2 connected in series with each other are connected to output lines from the polarity switching circuit 5, the voltage superimposing transformer 12 is connected to an intermediate junction (i) between the capacitors C1 and C2, and a high-frequency voltage for the start of lighting is supplied from the voltage superimposing transformer 12 to the cold cathode fluorescent tubes (71, ..., 7n) through the capacitors C1, C2.

[0068] With this circuit, the high-frequency voltage outputted from the high frequency power supply 9 is applied from the secondary-side winding T2 through the capacitors C1, C2 to start lighting each of the cold cathode fluorescent tubes (71, ..., 7n). The values of the capacitors C1, C2 may be equal to ($C1=C2$), or different from ($C1 \neq C2$) each other. Or, either of the capacitors C1, C2 may be absent, that is, the capacitor C1 or C2 may be short-circuited.

[0069] In this circuit, in particular when the values of the capacitors C1, C2 are close, high-frequency voltages

appear on both the lines (e), (h) on the output side of the polarity switching circuit 5, and these high-frequency voltages have generally the same potential and the same phase. That is, the high-frequency voltage having generally the same potential and the same phase can be applied from both ends of each cold cathode fluorescent tube, and all these high frequency currents flow into the chassis 8 via the cold cathode fluorescent tubes (71, ..., 7n) and wiring connected thereto. Therefore, uneven luminance which may occur in the cold cathode fluorescent tubes (71, ..., 7n) can be prevented. Further, a small transformer having a small core can be used.

[0070] Next, another example of a circuit in the lighting device of the present invention will be described. The high-frequency voltage superimposing circuit 1 described in FIGS. 5 to 9 is a separate-excitation type circuit using the high frequency power supply 9. However, a self-excitation type circuit in which a resonant circuit having a capacitor C and an inductor L is provided on both lines (e), (f) on the output side of the polarity switching circuit 5 to oscillate a high-frequency voltage can also be adopted.

[0071] FIG. 10 is a circuit diagram of a fluorescent lamp lighting device using a self-excitation type high-frequency voltage superimposing circuit 1'.

[0072] The inductor L is connected in series between the polarity switching circuit 5 and the cold cathode fluorescent tubes (71, ..., 7n), and the capacitor C is connected in parallel to the ground. Since a voltage is oscillated with the inductor L and the capacitor C, the relationship between L and C should satisfy $2\pi f_2 = 1/\sqrt{LC}$.

[0073] FIG. 11A shows an output voltage waveform of the polarity switching circuit 5 in the case where an LC resonant circuit does not exist. This waveform is the same as that in FIG. 6A. FIG. 11B shows an output voltage waveform obtained after a high-frequency voltage is superimposed by the LC resonant circuit. The LC resonant circuit causes an oscillation output to appear at the time when an low frequency output waveform is switched from negative to positive or from positive to negative, and the oscillation output gradually decreases.

[0074] Thus, since the high-frequency voltage is superimposed on the output voltage waveform of the polarity switching circuit 5, the high-frequency voltage is used to turn on the cold cathode fluorescent tubes (71, ..., 7n), so that the lighting can be readily started.

[0075] The above-mentioned capacitor C may be constituted by a stray capacitance between the cold cathode fluorescent tubes (71, ..., 7n) and the chassis 8 and between wiring connected thereto and the chassis 8.

[0076] A variation of a circuit in the fluorescent lamp lighting device will be described below.

[0077] FIG. 12 is a circuit diagram of a major portion of a transformerless lighting device from which a main transformer 3 is omitted. In this circuit, AC voltage is directly obtained by a resonant circuit 2' using a coil and a transistor, with respect to DC input. The AC voltage obtained by the resonant circuit is on the order of 10 times

of the DC input, for example, 240 V, and has a frequency of 200 kHz. A predetermined voltage, for example a DC voltage of 1500 V, is obtained by letting the AC voltage pass through the voltage doubler rectifier circuit 4. An arrangement subsequent to the voltage doubler rectifier circuit 4 is the same as those shown in FIGS. 1 to 10.

[0078] With this arrangement, the main transformer 3 can be eliminated, so that the lighting device can be further miniaturized.

[0079] FIG. 13 is a circuit diagram of a major portion showing an arrangement in which an inverter circuit 2 and a main transformer 3 of an apparatus (e.g., television receiver) 10, in which the lighting device is incorporated, are used and the above-described inverter circuit 2 and main transformer 3 are omitted. An AC voltage is obtained from a secondary-side winding of a power supply transformer in the apparatus 10, and then passes through the voltage doubler rectifier circuit 4 to perform voltage step-up and rectification. With this arrangement, the inverter 2 and the transformer both dedicated to the cold cathode fluorescent tubes (71, ..., 7n) are no longer required. Thus, the entire device can be minimized.

[0080] FIG. 14 is a circuit diagram showing another example of a uniform flow circuit. In the uniform flow circuit, a circuit 6a sending constant current in one direction and a circuit 6b sending it in the other direction are separated from each other and installed on respective sides of the cold cathode fluorescent tubes. The operation of the constant current circuits 6a, 6b is the same as that described using FIG. 1. This uniform flow circuit allows use of NPN transistors only, which is advantageously cost effective.

[0081] While the invention has been described with reference to the embodiments, the description is of course not to be construed as limiting the embodiments of the invention as described above. For example, with respect to the output of the main transformer 3, rectification and voltage step-up have been performed by the voltage doubler rectifier circuit 4. However, such voltage step-up and rectification are separated, so that voltage step-up may be performed by the main transformer, and rectification may be performed by a simple rectifier circuit which does not double voltage. Further, the example of the uniform flow circuit is not particularly limited to those shown in FIG. 1 and FIG. 14, and any constant current circuit using a transistor may be used. The present invention is not limited to the cold cathode fluorescent tube used in the above-mentioned embodiments, and can be applied to general fluorescent lamps.

Claims

1. A fluorescent lamp lighting device comprising:

a polarity switching circuit that outputs a low-frequency driving voltage of a predetermined frequency (f) by switching a polarity at the pre-

- determined frequency (f) on DC input;
 a plurality of fluorescent lamps of which one ends are connected to an output line of the polarity switching circuit; and
 a uniform flow circuit formed by a constant current circuit each of which connected to the other ends of the plurality of fluorescent lamps and having a transistor for supplying identical current to the respective fluorescent lamps.
2. The fluorescent lamp lighting device according to claim 1, wherein the frequency (f) of the polarity switching circuit is greater than 0 Hz but not greater than 10 kHz.
 3. The fluorescent lamp lighting device according to claim 2, wherein the frequency (f) of the polarity switching circuit is greater than 0 Hz but not greater than 1 kHz.
 4. The fluorescent lamp lighting device according to claim 1, further comprising a control circuit that controls the frequency (f) of the polarity switching circuit, wherein the control circuit controls a frequency at a start of lighting a fluorescent lamp such that the frequency is higher than the predetermined frequency (f).
 5. The fluorescent lamp lighting device according to claim 4, wherein a time (T), required for the control circuit to control the frequency such that the frequency is higher than the predetermined frequency (f), is in a range of 1 second to 10 seconds.
 6. The fluorescent lamp lighting device according to claim 1, further comprising a high-frequency voltage superimposing circuit for superimposing a high-frequency voltage having a frequency (f2) higher than the predetermined frequency (f) on the low-frequency driving voltage having the frequency (f).
 7. The fluorescent lamp lighting device according to claim 6, wherein the high-frequency voltage superimposing circuit comprises a high frequency superimposing transformer having a high frequency power supply with a frequency (f2) connected to a primary side thereof and taking a high-frequency voltage from a secondary side thereof.
 8. The fluorescent lamp lighting device according to claim 7, wherein a center tap is provided on secondary-side winding of the high frequency superimposing transformer, an output line of the polarity switching circuit is connected to the center tap, the plurality of fluorescent lamps are divided into two groups, and one ends of the fluorescent lamps belonging to each of the groups are connected to each of both ends of the secondary-side winding.
 9. The fluorescent lamp lighting device according to claim 7, wherein the secondary-side winding of the high frequency superimposing transformer is connected to one output line of the polarity switching circuit through one capacitor, and is connected to the other output line of the polarity switching circuit through another capacitor.
 10. The fluorescent lamp lighting device according to claim 1, wherein an LC resonant circuit having an inductor connected in series and a capacitor connected in parallel is connected between the polarity switching circuit and the plurality of fluorescent lamps, and a resonant frequency of the LC resonant circuit is higher than the predetermined frequency (f).
 11. The fluorescent lamp lighting device according to claim 1, further comprising a DC power supply circuit for producing the DC input, wherein the DC power supply circuit has a main transformer for converting an AC voltage, and a voltage doubler rectifier circuit which rectifies output of the main transformer, and an AC voltage supplied to the main transformer is produced by an inverter for obtaining a high frequency output from DC.

FIG. 1

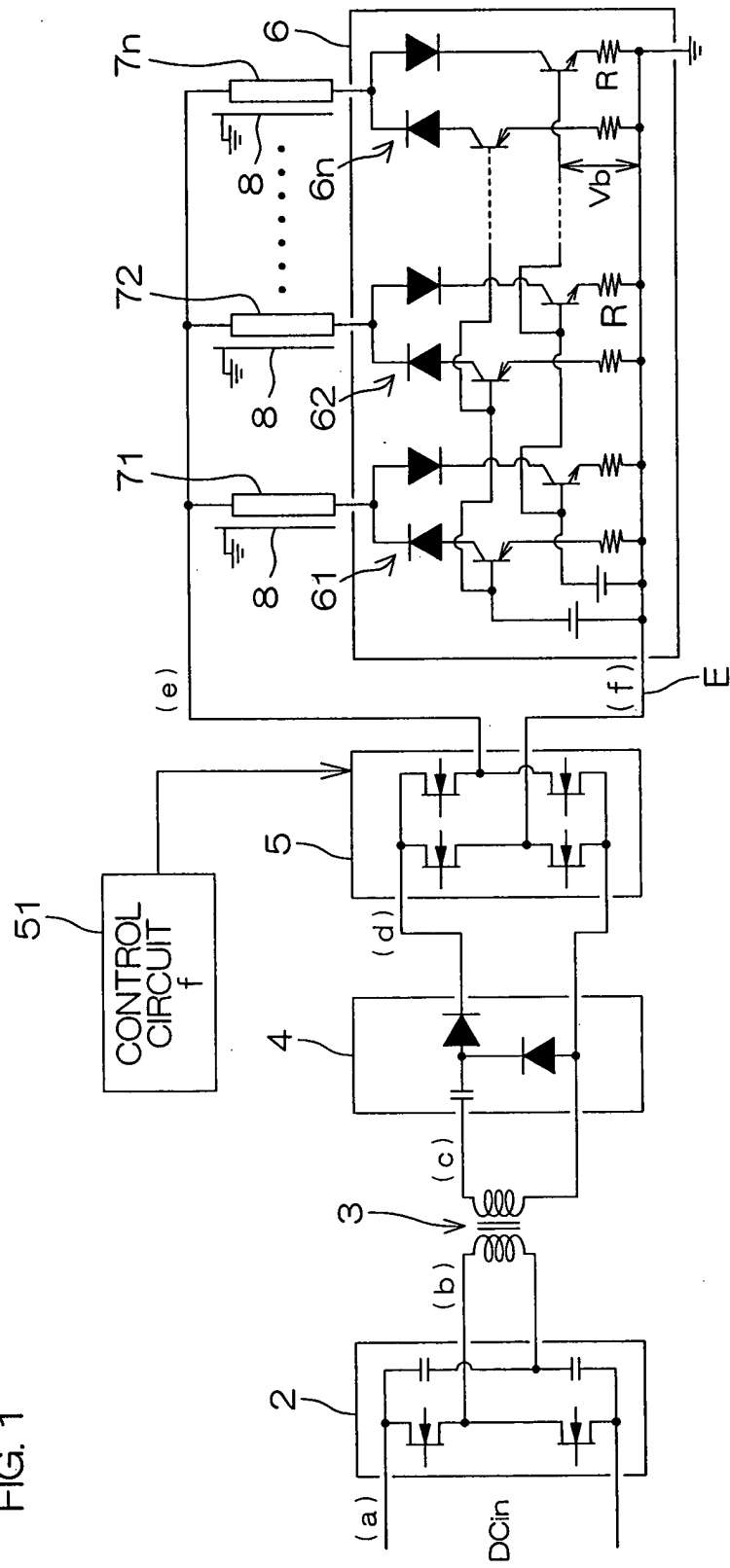


FIG. 2

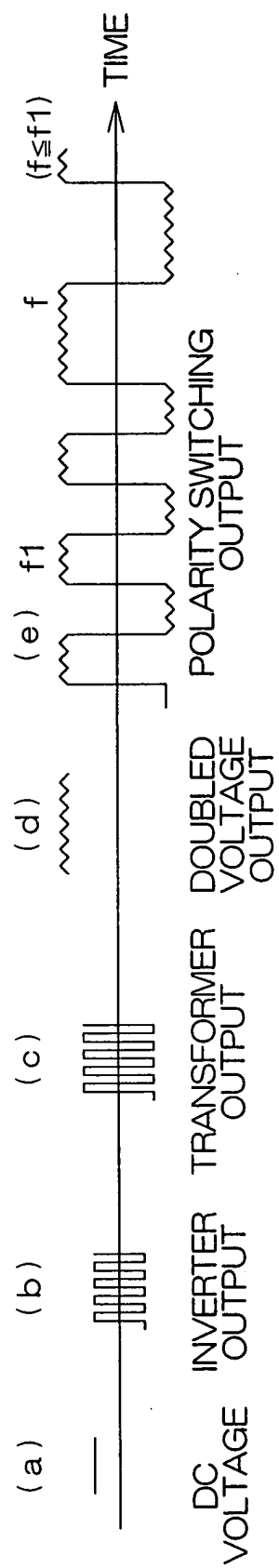


FIG. 3

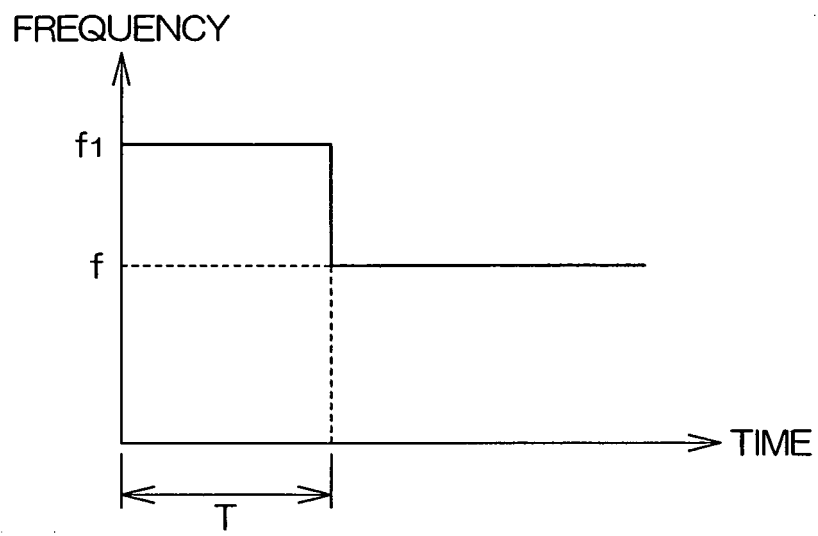


FIG. 4

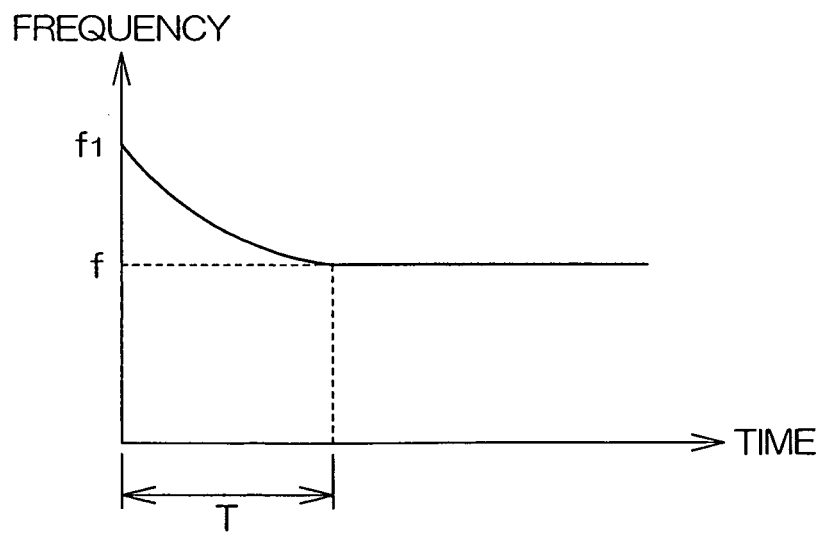


FIG. 5

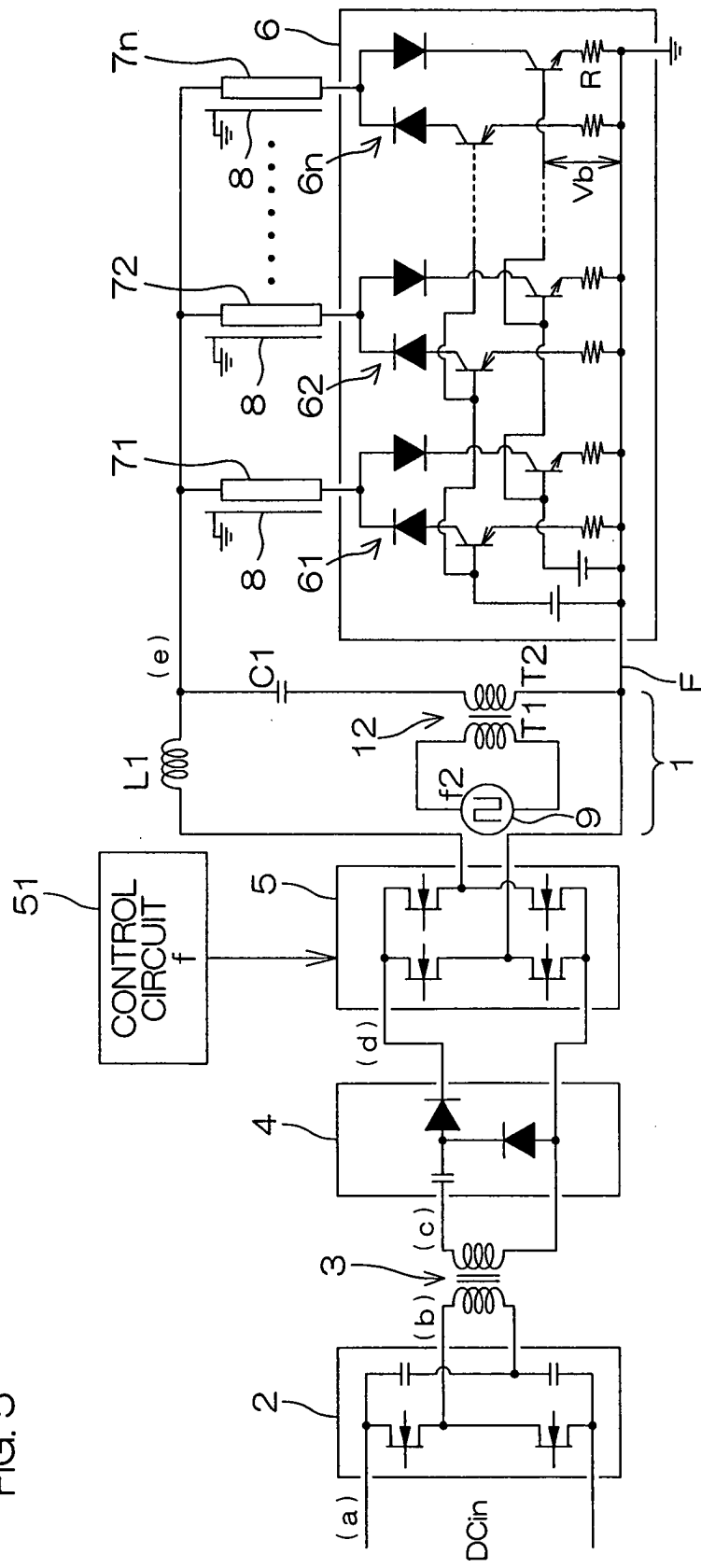


FIG. 6A

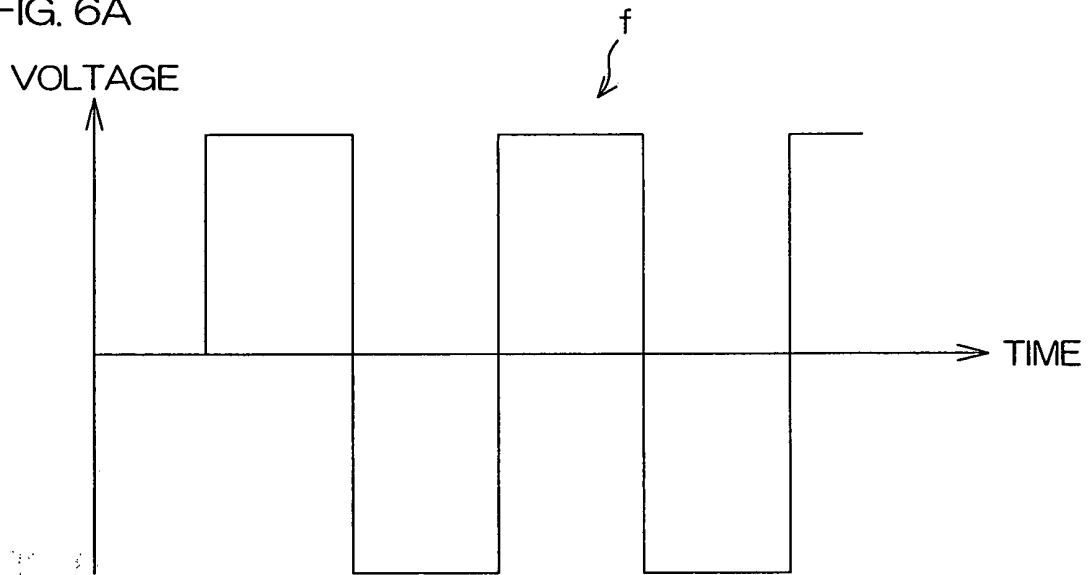


FIG. 6B

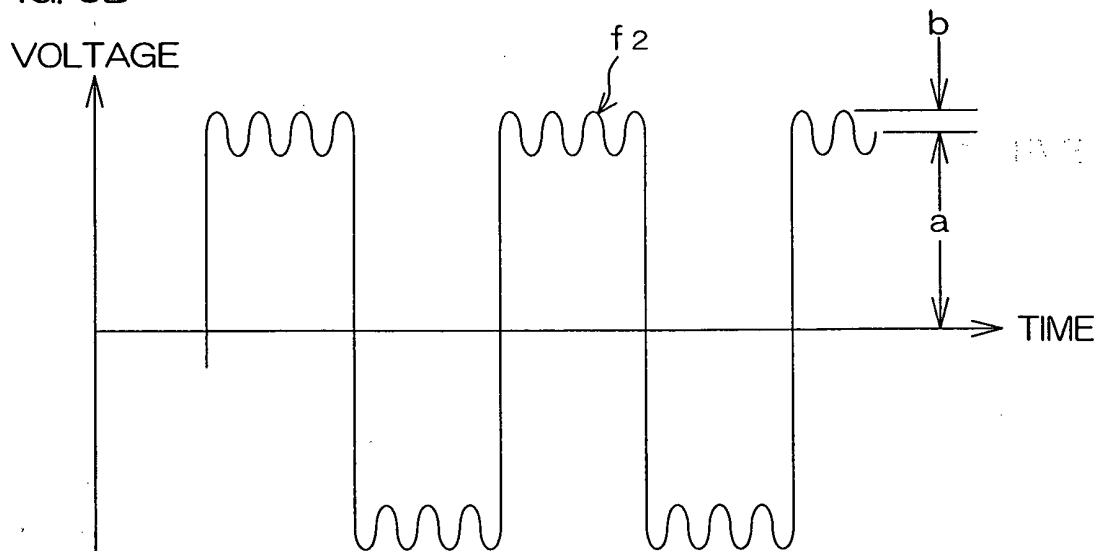
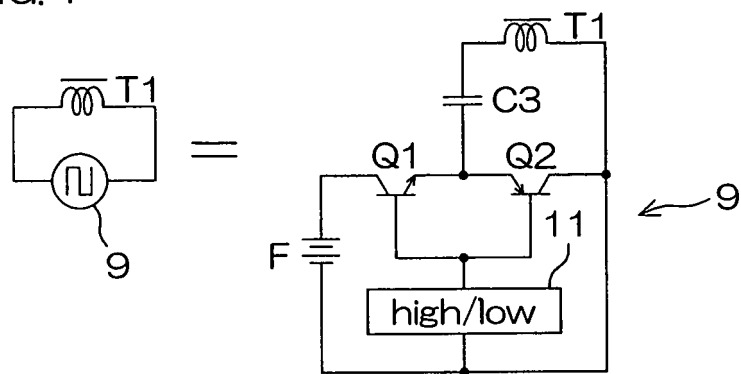


FIG. 7



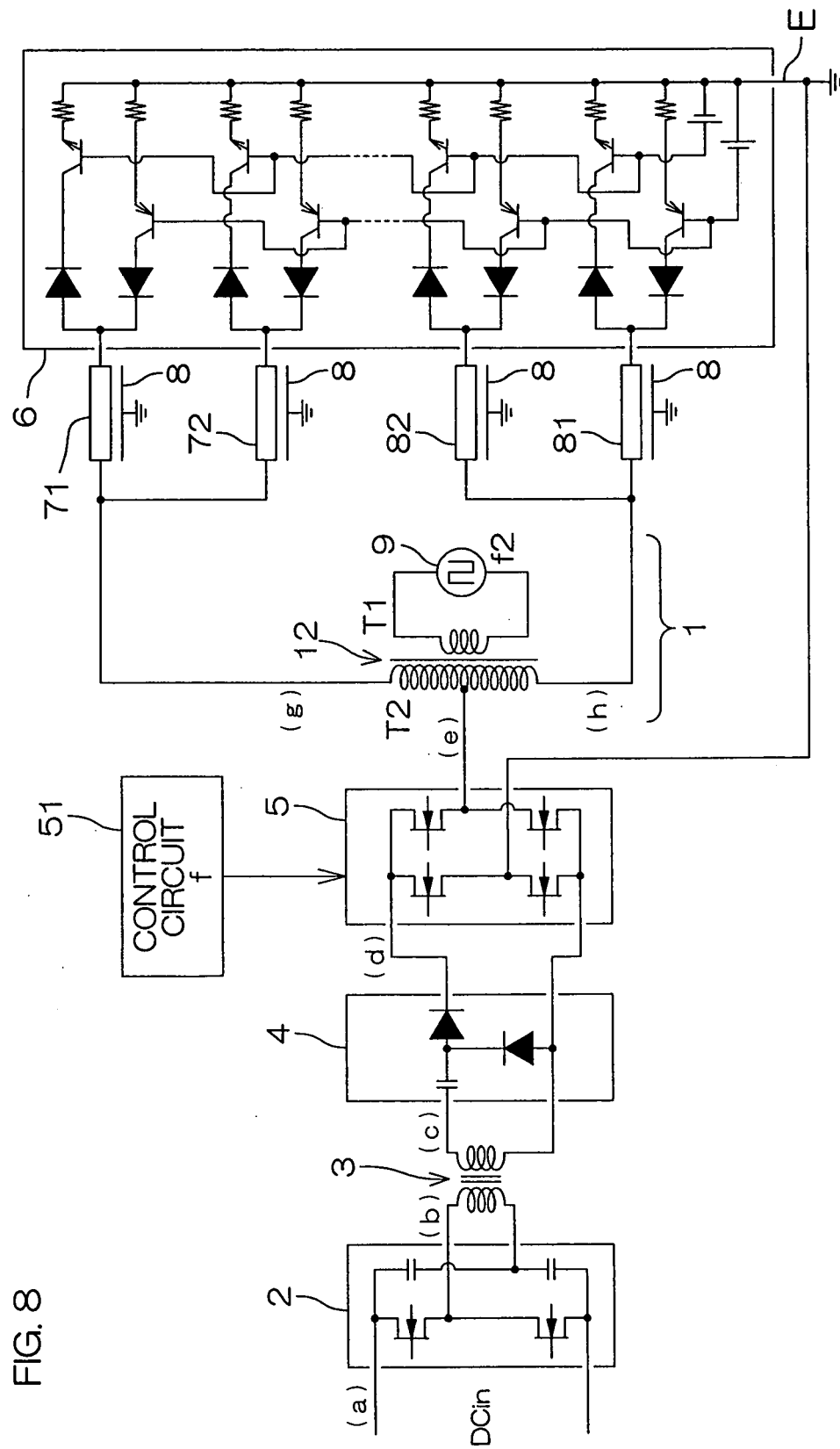


FIG. 9

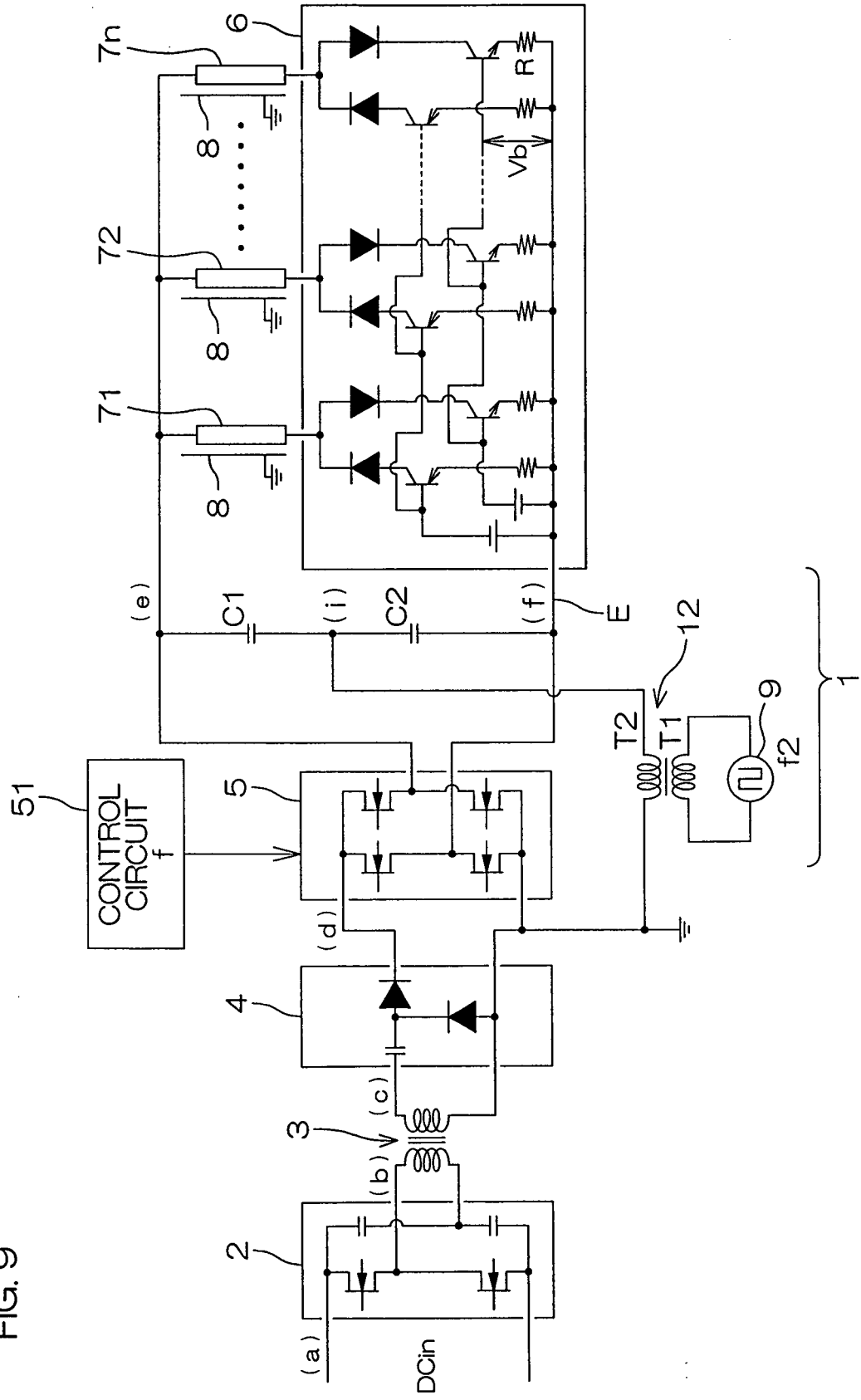


FIG. 10

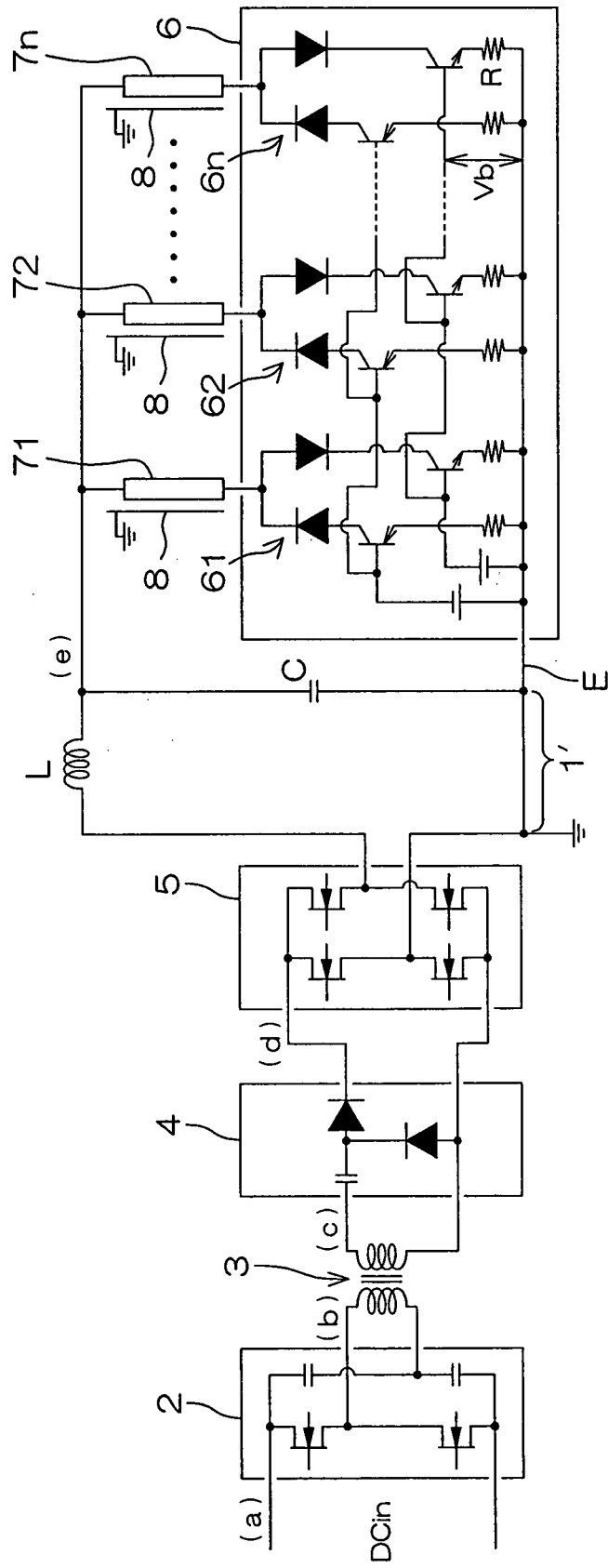


FIG. 11A

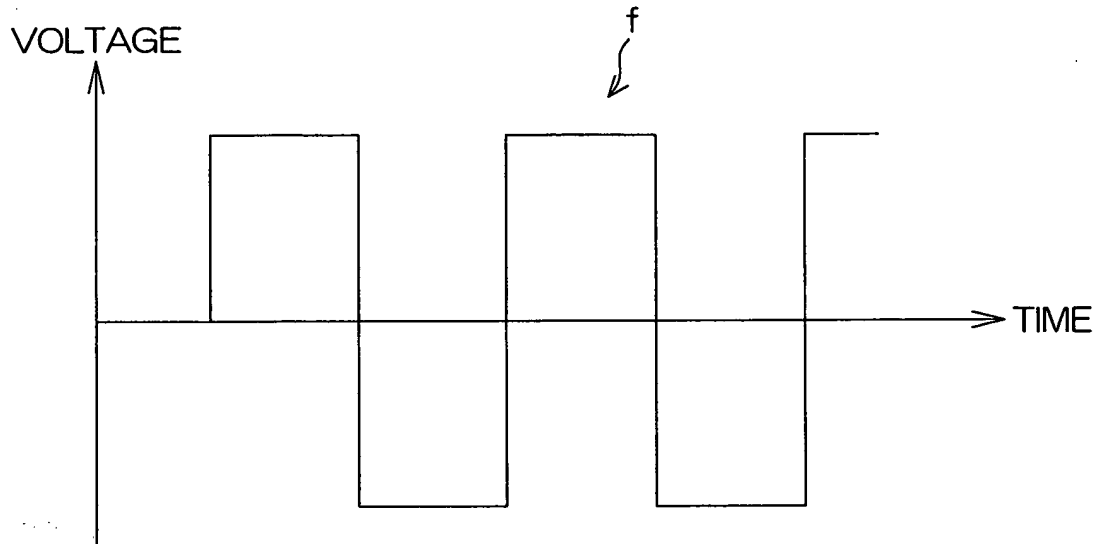


FIG. 11B

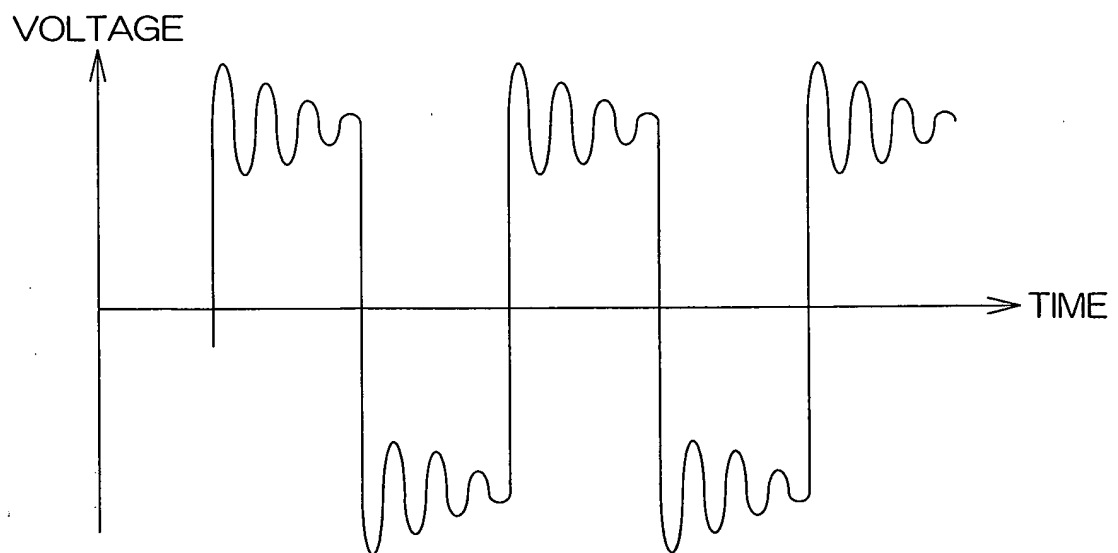


FIG. 12

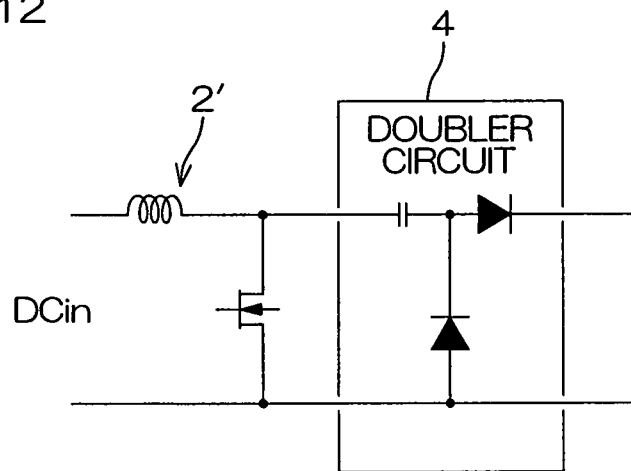


FIG. 13

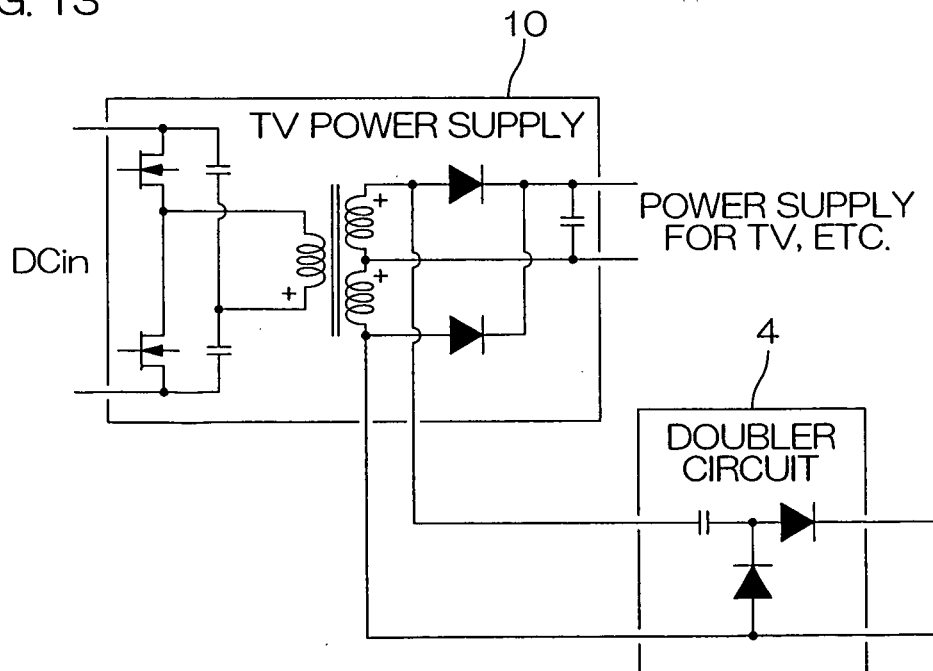


FIG. 14

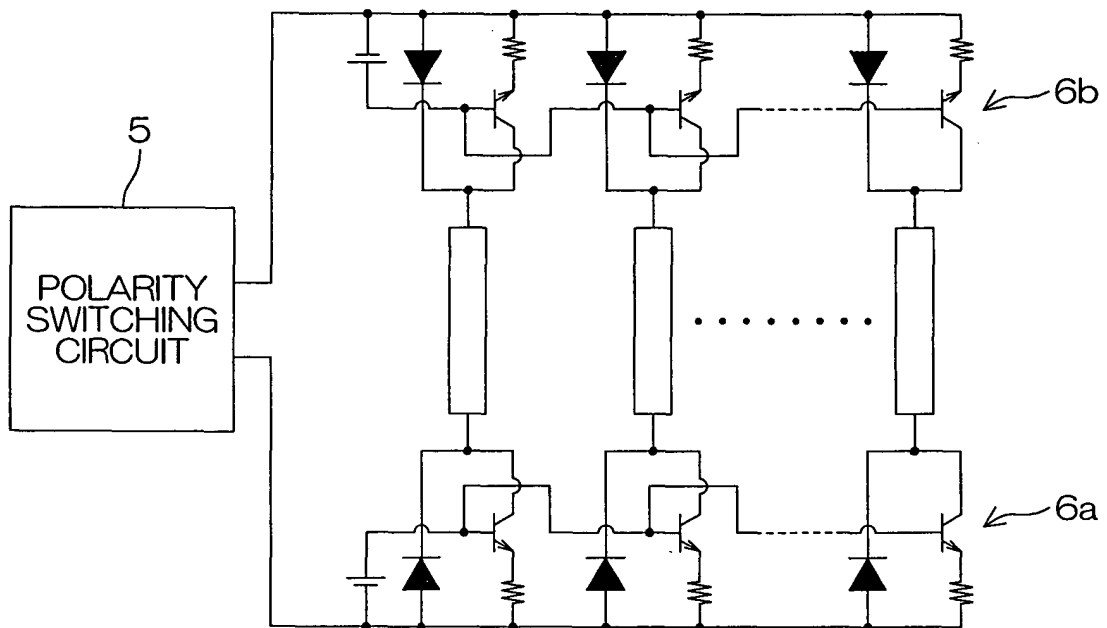


FIG. 15

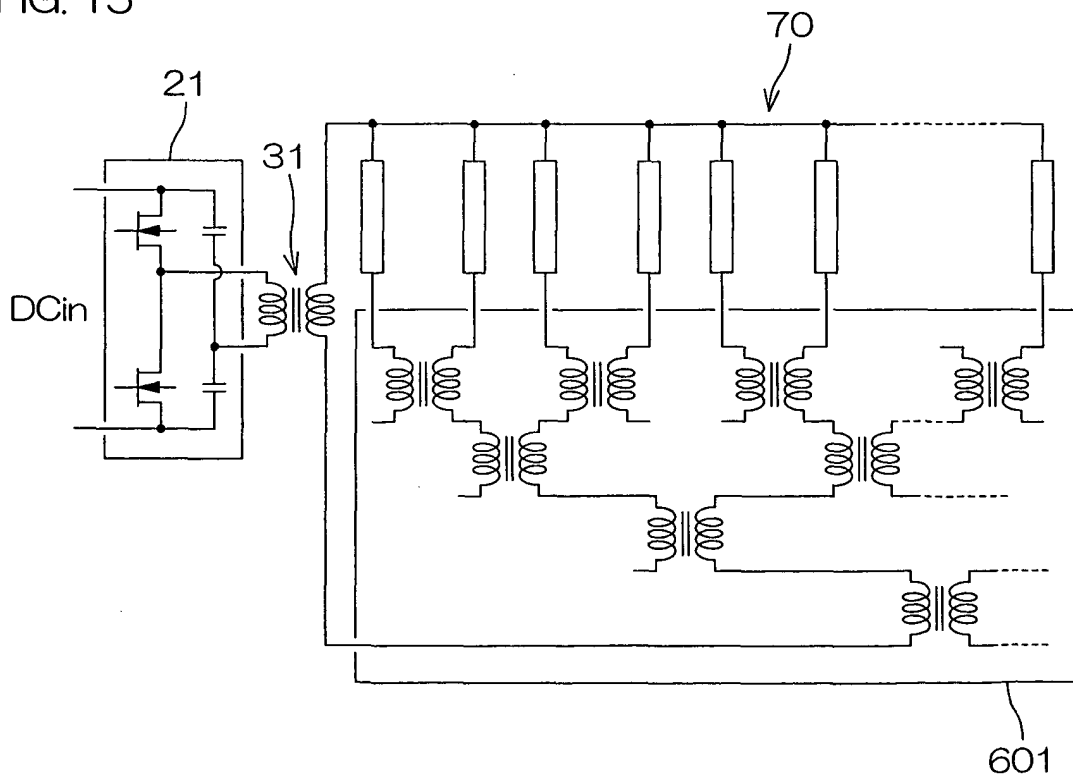
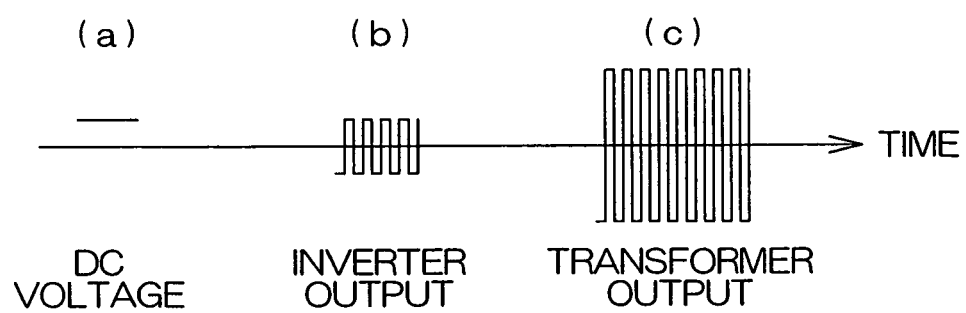


FIG. 16



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2006/322389

A. CLASSIFICATION OF SUBJECT MATTER

H05B41/282(2006.01) i, H05B41/24(2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H05B41/282, H05B41/24

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Jitsuyo Shinan Koho	1922-1996	Jitsuyo Shinan Toroku Koho	1996-2007
Kokai Jitsuyo Shinan Koho	1971-2007	Toroku Jitsuyo Shinan Koho	1994-2007

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y A	JP 2004-71226 A (Hitachi Media Electronics Co., Ltd.), 04 March, 2004 (04.03.04), Full text; all drawings (Family: none)	1-7, 10, 11 8, 9
Y A	JP 5-13182 A (Matsushita Electric Industrial Co., Ltd.), 22 January, 1993 (22.01.93), Par. Nos. [0009] to [0011]; all drawings (Family: none)	1-7, 10, 11 8, 9
Y A	JP 2-288197 A (Kabushiki Kaisha Shinwa Giken), 28 November, 1990 (28.11.90), Page 2, lower left column, line 1 to page 4, lower left column, line 6; all drawings (Family: none)	1-7, 10, 11 8, 9

☒ Further documents are listed in the continuation of Box C.☐ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search
30 January, 2007 (30.01.07)Date of mailing of the international search report
06 February, 2007 (06.02.07)Name and mailing address of the ISA/
Japanese Patent Office

Authorized officer

Facsimile No.

Telephone No.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2006/322389

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP 4-73894 A (Hitachi Shomei Kabushiki Kaisha), 09 March, 1992 (09.03.92), Page 1, lower right column, line 10 to page 2, upper left column, line 13; Figs. 10 to 14 (Family: none)	4, 5
Y	JP 2001-345196 A (Mitsubishi Electric Corp.), 14 December, 2001 (14.12.01), Par. Nos. [0015] to [0020]; Figs. 1, 2 (Family: none)	10

Form PCT/ISA/210 (continuation of second sheet) (April 2005)

REFERENCES CITED IN THE DESCRIPTION

This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.

Patent documents cited in the description

- JP 2000294391 A [0004]