(19) Europäisches Patentamt
European Patent Office
Office européen des brevets

(11) EP 1 970 902 A3

(12) EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
13.10.2010 Bulletin 2010/41

(43) Date of publication A2:
17.09.2008 Bulletin 2008/38

(21) Application number: **08152062.9**

(22) Date of filing: **28.02.2008**

(51) Int Cl.:
G10L 21/02 *(2006.01)* *G10L 19/02* *(2006.01)*

(84) Designated Contracting States:
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT RO SE SI SK TR
Designated Extension States:
AL BA MK RS

(30) Priority: **02.03.2007 JP 2007053246**
13.04.2007 JP 2007105711

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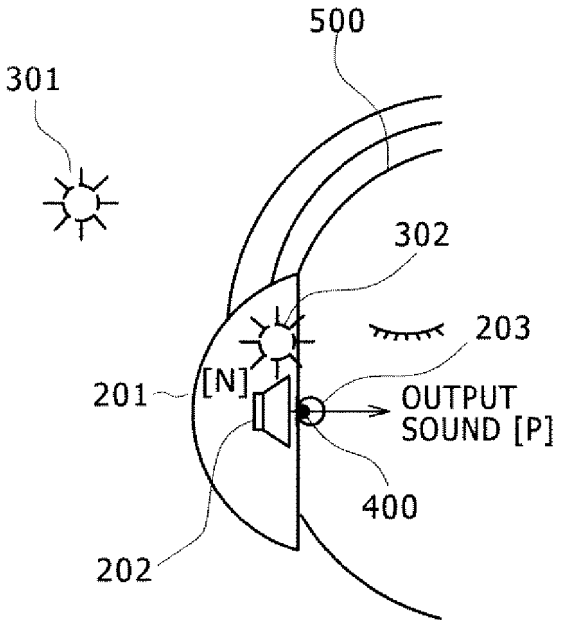
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(54) Signal processing apparatus and signal processing method

(57) Disclosed herein is a signal processing apparatus including: a first decimation processing section for generating, based on a digital signal in a first form, a digital signal in a second form; a second decimation processing section for generating, based on the digital signal in the second form, a digital signal in a third form; a first signal processing section for processing the digital signal in the third form; an interpolation processing section for converting a digital signal in the third form outputted from the first signal processing section into a digital signal in the second form; a second signal processing section for processing the digital signal in the second form outputted from the first decimation processing section; and a combining section for combining the digital signals in the second form outputted from the interpolation processing section and the second signal processing section.

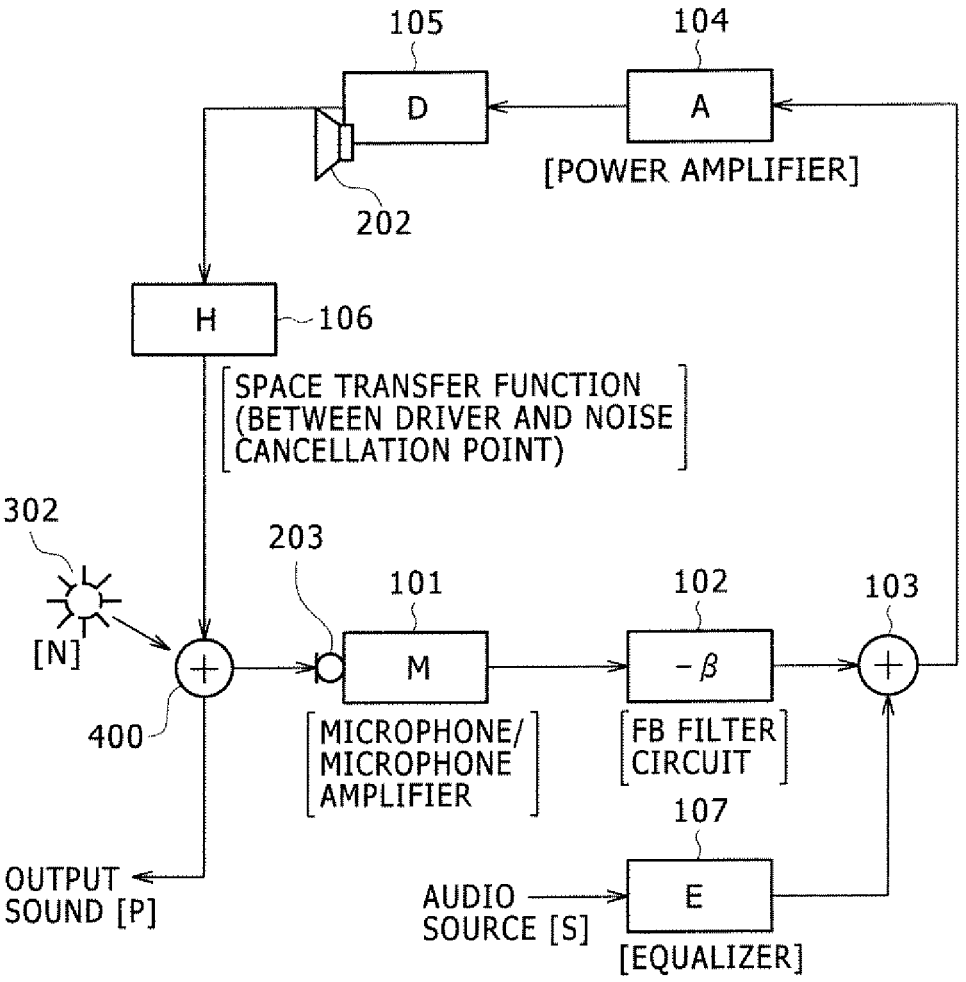
FIG.1A



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(Cont. next page)

FIG. 1B


105
104
D
A
[POWER AMPLIFIER]
202
H
106
[SPACE TRANSFER FUNCTION (BETWEEN DRIVER AND NOISE CANCELLATION POINT)]
302
203
101
102
103
[N]
M
-β
400
[MICROPHONE/ MICROPHONE AMPLIFIER]
[FB FILTER CIRCUIT]
107
OUTPUT SOUND [P]
AUDIO SOURCE [S]
E
[EQUALIZER]

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EUROPEAN SEARCH REPORT

Application Number
EP 08 15 2062

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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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TECHNICAL FIELDS SEARCHED (IPC)

G10L
G10K

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
The Hague	6 September 2010	Quélavoine, Régis

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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EPO FORM 1503 03.82 (P04C01)

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06-09-2010

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