



(11) **EP 2 099 184 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:
Corrected version no 1 (W1 B1)
Corrections, see
Bibliography INID code(s) 72

(51) Int Cl.:
H04L 25/02 (2006.01)

(48) Corrigendum issued on:
14.03.2012 Bulletin 2012/11

(45) Date of publication and mention
of the grant of the patent:
03.08.2011 Bulletin 2011/31

(21) Application number: **08152438.1**

(22) Date of filing: **07.03.2008**

(54) **Driver arrangement and signal generation method**

Verstärkeranordnung und Signalerzeugungsverfahren

Agencement de commande et procédé de génération de signal

(84) Designated Contracting States:
**AT BE BG CH CY CZ DE DK EE ES FI FR GB GR
HR HU IE IS IT LI LT LU LV MC MT NL NO PL PT
RO SE SI SK TR**

(43) Date of publication of application:
09.09.2009 Bulletin 2009/37

(73) Proprietor: **austriamicrosystems AG**
8141 Unterpremstätten (AT)

(72) Inventor: **Avalur, Krishna Kant**
500072 Kukatpally,
Hyderabad (IN)

(74) Representative: **Epping - Hermann - Fischer**
Patentanwaltsgesellschaft mbH
Ridlerstrasse 55
80339 München (DE)

(56) References cited:
WO-A-02/073804 WO-A-02/073912

- **JEAN-MICHEL REDOUTE ET AL: "An EMI Resisting LIN Driver in 0.35-micron High-Voltage CMOS" IEEE JOURNAL OF SOLID-STATE CIRCUITS, vol. 42, no. 7, 1 July 2007 (2007-07-01), pages 1574-1582, XP011186326 IEEE SERVICE CENTER, PISCATAWAY, NJ, US ISSN: 0018-9200**

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

EP 2 099 184 B8