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(54) Process and arrangement for turbo equalization with turbo decoding of signals

(57) The present invention is directed to a Process and arrangement for turbo equalization with turbo decoding of signals. In order to provide a low complexity schedule for turbo equalization with turbo decoding the process according to the present invention is **characterized in that** one equalization step is performed at each turbo equalizer iteration and that one decoder step is per-

formed at each turbo equalizer iteration, whereby at each turbo equalizer iteration only a part of the turbo decoder step is performed. An arrangement according to the present invention comprises one equalizer unit and further comprising a single decoder unit for performing only a part of a turbo decoding iteration per turbo equalizer iteration, said decoder unit being interposed between the output and an input of the equalizer unit.

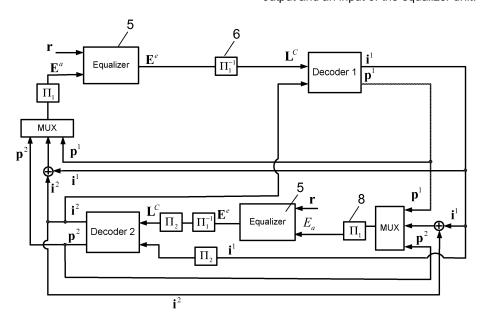


Fig. 3

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