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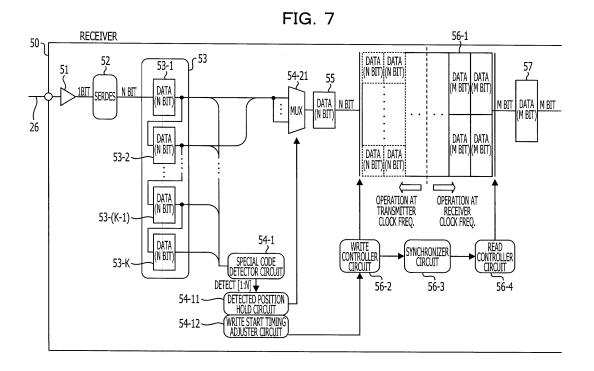
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(54)Serial data receiver circuit apparatus and serial data receiving method

(57)A serial data receiver circuit apparatus to receive serial data delimited by a first bit length, the circuit apparatus includes: a serial/parallel converter circuit to convert the serial data into parallel data of a second bit length that is smaller than the first bit length; a data hold circuit to hold a plurality of parallel data; a detector circuit to

detect a delimiter position in the received serial data; a detected position hold circuit to generate a select signal to select data included in the parallel data stored in the data hold circuit; and a selector circuit to select data in units of the second bit length starting from the data delimiter position based on the select signal.



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EUROPEAN SEARCH REPORT

Application Number EP 11 15 3586

Category	Citation of document with in of relevant pass	ndication, where appropriate, ages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X Y		990-11-14) - column 4, line 2 * - column 10, line 55 *	1-3 4,5	INV. H04L7/04
A	EP 0 249 935 A2 (FU 23 December 1987 (1 * abstract *	JITSU LTD [JP])	1-3	
Υ		KONISHI SHINYA [JP] ET	4,5	
A	AL) 31 December 200 * abstract * * figures 1-4 * * paragraph [0021]	- paragraph [0039] *	1-3	
X Y	AL) 25 March 2004 (* abstract * * paragraph [0022]	BOLES GLENN M [US] ET 2004-03-25) - paragraph [0033] * - paragraph [0038] *	6-8 4,5	TECHNICAL FIELDS SEARCHED (IPC)
	The present search report has	•		- Formula de la constitución de
	Place of search Munich	Date of completion of the search 15 December 2011	Mos	Examiner Ser, Jochen
X : parti Y : parti docu A : tech O : non	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone icularly relevant if oombined with anot ment of the same category nological background written disclosure mediate document	T : theory or principle E : earlier patent doc after the filing date D : document cited in L : document cited fo	underlying the i ument, but publi the application r other reasons	invention shed on, or



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CLAIMS INCURRING FEES						
The present European patent application comprised at the time of filing claims for which payment was due.						
Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s):						
No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due.						
LACK OF UNITY OF INVENTION						
The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:						
see sheet B						
All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.						
As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.						
Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:						
None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:						
The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims (Rule 164 (1) EPC).						



LACK OF UNITY OF INVENTION SHEET B

Application Number

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-3

detection of delimiter positions in a received serial data stream, wherein the detection processing is done based on a plurality of comparator circuits using data units having a bit length smaller than the bit length of the data contained in the serial bit stream.

2. claims: 4-8

transferring data from one clock domain to a second clock domain by use of a clock transfer buffer, wherein data is written into the clock transfer buffer according to the clock of the first clock domain and read out of the clock transfer buffer according to the clock of the second domain.

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 11 15 3586

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82