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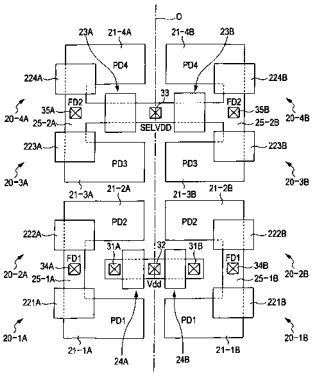
(54) **Solid state image sensor, imaging apparatus, and electronic device**

(57) A solid state image sensor includes a pixel array, as well as charge-to-voltage converters, reset gates, and amplifiers each shared by a plurality of pixels in the array. The voltage level of the reset gate power supply is set higher than the voltage level of the amplifier power supply. Additionally, charge overflowing from photodetectors in the pixels may be discarded into the charge-to-voltage converters. The image sensor may also include a row scanner configured such that, while scanning a row in the pixel array to read out signals therefrom, the row scanner resets the charge in the photodetectors of the pixels sharing a charge-to-voltage converter with pixels on the readout row. The charge reset is conducted simultaneously with or prior to reading out the signals from the pixels on the readout row.
A pixel layout is also proposed. The resulting layout is efficient for securing the area occupied by the photodetectors. Consequently, a large footprint for a photodetector can be acquired within a limited pixel boundary, even when attempting to further miniaturize pixels by adopting a multi-pixel sharing architecture in order to meet the demand for more detailed images.

FIG. 8



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