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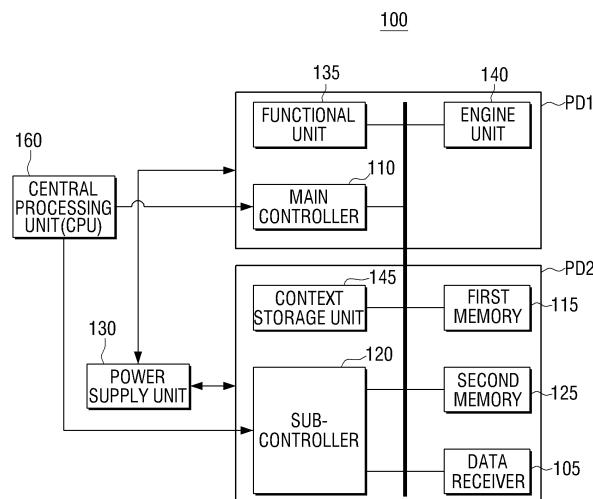
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(54) **Image forming apparatus, system-on-chip (SOC) unit, and driving method thereof**

(57) An image forming apparatus is connected to a host device including first and second power domains which are separately supplied with power and includes first and second memories to be disposed in the second power domain, a main controller disposed in the first power domain and to perform a control operation using the first memory in a normal mode, and a sub-controller disposed in the second power domain and perform a control operation using the second memory in a power-saving

mode, where when the normal mode is changed to the power-saving mode a power supply to the first power domain is shut off, the first memory operates in a self-refresh mode, and the main controller copies central processing unit (CPU) context information into a context storage unit, and when the power-saving mode is changed to the normal mode, the main controller is booted using the CPU context information stored in the context storage unit.

FIG. 1





EUROPEAN SEARCH REPORT

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The present search report has been drawn up for all claims			
1	Place of search The Hague	Date of completion of the search 26 September 2012	Examiner Absalom, Richard
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ----- & : member of the same patent family, corresponding document	
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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