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(54) **MAPPING PROCESSING LOGIC HAVING DATA PARALLEL THREADS ACROSS PROCESSORS**

ABBILDUNG EINER ABLAUFLOGIK MIT DATENPARALLELEN THREADS ÜBER MEHRERE
PROZESSOREN

MAPPAGE D'UNE LOGIQUE DE TRAITEMENT AYANT DES FILS D'EXÉCUTION DE DONNÉES
PARALLÈLES SUR DES PROCESSEURS

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(56) References cited:
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- **Anonymous: "Multithreaded Programming Guide", , 31 December 2001 (2001-12-31), XP055207852, Retrieved from the Internet: URL:<http://docs.oracle.com/cd/E19455-01/806-5257/book-info/index.html> [retrieved on 2015-08-14]**
- **"Understanding Basic Multithreading Concepts (Multithreaded Programming Guide)", , 31 December 2001 (2001-12-31), XP055207853, Retrieved from the Internet: URL:<http://docs.oracle.com/cd/E19455-01/806-5257/6je9h032e/index.html#mtintro-69945> [retrieved on 2015-08-14]**

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- **Wikipedia: "Barrier (computer science)", , 7 November 2006 (2006-11-07), XP055272330, Retrieved from the Internet:
URL:[https://en.wikipedia.org/w/index.php?title=Barrier_\(computer_science\)&oldid=86330724](https://en.wikipedia.org/w/index.php?title=Barrier_(computer_science)&oldid=86330724) [retrieved on 2016-05-12]**