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(54) **Method and system to quickly fade the luminance of an OLED display**

(57) Various embodiments include devices, methods, circuits, data structures, and software that allow for rapid variation in the luminance of an OLED display panel. An OLED display driver circuit can include a first input, a second input, and a scaling circuit. The first input is to receive a scaling factor. The second input is to receive

an image input signal including a digital representation of a desired output for a pixel. The scaling circuit is to multiply a pixel-level output voltage associated with the desired output for the pixel by the scaling factor.

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Description

BACKGROUND

5 **[0001]** Mobile devices are incorporating advanced display technology, such as liquid crystal light-emitting diode displays and organic light-emitting diode (OLED) based displays. As the capabilities of the display technology advances so too does the consumer's expectations in terms of functionality and esthetics associated with the display. Consumers demand high quality displays that are capable of fast response and vibrant display. One such capability commonly expected of a mobile device display is the ability to rapidly and smoothly fade or brighten the display in response to user input, programming, or external lighting conditions.

BRIEF DESCRIPTION OF THE DRAWINGS

[0002] FIG. 1 is a block diagram illustrating a portion of an OLED driver circuit, according to an example embodiment.

15 **[0003]** FIG. 2 is a block diagram of a section of an OLED driver IC, according to an example embodiment.

[0004] FIG. 3 is a block diagram illustrating a system for rapidly fading the luminance of an OLED display, according to an example embodiment.

[0005] FIG. 4 is a flowchart illustrating an example method for varying the illumination output of an OLED display according to a luminance scaling signal, according to an example embodiment.

20 **[0006]** FIG. 5 is a flowchart illustrating an example method of varying the luminance of an OLED display, according to an example embodiment.

[0007] FIG. 6 is a flowchart illustrating an example method of varying the luminance of an OLED display based on receiving a triggering event, according to an example embodiment.

[0008] FIG. 7 is a block diagram depicting a mobile device, according to an example embodiment.

OVERVIEW

30 **[0009]** The present inventors have recognized, among other things, a need for a mechanism to rapidly and smoothly fade (vary) the luminance of an OLED display. The present systems and methods address this need by enabling an OLED display driver to rapidly and smoothly fade the luminance of an OLED display using a single input signal.

[0010] Example 1 includes an OLED display driver circuit comprising a first input, a second input, and a scaling circuit. The first input is structured to receive a scaling factor. The second input is structured to receive an image input signal. The image input signal includes a digital representation of a desired output for a pixel within an OLED display. The scaling circuit is structured to multiply a pixel-level output voltage corresponding to the image input signal by the scaling factor.

35 **[0011]** In example 2, the OLED display driver circuit of claim 1 can optionally include the first input structured to receive a signal that can be interpreted as a value between 1 and 0.

[0012] In example 3, the OLED display driver circuit of one or any combination of examples 1-2 can optionally include the first input structured to receive a pulse-width modulated signal representing a value between 1 and 0.

40 **[0013]** In example 4, the OLED display driver circuit of one or any combination of examples 1-3 can optionally include the second input structured to receive a signal representing a gray-scale pixel value.

[0014] In example 5, the OLED display driver circuit of one or any combination of examples 1-4 can optionally include a register coupled to the second input and containing a look-up table, the look-up table configured to map the digital representation of a desired output for the pixel to a pixel-level output voltage.

45 **[0015]** In example 6, the OLED display driver circuit of example 5 can optionally include the second input structured to receive an image input signal representing a color pixel value, the image input signal including a first component representing red, a second component representing green, and a third component representing blue.

[0016] In example 7, the OLED display driver circuit of example 6 can optionally include the register including a look-up table associated with each of the first, second, and third components of the image input signal representing the color pixel value.

50 **[0017]** In example 8, the OLED display driver circuit of one or any combination of examples 5-7 can optionally include the register structured to map:

an 8-bit value representing a red portion of the digital representation of the desired output for the first pixel to a first pixel-level output voltage;

an 8-bit value representing a green portion of the digital representation of the desired output for the first pixel to a second pixel-level output voltage; and

an 8-bit value representing a blue portion of the digital representation of the desired output for the first pixel to a

third pixel-level output voltage.

[0018] In example 9, the OLED display driver circuit of example 8 can optionally include the scaling circuit structured to scale the first, second, and third pixel-level output voltages using the scaling factor.

[0019] Example 10 includes a method comprising receiving a luminance scaling signal, receiving a pixel input signal, determining a pixel output voltage, scaling the pixel output voltage, and outputting the scaled pixel output voltage. The pixel input signal includes a digital representation of a desired output for a pixel within a display device at a particular moment in time. The pixel output voltage is determined from the pixel input signal. The pixel output voltage is scaled using the luminance scaling signal to produce a scaled pixel output voltage.

[0020] In example 11, the method of example 10 can optionally include determining the pixel output voltage from the pixel input signal including using a look-up table, the look-up table including gamma values that map digital pixel input signals to pixel-level output voltages.

[0021] In example 12, the method of one or any combination of examples 10-11 can optionally include receiving the luminance scaling signal including receiving a signal that can be interpreted as a value between 0 and 1.

[0022] In example 13, the method of one or any combination of examples 10-12 can optionally include receiving the luminance scaling signal including receiving a pulse-width modulated signal representing a value between 0 and 1.

[0023] In example 14, the method of one or any combination of examples 10-13 can optionally include receiving the pixel input signal including receiving a signal representing a gray-scale pixel value.

[0024] In example 15, the method of one or any combination of examples 10-14 can optionally include receiving the pixel input signal including receiving an image input signal representing a color pixel value, the image input signal including a first component addressing a red portion of a pixel, a second component addressing a green portion of the pixel, and a third component addressing a blue portion of the pixel.

[0025] In example 16, the method of example 15 can optionally include determining the pixel output voltage from the pixel input signal including using a series of look-up tables, each look-up table including gamma values that map one of the first, second, and third components addressing red, green, or blue portion of the pixel input signal to pixel-level output voltage.

[0026] In example 17, the method of one or any combination of examples 15-16 can optionally include determining the pixel output voltage including determining a first pixel output voltage using the first component addressing the red portion of the pixel, a second pixel output voltage using the second component addressing the green portion of the pixel, and a third pixel output voltage using the third component addressing the blue portion of the pixel.

[0027] In example 18, the method of example 17 can optionally include scaling the first, second, and third output voltages using the luminance scaling signal.

[0028] Example 19 includes an apparatus comprising a processor, an OLED display, and an OLED display driver circuit. The processor is coupled to a memory circuit and the OLED display includes a plurality of individually addressable pixels. The OLED display driver circuit includes a first input, a second input, and a scaler. The first input is structured to receive a scaling factor from the processor. The second input is structured to receive an image input signal from the processor. The image input signal can include image data to drive the plurality of individually addressable pixels. The scaler is coupled to the first input and the second input. The scaler is also structured to multiply pixel-level output voltages associated with the plurality of individually addressable pixel in the image input signal by the scaling factor.

[0029] In example 20, the apparatus of example 19 can optionally include a register coupled between the second input and the scaler, the register including a look-up table and an output input, the look-up table configured to map data representing each of the plurality of individual addressable pixels within the image input signal to the pixel-level output voltages.

[0030] In example 21, the apparatus of one or any combination of examples 19-20 can optionally include the OLED display driver circuit structured to receive, at the first input, a scaling factor that can be converted into a scaling value between 0 and 1.

[0031] In example 22, the apparatus of one or any combination of examples 19-21 can optionally include the OLED display driver circuit structured to receive at the first input a pulse-width modulated signal representing a value between 0 and 1.

[0032] In example 23, the apparatus of one or any combination of examples 19-22 can optionally include the OLED display driver circuit structured to receive, at the second input, an image input signal including a value associated with each of the plurality of individually addressable pixels of the OLED display.

[0033] This overview is intended to provide an overview of subject matter of the present patent application. It is not intended to provide an exclusive or exhaustive explanation of the invention. The detailed description is included to provide further information about the present patent application.

DETAILED DESCRIPTION

[0034] In the following description, numerous specific details are set forth in order to provide a thorough understanding of example embodiments. It is to be understood, however, that the various embodiments may be practiced without these specific details. For example, logical, electrical and structural changes may be made without departing from the spirit and scope of the present subject matter. The following detailed description is, therefore, not to be taken in a limiting sense.

[0035] Methods and systems to quickly fade the luminance of an organic light-emitting diode (OLED) display panel are described. Embodiments described herein are directed to mobile device OLED display driver circuits that provide for efficient variable control over output brightness (luminance) through a single input. However, the concepts discussed herein are applicable to any OLED display (e.g., computer monitor, television). Mobile devices that utilize an OLED display can include cell phones, personal digital assistants (PDAs), smartphones, and tablet-style computers, among others. In an example, control over OLED luminance can be accomplished through the use of a scaling register associated with the gamma values programmed to control per pixel output within the OLED display. Gamma values refer to gamma correction used to code and decode luminance values in graphic display systems (e.g., video or still image electronic displays, such as a computer monitor or mobile device screen).

[0036] A mobile device can use a variety of display technologies, such as a liquid crystal display (LCD). Some mobile devices use an LCD-type display that includes a backlight or an active array of transistors (e.g., an active thin-film transistor matrix, or the like), or both, to control each pixel in the display. Backlit LCD displays can provide for fast response times and vibrant displays desired by today's mobile device consumer. Backlit LCD displays allow for rapid variations in display luminance simply by varying the output of the backlights. However, LCDs including a backlight or an active transistor matrix, or both, can have high power demands, thus shortening battery life of the mobile device.

[0037] OLED display technology is rapidly gaining ground versus LCDs for use in mobile devices due to the potential for improved power efficiency, improved color reproduction, and potential for thinner displays. Additionally, OLED displays can achieve faster response rates, achieve higher contrast levels, and produce higher saturated color reproduction. Unlike traditional LCD technology that requires a backlight to illuminate the display, OLED pixels are self-emissive (i.e. produce their own light). An OLED is an LED whose emissive electroluminescent layer can be composed of a thin-film of organic compounds capable of producing light when an electrical current is passed through it. OLEDs are capable of greater contrast ratios, thinner packaging, and deeper black levels, when compared to traditional LCD displays. However, fading or controlling the brightness of an OLED display can be more complicated than with LCD displays. As mentioned above, LCD displays are typically backlit; thus, varying the output level is a matter of varying the backlight.

[0038] Within a traditional LCD display, varying the output brightness can be accomplished by a backlight driver integrated circuit (IC). Typical driver ICs can use a pulse-width modulated (PWM) signal to vary the power delivered to the LEDs and thus vary the output brightness. Because OLEDs are self-emissive and depend upon programmed gamma values to map pixel input values to output voltages (e.g., to maintain color balance), current OLED driver ICs cannot effectively utilize a similar PWM signal to directly control brightness. Currently available driver ICs for OLEDs require reprogramming of the gamma values (e.g., reprogramming a look-up table mapping input values to output voltages) to change the brightness (luminance) of the OLEDs in a controlled manner. Reprogramming of the gamma values used for mapping digital input values to output voltages can require additional command traffic over a control interface, additional storage of pre-programmed gamma value mappings, or both.

[0039] FIG. 1 is a block diagram illustrating a portion of an OLED driver circuit, according to an example embodiment. In an example, a circuit 100 can include a pixel input 110, a splitter 120, gamma adjustment circuits 130A, 130B, 130N (collectively referred to as gamma adjustment circuits 130), and pixel-level voltage outputs 140A, 140B, and 140N (collectively referred to as pixel-level voltage outputs 140). In this example, the pixel input 110 can be a 24-bit value containing information to address an RGB (red, green, and blue) color space pixel. In another example, the pixel input 110 can be an 8-bit value containing information to address a grey-scale (0-254) pixel. In yet another example, the pixel input 110 can be a 32-bit (or greater) value containing information to address a CMYK (cyan, magenta, yellow, and key black) color space pixel.

[0040] In the example illustrated by FIG. 1, the splitter 120 can receive a 24-bit RGB color space pixel and divide the pixel input 110 into three individual 8-bit color values (e.g., 8-bit red value 122, 8-bit green value 124, and 8-bit blue value 126). In this example, the splitter 120 can pass the individual color components to associated gamma adjustment circuits 130. The gamma adjustment circuits 130 can map the individual color components into analog pixel-level voltage outputs 140. In some examples, the gamma adjustment circuits 130 can use look-up tables (LUTs) to map between input and output values. In other examples, the gamma adjustment circuits 130 can use circuit elements to transform input values into desired output values. The pixel-level voltage outputs 140 can be used to drive an individual pixel to emit light associated with the pixel input 110. In the example illustrated by FIG. 1, varying (or fading) the luminance of the OLED display requires reprogramming of the gamma adjustment circuits 130. Varying the luminance or intensity of light emitted by a display is often referred to as fading the display.

[0041] In an example embodiment, an OLED driver IC can be configured to accept a single scaling input that can be

used to rapidly vary the brightness (luminance) output of an OLED display. The single scaling input can be either digital or analog (e.g., PWM signal). The OLED driver IC can use the scaling input to attenuate the gamma-adjusted voltage. For example, the scaling input can be applied to attenuate the gamma-adjusted voltage after the pixel input has been mapped by a gamma adjustment portion of the OLED driver IC. The use of a scaling input to enable rapid adjustment of the luminance of an OLED display is discussed further below in reference to FIGs. 2-6.

[0042] FIG. 2 is a block diagram of a section of an OLED driver IC, according to an example embodiment. In an example, a circuit 200 can include a pixel input 205, a luminance scaling input 210, a gamma block 220, a scaler circuit 240, and an output 250. In certain examples, the gamma block 220 and the scaler circuit 240 can be integrated into a voltage mapping circuit 215. In an example, the pixel input 205 can be a digital signal including eight (8) or more bits of data. In this example, the pixel input 205 is a 24-bit digital signal representing an RGB color space pixel value. In an example, the luminance scaling input 210 can be a digital or analog input that can be converted into a value between zero (0) and one (1) by the scaler circuit 240. In this example, the luminance scaling input is an 8-bit digital input. In another example, the luminance scaling input can be a PWM signal. In certain examples, the luminance scaling input 210 is programmable. In an example, the luminance scaling input 210 can be provided by a programmable processor, such as a processor within a mobile device. In this example, the programmable processor can vary the luminance scaling input 210 over a range that when converted is smaller than between zero (0) and one (1) (e.g., between .5 and 1). Varying the luminance scaling input 210 over a smaller range can result in quickly scaling the luminance uniformly across an OLED display above a threshold (i.e. minimum level of brightness). Other programmatic manipulation of the luminance scaling input 210 can produce various rapid uniform changes to the luminance across an OLED display. In some examples, the luminance scaling input is referred to as a scaling factor.

[0043] In an example, the circuit 200 includes a gamma block 220 that can be used to map pixel input 205 into a representative pixel-level analog voltage. The pixel-level analog voltage can be used to drive a pixel within an OLED display. In an example, the gamma block 220 contains a look-up table (LUT) with a fixed number of entities to map from a digital input signal to an analog output voltage level. In this example, the gamma block 220 contains a LUT with eight (8) voltage mappings (e.g., 225A - 225N). In this example, the LUT is configured to directly map digital input values of 0, 1, 32, 80, 172, 220, 254, and 255 to corresponding pixel-level analog voltage values. In this example, the gamma block 220 can interpolate digital values that fall between the directly mapped values. Interpolation ranges are depicted within FIG. 2 by ranges 230A - 230N. In an example, the gamma block 220 can use a linear interpolation to map the voltage of a value between directly mapped values. In another example, the gamma block 220 can simply round up or down to the nearest directly mapped value when interpolating inputs that fall between directly mapped values. In certain examples, the gamma block 220 can include a LUT with two-hundred and fifty five (255) directly mapped values, eliminating the need to interpolate for a given 8-bit input value.

[0044] In the example depicted by FIG. 2, the gamma block 220 outputs a mapped pixel-level voltage to a scaler circuit 240 (also referred to as a scaling circuit in some examples). In an example, the scaler circuit 240 can multiply the pixel-level voltage by the luminance scaling input 210 to reduce (fade) the pixel-level output voltage sent to the output 250. In an example, the luminance scaling input 210 can be interpreted by the scaler circuit 240 as a value between zero (0) and one (1). A value of one (1) will result in a full brightness (maximum luminance) output from the addressed pixel within the OLED display. A value of zero (0) can result in the addressed pixel being turned off (e.g., faded to zero luminance). Varying the luminance scaling input 210 between zero (0) and one (1) can result in the OLED display pixel varying between zero (0) output and full luminance.

[0045] In one example, the scaler circuit 240 can use the following equation to scale the output voltage:

$$V_{out_X} = (L/L_{max}) * \text{Gamma_LUT}[X(7:0)]$$

In the scaling equation, V_{out_X} represents the scaled pixel-level output voltage. L represents the luminance scaling input 210, L_{max} represents the maximum value that can be input for the luminance scaling input 210, and Gamma_LUT represents the mapped pixel-level voltage for a given pixel input, such as pixel input 205 (e.g., $X(23:0)$, which is an 24-bit digital input in this example.). This equation allows for the scaling input 210 to be interpreted as any value less than the maximum allowable scaling input. For example, $L(7:0)$ is an 8-bit digital scaling input value that can vary between 0 and 254, with 254 being the maximum allowable scaling input (L_{max}).

[0046] FIG. 3 is a block diagram illustrating a system 300 for rapidly fading the luminance of an OLED display. In an example, the system 300 includes an image source 305, a scaling source 306, a luminance event source 308, a display driver circuit 310, and a display device 350. In one example, the display driver circuit 310 includes a scaling input 312, an image input 314, one or more gamma registers 320, a multiplier 330, an event input 335, and an output 340. In this example, the scaling input 312 can receive either a digital signal or a PWM analog signal from the scaling source 306. The scaling source 306 can include a general purpose processor or a dedicated ambient light control circuit. In certain

examples, the general purpose processor can be programmed to provide scaling signals to the scaling input 312 in response to programmatic events. In some example, the general purpose processor can be programmed to provide scaling signals to the scaling input 312 in response to inputs received through a user interface displayed on the OLED display.

[0047] In the example depicted in FIG. 3, the image input 314 is coupled to the image source 305. The image source 305 can include dedicated or general purpose device memory accessed by a dedicated graphics processor or a general purpose device processor. In an example, the image source 305 can provide a stream of digital data addressed to individual pixels within the display device 350.

[0048] In an example, the gamma registers 320 can include one or more LUTs configured to map the digital pixel data received over the image input 314 into pixel-level voltages used to drive the individual pixels within the display device 350. In one example, the gamma registers 320 can include three LUTs (325A, 325B, and 325N), which can be used to map digital pixel data in an RGB color space (e.g., an 8-bit red value, an 8-bit green value, and an 8-bit blue value). In an example, the output of the gamma registers 320 can be operated on by the multiplier 330. The multiplier 330 can use the scaling input 312 to scale the output of the gamma registers 320 according to the desired luminance level (represented by the scaling input 312).

[0049] In an example, the display driver circuit 310 can be structured to bypass the gamma registers 320 and pass the image source 305 data received by the image input 314 directly to the multiplier 330. In this example, the multiplier 330 can include circuitry structured to convert the image source 305 data into pixel-level voltages as well as scaling the pixel-level voltages according to the scaling input 312.

[0050] In certain examples, the multiplier 330 can be activated when a luminance event is received at the event input 335 from the luminance event source 308. The luminance event source 308 can include a general purpose processor or a user activated switch, among other structures. In an example, a general purpose processor can include programming that triggers luminance events in response to user input or other programming, such as a low battery power indication. In these examples, the multiplier 330 applies the scaling input 312 in response to receiving a luminance event from the event input 335.

[0051] FIG. 4 is a flowchart illustrating an example method 400 for varying the illumination output of an OLED display according to a luminance scaling signal. In an example, the method 400 includes operations for receiving a luminance scaling signal (410), receiving a first pixel input signal (420), determining a first pixel output voltage (430), scaling the first pixel output voltage (440), and outputting the scaled first pixel output voltage (450). In one example, the method 400 can begin at operation 410, with respect to system 300 of Figure 3, with the display driver circuit 310 receiving a luminance scaling signal on the scaling input 312. In certain examples, the scaling input 312 is configured to receive a digital input that can be used to scale the luminance of an OLED panel. In some examples, the scaling input 312 can be configured to receive a PWM signal that can be used by the display driver circuit 310 to vary the luminance of an OLED display.

[0052] At operation 420, the method 400 continues with the display driver circuit 310 receiving a first pixel input signal on the image input 314. At operation 430, the method 400 continues with the display driver circuit 310 determining a first pixel output voltage based on the first pixel input signal. In an example, the display driver circuit 310 communicates the first pixel input signal into the gamma registers 320, which can include one or more LUTs used to map the pixel input signal to an appropriate output voltage. At operation 440, the method 400 continues with the display driver circuit 310 scaling the first pixel output voltage. In an example, the display driver circuit 310 can use the multiplier 330 to scale the first pixel output voltage according to the luminance scaling signal received on the scaling input 312. At operation 450, the method 400 can conclude with the display driver circuit 310 outputting a scaled first pixel output voltage to the display device 350. In an example, the display driver circuit 310 can send the scaled first pixel output voltage to the display device 350 via an output 340.

[0053] FIG. 5 is a flowchart illustrating an example method 500 of varying the luminance of an OLED display. As discussed above, an image source, such as image source 305, can consist of a stream of pixel values that include red, green, and blue components (or portions). In an example, the image source can provide a 24-bit value that contains three (3) 8-bit values representing red, blue, and green portions of each pixel. The method 500 provides an example of scaling individual color component output voltages (e.g., in an RGB color space, output of scaled red, green, and blue output voltages). In an example, the method 500 includes operations for receiving a luminance scaling signal (505), receiving a first pixel input signal (510), separating color components of the first pixel input signal (515), determining a red pixel component voltage (520), determining a green pixel component voltage (540), determining a blue pixel component voltage (560), scaling the red pixel component voltage (525), scaling the green pixel component voltage (545), scaling the blue pixel component voltage (565), outputting the scaled red pixel component voltage (530), outputting the scaled green pixel component voltage (550), and outputting the scaled blue pixel component voltage (570). Example method 500 can be described with respect to example system 300 of Figure 3.

[0054] At operation 505, the method 500 begins with the display driver circuit 310 receiving a luminance scaling signal over the scaling input 312. At operation 510, the method 500 continues with the display driver circuit 310 receiving a

first pixel input signal over the image input 314. In this example, the first pixel input signal is a 24-bit RGB color space pixel value that includes 8-bits of data for the red, green, and blue components of a single pixel. In some examples, the first pixel input signal can include a stream of pixel data that is processed by the display driver circuit 310 in a similar fashion.

[0055] At operation 515, the method 500 continues with the display driver circuit 310 separating the color components (e.g., red, green, and blue) for further processing. The remaining operations of the method 500 can be performed in parallel or sequentially depending upon the configuration of the display driver circuit 310 and associated hardware. In certain examples, the gamma registers 320 include individual registers associated with each color component (325A, 325B, 325N). In an example, the display driver circuit 310 can include multiple multipliers, such as multiplier 330, to assist in parallel processing of a signal color pixel input signal.

[0056] At operation 520, the method 500 continues with the display driver circuit 310 determining a red pixel component voltage. In an example, the display driver circuit 310 can use the gamma registers 320 to map the red component of the first pixel input signal to the red pixel component voltage. At operation 525, the method 500 continues with the display driver circuit 310 scaling the red pixel component voltage according to the luminance scaling signal. In an example, the display driver circuit 310 can use the multiplier 330 to scale the red pixel component voltage. At operation 530, the method 500 continues with the display driver circuit 310 outputting the scaled red pixel component voltage to a pixel on the display device 350.

[0057] Operations 540 through 570 of method 500 mirror operations 520-530 for the green and blue components of the first pixel input signal. The method 500 concludes by outputting three discrete voltage signals representing the red, green, and blue components of the first pixel input signal to the display device 350. In an example, the three voltage signals are sent to the display device 350 simultaneously.

[0058] FIG. 6 is a flowchart illustrating an example method 600 of varying the luminance of an OLED display based on receiving a triggering event, according to an example embodiment. The method 600 includes operations for receiving an image input signal (610), determining per pixel output voltages for the image input signal (620), determining whether a scaling event has been received (630), reading a luminance scaling signal (640), scaling the per pixel output voltages (650), and outputting the per pixel output voltages (660). Example method 500 can be described with respect to example system 300 of Figure 3.

[0059] The method 600 begins at operation 610 with the display driver circuit 310 receiving an image input signal over the image input 314. In an example, the image input signal is received from an image source 305. At operation 620 the method 600 continues with the display driver circuit 310 determining per pixel output voltages for the image input signal received by image input 314. In an example, the display driver circuit 310 can use the gamma registers 320 to map each pixel within the image input signal to an associated pixel output voltage. In certain examples, the image input signal can represent a still image to be displayed on the OLED display. In some examples, the image input signal can represent a dynamic image (e.g., video feed or graphical user-interface) sampled at a certain frequency, such as 60 Hz.

[0060] At operation 630, the method 600 continues with the display driver circuit 310 determining whether a scaling event has been received over event input 335. In an example, the scaling event can be used by the display driver circuit 310 to enable or disable scaling of the per pixel output voltages. Scaling of the per pixel output voltages can result in rapidly fading the luminance of the OLED display. If no scaling event has been received by the display driver circuit 310, the method 600 concludes at operation 660 with the display driver circuit 310 outputting the non-scaled per pixel output voltages over the output 340 to the display device 350.

[0061] If a scaling event has been received the method 600 continues at operation 640 with the display driver circuit 310 reading the luminance scaling signal received on the scaling input 312. At 650, the method 600 continues with the display driver circuit 310 using the scaling signal to scale the per pixel output voltages. In some examples, the scaling can be done incrementally over multiple scans of the image input signal to create a smooth effect on the OLED display. For example, when a scaling event is received the display driver circuit 310 can incrementally scale the per pixel output voltages over a certain number of scans of the 60 Hz input image signal, such as over 30 scans. This example would result in the luminance of the display fading smoothly over a half second period of time. At 660, the method 600 concludes with the display driver circuit outputting the scaled per pixel output voltages to the display device 350 via output 340.

[0062] FIG. 7 is a block diagram depicting a mobile device 700 according to an example embodiment. In an example, the mobile device 700 includes a processing unit 702, memory 704, removable storage 712, non-removable storage 714, display 722, and display driver 728. The processing unit 702 may include one or more processing units or may include one or more multiple-core processing units. Memory 704 may include volatile memory 706 and non-volatile memory 708. Mobile device 700 may include a variety of device-readable media, such as volatile memory 706 and non-volatile memory 708, removable storage 712 and non-removable storage 714. The storage may include random access memory (RAM), read-only memory (ROM), erasable programmable read-only memory (EPROM) and electrically erasable programmable read-only memory (EEPROM), flash memory or other memory technologies, or any other medium capable of storing machine-readable instructions and data that may be present in a mobile electronic device. Mobile device 700 may include input 716, output 718, and a communication connection device 720.

[0063] The mobile device 700 typically operates in a networked environment using the communication connection

device 720 to connect to one or more networks, such as a wireless telephone network. Through the communication connection device 720, the mobile device 700 may connect to one or more remote computers. The remote computer may include a personal computer (PC), server, router, network PC, a peer device, or other common network input, or the like. The communication connection device 720 may connect to various network types that may include a wireless telephone network, a Local Area Network (LAN), a Wide Area Network (WAN), the Internet, a proprietary subscription-based network, or other networks. The mobile device 700 also may include wireless telephone capabilities to provide voice telephone service via a wireless telephone network.

[0064] Machine-readable instructions stored on a machine-readable medium are executable by the processing unit 702 of the mobile device 700. The memory 704, removable storage 712, and non-removable storage 714 are examples of articles including a machine-readable medium. For example, a program with instructions that may be stored in memory 704 and when executed by the processing unit 702 can cause the mobile device 700 to perform one or more of the methods described herein. Other programs may also be stored on a machine-readable medium, such as a browser application providing web browsing functionality for the mobile device 700.

[0065] Method examples described herein can be machine or computer-implemented, at least in part. Some examples can include a computer-readable medium or machine-readable medium encoded with instructions operable to configure an electronic device to perform methods as described in the above examples. An implementation of such methods can include code, such as microcode, assembly language code, a higher-level language code, or the like. Such code can include computer-readable instructions for performing various methods. The code may form portions of computer program products. Further, the code may be stored on one or more volatile or non-volatile computer-readable media during execution or at other times. These computer-readable media may include, but are not limited to, hard disks, removable magnetic disks, removable optical disks (e.g., compact disks and digital video disks), magnetic cassettes, memory cards or sticks, random access memories (RAMs), read-only memories (ROMs), and the like.

[0066] The above detailed description includes references to the accompanying drawings, which form a part of the detailed description. The drawings show, by way of illustration, specific embodiments in which the subject matter can be practiced. These embodiments are also referred to herein as "examples." Such examples can include elements in addition to those shown and described. However, the present inventors also contemplate examples in which only those elements shown and described are provided. It will be readily understood to those skilled in the art that various other changes in the details, material, and arrangements of the parts and method stages which have been described and illustrated herein may be made without departing from the principles of the inventive subject matter.

[0067] In this document, the terms "a" or "an" are used, as is common in patent documents, to include one or more than one, independent of any other instances or usages of "at least one" or "one or more." In this document, the term "or" is used to refer to a nonexclusive or, such that "A or B" includes "A but not B," "B but not A," and "A and B," unless otherwise indicated. In the appended claims, the terms "including" and "in which" are used as the plain-English equivalents of the respective terms "comprising" and "wherein." Also, in the following claims, the terms "including" and "comprising" are open-ended, that is, a system, device, article, or process that includes elements in addition to those listed after such a term in a claim are still deemed to fall within the scope of that claim. Moreover, in the following claims, the terms "first," "second," and "third," etc. are used merely as labels, and are not intended to impose numerical requirements on their objects.

[0068] The above description is intended to be illustrative, and not restrictive. For example, the above-described examples (or one or more features thereof) may be used in combination with each other. Other embodiments can be used, such as by one of ordinary skill in the art upon studying the above description. The Abstract is provided to comply with 37 C.F.R. §1.72(b), to allow the reader to quickly ascertain the nature of the technical disclosure. It is submitted with the understanding that it will not be used to interpret or limit the scope or meaning of the claims. Also, in the above Detailed Description, various features may be grouped together to streamline the disclosure. This should not be interpreted as intending that an unclaimed disclosed feature is essential to any claim. Rather, inventive subject matter may lie in less than all features of a particular disclosed embodiment. Thus, the following claims are hereby incorporated into the Detailed Description, with each claim standing on its own as a separate embodiment.

Claims

1. An organic light emitting diode (OLED) display driver circuit (310) comprising:

a first input (312) to receive a scaling factor;
 a second input (314) to receive an image input signal, the image input signal including a digital representation of a desired output for a pixel within an OLED display; and
 a scaling circuit (330) to multiply a pixel-level output voltage, corresponding to the image input signal, by the scaling factor.

2. The OLED display driver circuit (310) of claim 1, wherein the first input is structured to receive a signal representing a value between 1 and 0.
- 5 3. The OLED display driver circuit (310) of any one of claims 1 through 2, wherein the first input is structured to receive a pulse-width modulated signal representing a value between 1 and 0.
4. The OLED display driver circuit (310) of any one of claims 1 through 3, wherein the second input is structured to receive a signal representing a gray-scale pixel value.
- 10 5. The OLED display driver circuit (310) of any one of claims 1 through 4, further including a register coupled to the second input and containing a look-up table, the look-up table configured to map the digital representation of a desired output for the pixel to a pixel-level output voltage.
- 15 6. The OLED display driver circuit (310) of claim 5, wherein the second input is structured to receive an image input signal representing a color pixel value, the image input signal including a first component representing red, a second component representing green, and a third component representing blue.
- 20 7. The OLED display driver circuit (310) of claim 6, wherein the register contains a look-up table associated with each of the first component, the second component, and the third component of the image input signal representing the color pixel value.
8. The OLED display driver circuit (310) of any one of claims 5 through 7, wherein the register is structured to map:
25 an 8-bit value representing a red portion of the digital representation of the desired output for the first pixel to a first pixel-level output voltage;
an 8-bit value representing a green portion of the digital representation of the desired output for the first pixel to a second pixel-level output voltage; and
an 8-bit value representing a blue portion of the digital representation of the desired output for the first pixel to a third pixel-level output voltage.
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9. The OLED display driver circuit (310) of claim 8, wherein the scaling circuit is to scale the first, second, and third pixel-level output voltages using the scaling factor.
- 35 10. A method comprising:
receiving a luminance scaling signal (410);
receiving a pixel input signal (420), the pixel input signal including a digital representation of a desired output for a pixel within a display device;
determining a pixel output voltage from the pixel input signal (430);
40 scaling the pixel output voltage (440) using the luminance scaling signal to produce a scaled pixel output voltage; and
outputting the scaled pixel output voltage (450).
- 45 11. The method of claim 10, wherein the determining the pixel output voltage from the pixel input signal includes using a look-up table, the look-up table including gamma values that map digital pixel input signals to pixel-level output voltages.
12. The method of any one of claims 10 through 11, wherein receiving the luminance scaling signal includes receiving a signal representing a value between 0 and 1.
- 50 13. The method of any one of claims 10 through 12, wherein receiving the luminance scaling signal includes receiving a pulse-width modulated signal representing a value between 0 and 1.
- 55 14. The method of any one of claims 10 through 13, wherein receiving the pixel input signal includes receiving an image input signal representing a color pixel value, the image input signal including a first component addressing a red portion of a pixel, a second component addressing a green portion of the pixel, and a third component addressing a blue portion of the pixel.

15. An apparatus comprising:

a processor (702) coupled to a memory circuit (704);
 an organic light emitting diode (OLED) display (350) including a plurality of individually addressable pixels;
 an OLED display driver circuit (310) coupled to the OLED display (350), the OLED display driver circuit including:

a first input (312) to receive a scaling factor from the processor;
 a second input (314) to receive an image input signal from the processor, the image input signal including image data to drive the plurality of individually addressable pixels; and
 a scaler circuit (330) coupled to the first input (312) and the second input (314), the scaler circuit structured to multiply pixel-level output voltages associated with the plurality of individually addressable pixels in the image input signal by the scaling factor.

Amended claims in accordance with Rule 137(2) EPC.

1. An organic light emitting diode (OLED) display driver circuit (310) comprising:

a first input (312) to receive a luminance scaling factor;
 a second input (314) to receive an image input signal, the image input signal including a digital representation of a desired output for a pixel within an OLED display; and
 a luminance scaling circuit (330) to multiply a pixel-level output voltage, corresponding to the image input signal, by the luminance scaling factor.

2. The OLED display driver circuit (310) of claim 1, wherein the first input is structured to receive a signal representing a value between 1 and 0.

3. The OLED display driver circuit (310) of any one of claims 1 through 2, wherein the first input is structured to receive a pulse-width modulated signal representing a value between 1 and 0.

4. The OLED display driver circuit (310) of any one of claims 1 through 3, wherein the second input is structured to receive a signal representing a gray-scale pixel value.

5. The OLED display driver circuit (310) of any one of claims 1 through 4, further including a register coupled to the second input and containing a look-up table, the look-up table configured to map the digital representation of a desired output for the pixel to a pixel-level output voltage.

6. The OLED display driver circuit (310) of claim 5, wherein the second input is structured to receive an image input signal representing a color pixel value, the image input signal including a first component representing red, a second component representing green, and a third component representing blue.

7. The OLED display driver circuit (310) of claim 6, wherein the register contains a look-up table associated with each of the first component, the second component, and the third component of the image input signal representing the color pixel value.

8. The OLED display driver circuit (310) of any one of claims 5 through 7, wherein the register is structured to map:

an 8-bit value representing a red portion of the digital representation of the desired output for the first pixel to a first pixel-level output voltage;
 an 8-bit value representing a green portion of the digital representation of the desired output for the first pixel to a second pixel-level output voltage; and
 an 8-bit value representing a blue portion of the digital representation of the desired output for the first pixel to a third pixel-level output voltage.

9. The OLED display driver circuit (310) of claim 8, wherein the scaling circuit is to scale the first, second, and third pixel-level output voltages using the scaling factor.

10. A method for controlling an organic light emitting diode (OLED) display comprising:

receiving a luminance scaling factor signal (410);
 receiving a pixel input signal (420), the pixel input signal including a digital representation of a desired output
 for a pixel within a display device;
 determining a pixel output voltage from the pixel input signal (430);
 multiplying the pixel output voltage (440) by the luminance scaling factor to produce a scaled pixel output voltage;
 and
 outputting the scaled pixel output voltage (450).

11. The method of claim 10, wherein the determining the pixel output voltage from the pixel input signal includes
 using a look-up table, the look-up table including gamma values that map digital pixel input signals to pixel-level
 output voltages.

12. The method of any one of claims 10 through 11, wherein receiving the luminance scaling signal includes receiving
 a signal representing a value between 0 and 1.

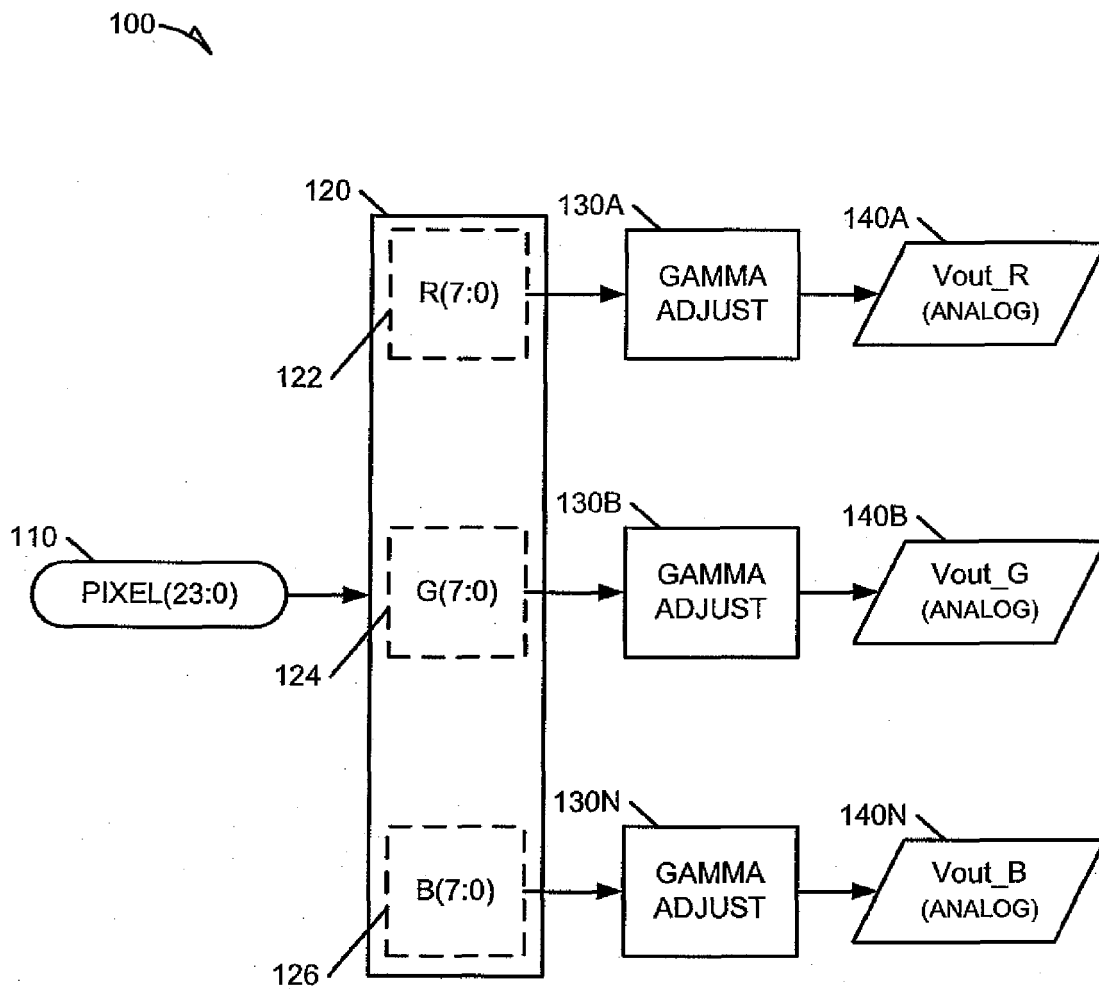
13. The method of any one of claims 10 through 12, wherein receiving the luminance scaling signal includes receiving
 a pulse-width modulated signal representing a value between 0 and 1.

14. The method of any one of claims 10 through 13, wherein receiving the pixel input signal includes receiving an
 image input signal representing a color pixel value, the image input signal including a first component addressing
 a red portion of a pixel, a second component addressing a green portion of the pixel, and a third component addressing
 a blue portion of the pixel.

15. A system comprising:

a processor (702) coupled to a memory circuit (704);
 an organic light emitting diode (OLED) display (350) including a plurality of individually addressable pixels; and
 an OLED display driver circuit (310) as claimed in any of claims 1-9 coupled to the OLED display (350):

said first input (312) receiving said luminance scaling factor from the processor;
 said second input (314) receiving said image input signal from the processor, the image input signal including
 image data to drive the plurality of individually addressable pixels; and
 said luminance scaling circuit (330) coupled to the first input (312) and the second input (314).

*FIG. 1*

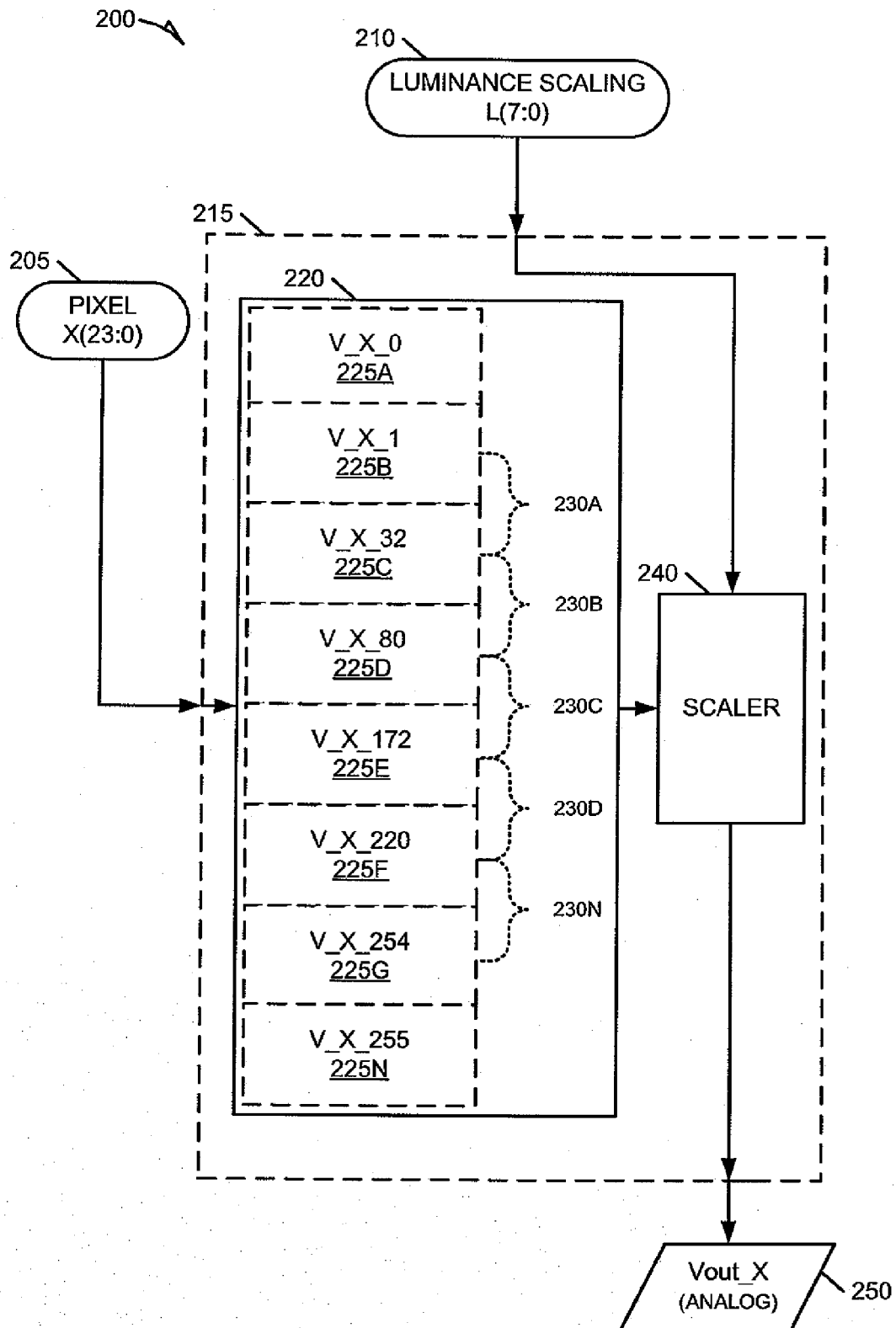


FIG. 2

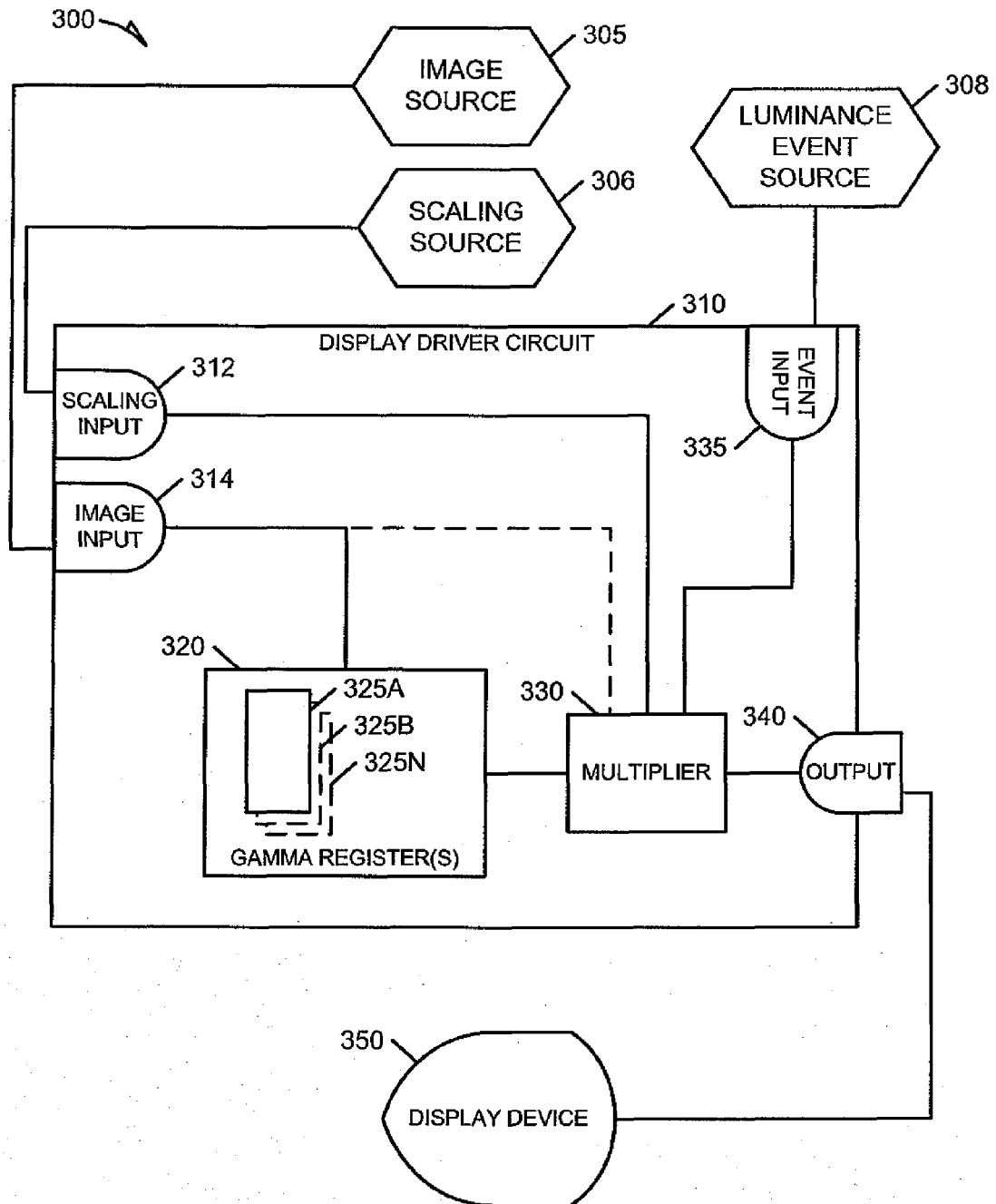
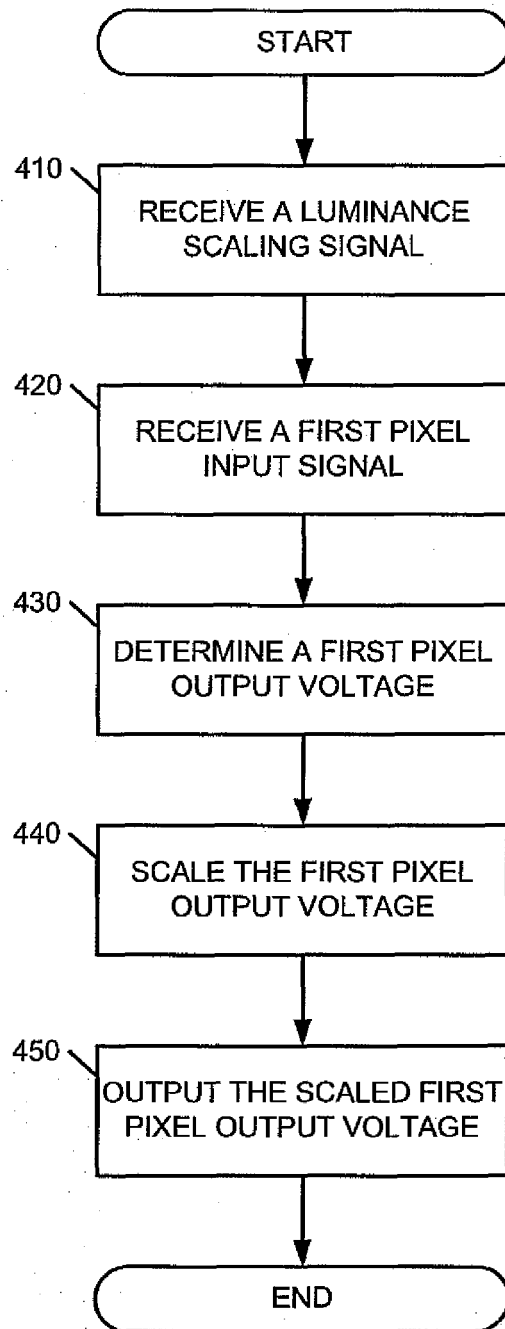
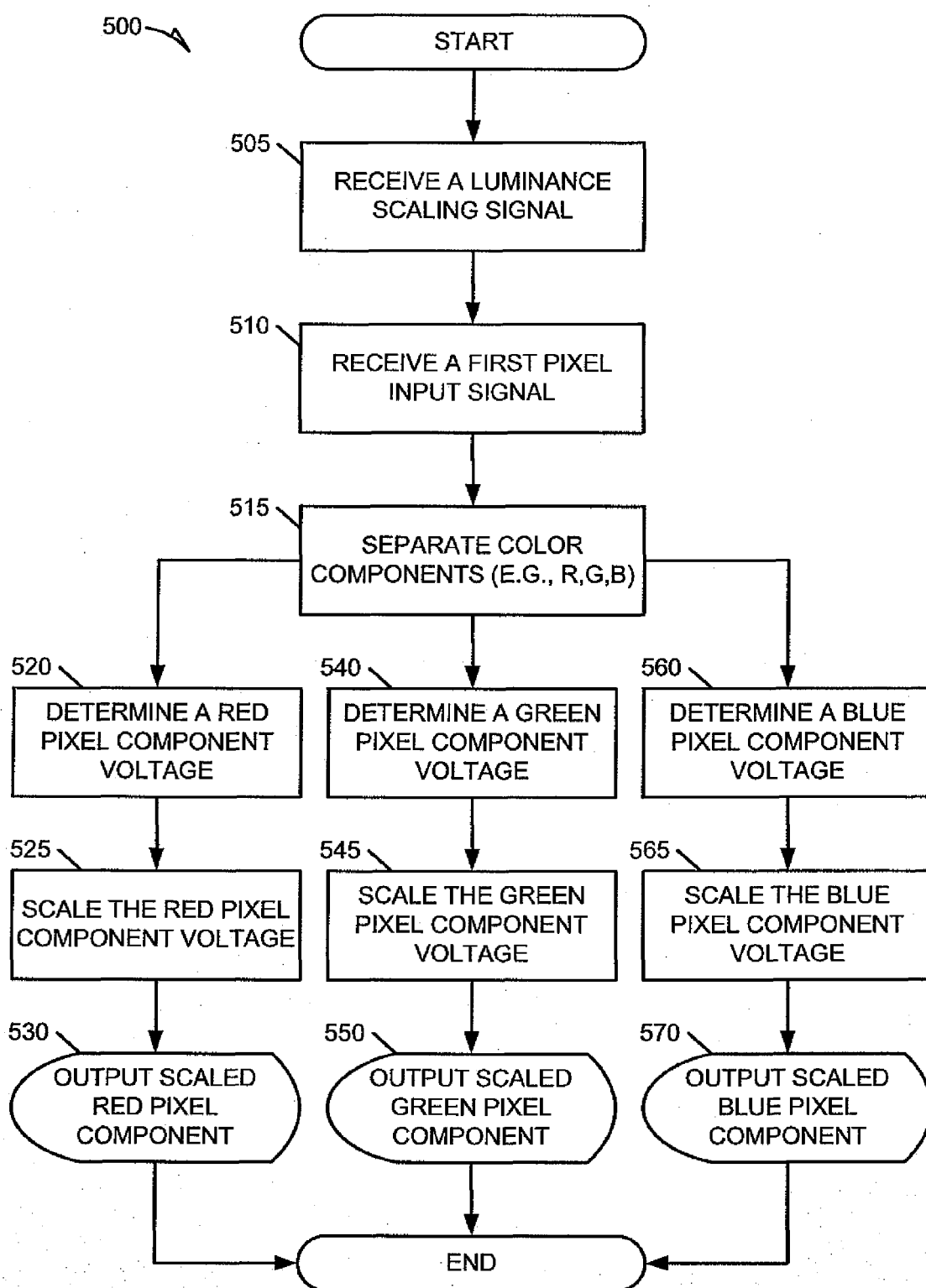
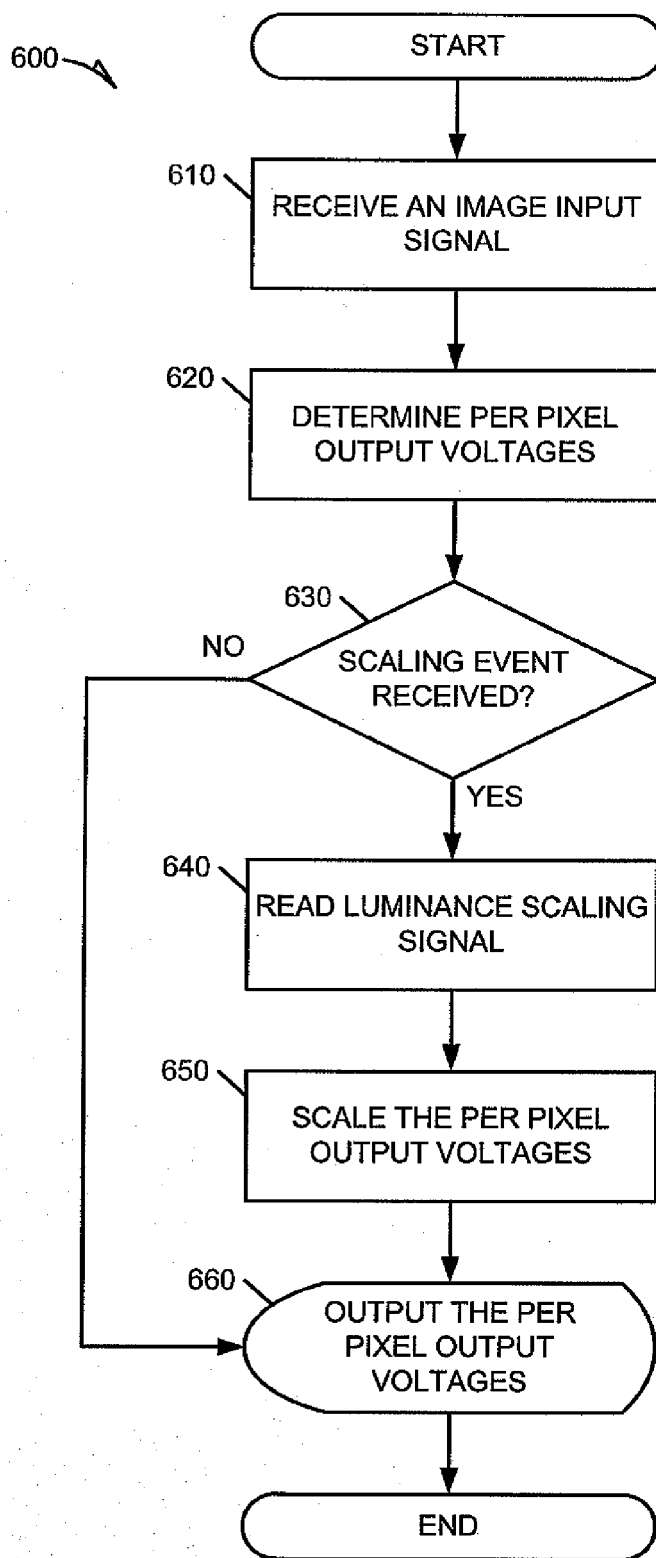


FIG. 3

400

*FIG. 4*

*FIG. 5*

*FIG. 6*

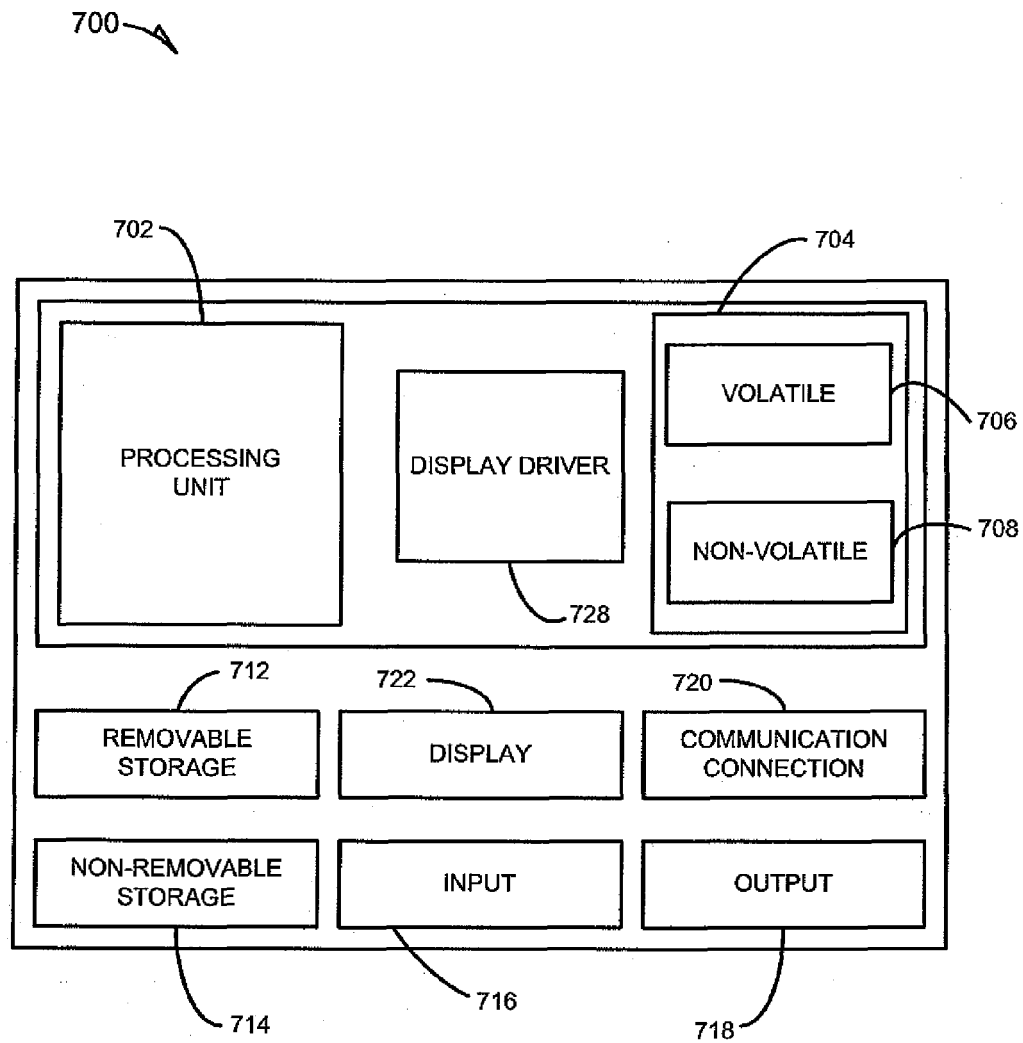


FIG. 7



EUROPEAN SEARCH REPORT

Application Number
EP 11 15 6106

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
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			TECHNICAL FIELDS SEARCHED (IPC)
			G09G
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 14 April 2011	Examiner Bader, Arnaud
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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14-04-2011

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