



(11) **EP 2 518 627 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:
Corrected version no 1 (W1 B1)
Corrections, see
Bibliography INID code(s) 73

(51) Int Cl.:
G06F 11/20 ^(2006.01) **G06F 11/07** ^(2006.01)

(48) Corrigendum issued on:
04.03.2015 Bulletin 2015/10

(45) Date of publication and mention
of the grant of the patent:
27.08.2014 Bulletin 2014/35

(21) Application number: **12165177.2**

(22) Date of filing: **23.04.2012**

(54) **Partial fault processing method in computer system**

Bearbeitungsverfahren von teilweisen Fehlern in einem Computersystem

Procédé de traitement de panne partielle dans un système informatique

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB
GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO
PL PT RO RS SE SI SK SM TR

(30) Priority: **25.04.2011 JP 2011096689**

(43) Date of publication of application:
31.10.2012 Bulletin 2012/44

(73) Proprietor: **Hitachi, Ltd.**
Tokyo (JP)

(72) Inventors:
• **Sekiguchi, Tomoki**
Chiyoda-ku, Tokyo 100-8220, (JP)

• **Ueno, Hitoshi**
Chiyoda-ku, Tokyo 100-8220, (JP)

(74) Representative: **Calderbank, Thomas Roger et al**
Mewburn Ellis LLP
33 Gutter Lane
London
EC2V 8AS (GB)

(56) References cited:
US-A1- 2007 011 495 US-A1- 2008 162 983
US-A1- 2009 144 731 US-B2- 7 134 052

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).
