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(54) **Switching array having circuitry to adjust a temporal distribution of a gating signal applied to the array**

(57) A Micro-electro-mechanical systems (MEMS) switching array (1000) includes circuitry (1008), which may be coupled to a gate line (1006) of the array to adjust a temporal distribution of a gating signal applied to a plu-

rality of MEMS switches that make up the switching array. The temporal distribution may be shaped to reduce a voltage surge that can develop in the switches during switching of electrical current. This voltage surge reduction is conducive to improving the durability of the array.

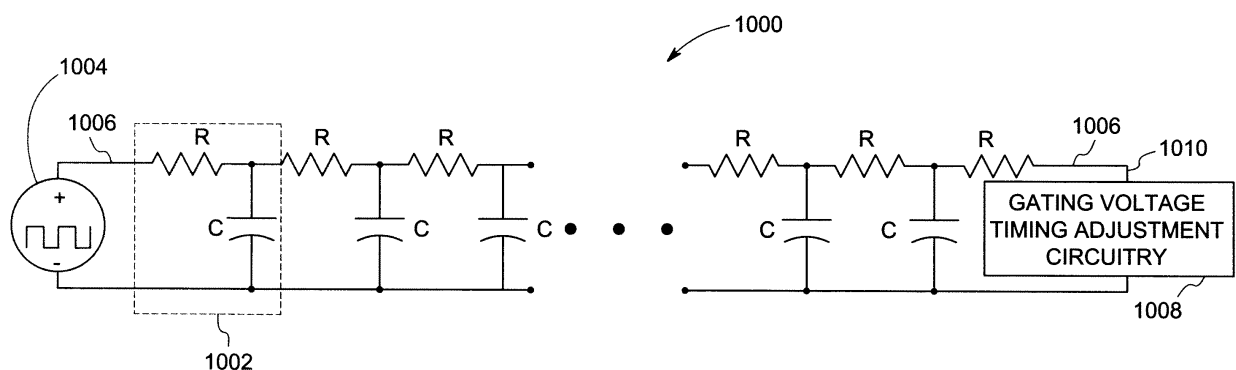


FIG. 10



EUROPEAN SEARCH REPORT

Application Number
EP 12 16 5462

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	EP 1 965 404 A2 (GEN ELECTRIC [US]) 3 September 2008 (2008-09-03) * paragraph [0012] - paragraph [0046]; figures 1-3 *	1-15	INV. H01H1/00 H01H9/54
A	US 2007/139831 A1 (WRIGHT JOSHUA ISAAC [US] ET AL) 21 June 2007 (2007-06-21) * paragraph [0049] - paragraph [0054]; figures 7,8 *	1-15	
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			TECHNICAL FIELDS SEARCHED (IPC)
			H01H
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 19 March 2013	Examiner Drabko, Jacek
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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