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(54) **CONVERTER FOR HIGH VOLTAGE DC DC TRANSMISSION**

UMRICHTER FÜR DC-DC-HOCHSPANNUNGSÜBERTRAGUNG

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(56) References cited:
WO-A1-2008/086760 DE-A1- 10 103 031
US-A- 3 867 643

- **HAGIWARA M ET AL: "PWM control and experiment of modular multilevel converters", POWER ELECTRONICS SPECIALISTS CONFERENCE, 2008. PESC 2008. IEEE, IEEE, PISCATAWAY, NJ, USA, 15 June 2008 (2008-06-15), pages 154-161, XP031299968, ISBN: 978-1-4244-1667-7**
- **RAJU N R ED - INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "A DC link-modulated three-phase converter", CONFERENCE RECORD OF THE 2001 IEEE INDUSTRY APPLICATIONS CONFERENCE. 36TH IAS ANNUAL MEETING . CHICAGO, IL, SEPT. 30 - OCT. 4, 2001; [CONFERENCE RECORD OF THE IEEE INDUSTRY APPLICATIONS CONFERENCE. IAS ANNUAL MEETING], NEW YORK, NY : IEEE, US, vol. 4, 30 September 2001 (2001-09-30), pages 2181-2185, XP010561989, DOI: DOI:10.1109/IAS.2001.955928 ISBN: 978-0-7803-7114-9**
- **LESNICAR A ET AL: "A new modular voltage source inverter topology", INTERNET CITATION, 2003, XP002454302, Retrieved from the Internet: URL:http://www.unibw.de/eit62/forsch/SP/M2 LC/m2lcveroeff [retrieved on 2007-10-10]**

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