



(11) **EP 2 769 451 B8**

(12) **CORRECTED EUROPEAN PATENT SPECIFICATION**

(15) Correction information:
Corrected version no 1 (W1 B1)
Corrections, see
Bibliography INID code(s) 73

(51) Int Cl.:
H02J 9/06 ^(2006.01)

(86) International application number:
PCT/US2011/057196

(48) Corrigendum issued on:
08.01.2020 Bulletin 2020/02

(87) International publication number:
WO 2013/058759 (25.04.2013 Gazette 2013/17)

(45) Date of publication and mention
of the grant of the patent:
04.12.2019 Bulletin 2019/49

(21) Application number: **11782277.5**

(22) Date of filing: **21.10.2011**

(54) **METHOD AND APPARATUS OF MASTER SELECTION LOGIC IN UPS PARALLEL SYSTEM**

VERFAHREN UND VORRICHTUNG FÜR MASTERAUSWAHLLOGIK IN EINEM PARALLELEN
USV-SYSTEM

PROCÉDÉ ET APPAREIL À LOGIQUE DE SÉLECTION DU MODE MAÎTRE DANS UN SYSTÈME
D'ALIMENTATIONS ÉLECTRIQUES SANS COUPURE PARALLÈLES

(84) Designated Contracting States:
AL AT BE BG CH CY CZ DE DK EE ES FI FR GB
GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO
PL PT RO RS SE SI SK SM TR

- **KAYANADATH, Suresh**
Bangalore 560066 (IN)
- **GEORGE, Joy**
Bangalore 560066 (IN)

(43) Date of publication of application:
27.08.2014 Bulletin 2014/35

(74) Representative: **Murgitroyd & Company**
Murgitroyd House
165-169 Scotland Street
Glasgow G5 8PL (GB)

(73) Proprietor: **Schneider Electric IT Corporation**
Boston, MA 02108 (US)

(72) Inventors:

- **AGRAWAL, Mahima**
Bangalore 560066 (IN)

(56) References cited:
JP-A- 2006 318 394 US-A1- 2005 041 445
US-A1- 2005 043 859 US-A1- 2009 234 998
US-B1- 6 396 170

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

EP 2 769 451 B8