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(54) **Resolver-to-digital converter**

(57) According to one aspect, a resolver-to-digital converter induces a first filter 208 configured to receive a first delta-sigma modulated resolver input. A second filter 210 is configured to receive a second delta-sigma modulated resolver input. A summing junction 216 is configured to output a difference between a scaled output of the first filter and a scaled output of the second filter. A controller 222 is configured to generate a controller out-

put based on a product of a demodulator 226 and an output of the summing junction. An integrator 230 is configured to generate an estimated position based on the controller output. The resolver-to-digital converter also includes a compensator configured to generate a compensated estimated position based on the controller output and a compensation offset delay adjustment.

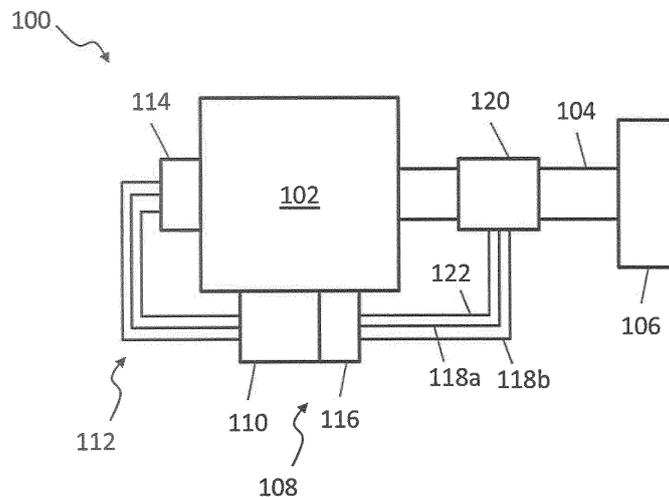


FIG. 1

**EP 2 782 259 A3**



EUROPEAN SEARCH REPORT

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EP 14 15 1925

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			TECHNICAL FIELDS SEARCHED (IPC)
			H03M G01P G01D
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 28 November 2014	Examiner Jesus, Paulo
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ..... & : member of the same patent family, corresponding document	

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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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EP 14 15 1925

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EPO FORM P0459

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82