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(72) Inventors:
• **Wolf, Eric, A.**
Norwood, MA 02062 (US)
• **Corkum, David, L.**
Attleboro, MA 02703 (US)

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(74) Representative: **Legg, Cyrus James Grahame**
Abel & Imray
20 Red Lion Street
London WC1R 4PQ (GB)

(71) Applicant: **Sensata Technologies, Inc.**
Attleboro, MA 02703 (US)

(54) **Circuit designs for induced transient immunity**

(57) A circuit for protecting a voltage supply to a sensor from a transient event is provided. The circuit includes at least one buffer capacitor configured to provide output during the transient event, the output substantially equivalent to output of a regulated supply during normal operation of the sensor. A method of fabrication and a sensor are disclosed.

alent to output of a regulated supply during normal operation of the sensor. A method of fabrication and a sensor are disclosed.

Fig. 1

