



(11)

EP 3 586 465 B9

(12)

## CORRECTED EUROPEAN PATENT SPECIFICATION

(15) Correction information:

**Corrected version no 1 (W1 B1)**  
**Corrections, see**  
**Claims EN 1**

(48) Corrigendum issued on:

20.03.2024 Bulletin 2024/12

(45) Date of publication and mention  
of the grant of the patent:

06.09.2023 Bulletin 2023/36

(21) Application number: 18705220.4

(22) Date of filing: 25.01.2018

(51) International Patent Classification (IPC):

**H04L 5/00(2006.01)**

(52) Cooperative Patent Classification (CPC):

**H04L 5/0044; H04L 5/005; H04L 5/0053;**  
**H04L 5/0055; H04L 25/03866; H04L 27/2613;**  
**H04W 56/00; H03M 13/09; H04L 5/001;**  
**H04L 27/26136; Y02D 30/70**

(86) International application number:

**PCT/US2018/015290**

(87) International publication number:

**WO 2018/156301 (30.08.2018 Gazette 2018/35)**(54) **USAGE OF SYNCHRONIZATION SIGNAL BLOCK INDEX IN NEW RADIO**

VERWENDUNG EINES SYNCHRONISATIONSSIGNALBLOCKINDEX IN NEW RADIO

UTILISATION D'INDICE DE BLOC DE SIGNAL DE SYNCHRONISATION DANS LA NORME NEW RADIO

(84) Designated Contracting States:

**AL AT BE BG CH CY CZ DE DK EE ES FI FR GB**  
**GR HR HU IE IS IT LI LT LU LV MC MK MT NL NO**  
**PL PT RO RS SE SI SK SM TR**

(30) Priority: 23.02.2017 US 201762462872 P  
02.11.2017 US 201715802398(43) Date of publication of application:  
01.01.2020 Bulletin 2020/01(73) Proprietor: **QUALCOMM Incorporated**  
**San Diego, CA 92121-1714 (US)**(72) Inventor: **LUO, Tao**  
**San Diego, California 92121-1714 (US)**

(74) Representative: **Dunlop, Hugh Christopher et al**  
**Maucher Jenkins**  
**Seventh Floor Offices**  
**Artillery House**  
**11-19 Artillery Row**  
**London SW1P 1RT (GB)**

(56) References cited:

**US-A1- 2016 192 335**

- **SEQUANS COMMUNICATIONS:** "Discussion on synchronization procedure in NR", 3GPP DRAFT; R1-1701850 - SS TIME INDEX INDICATION IN NR SYNC, 3RD GENERATION PARTNERSHIP PROJECT (3GPP), MOBILE COMPETENCE CENTRE ; 650, ROUTE DES LUCIOLES ; F-06921 SOPHIA-ANTIPOLIS CEDEX ; FRANCE , vol. RAN WG1, no. Athens, Greece; 20170213 - 20170217 12 February 2017 (2017-02-12), XP051209015, Retrieved from the Internet:  
URL:[http://www.3gpp.org/ftp/Meetings\\_3GPP\\_SYNC/RAN1/Docs/](http://www.3gpp.org/ftp/Meetings_3GPP_SYNC/RAN1/Docs/) [retrieved on 2017-02-12]

Note: Within nine months of the publication of the mention of the grant of the European patent in the European Patent Bulletin, any person may give notice to the European Patent Office of opposition to that patent, in accordance with the Implementing Regulations. Notice of opposition shall not be deemed to have been filed until the opposition fee has been paid. (Art. 99(1) European Patent Convention).

**Description****BACKGROUND**5   **Technical Field**

**[0001]** The present disclosure relates generally to communication systems, and more particularly, to usage of a synchronization signal block index for scrambling.

10   **Introduction**

**[0002]** Wireless communication systems are widely deployed to provide various telecommunication services such as telephony, video, data, messaging, and broadcasts. Typical wireless communication systems may employ multiple-access technologies capable of supporting communication with multiple users by sharing available system resources.

15   Examples of such multiple-access technologies include code division multiple access (CDMA) systems, time division multiple access (TDMA) systems, frequency division multiple access (FDMA) systems, orthogonal frequency division multiple access (OFDMA) systems, single-carrier frequency division multiple access (SC-FDMA) systems, and time division synchronous code division multiple access (TD-SCDMA) systems.

**[0003]** Sequans Communications, "Discussion on synchronization procedure in NR", 3GPP Draft; R1-1701850, 12 February 2017, relates to several methods for signaling the SS-block time index.

**[0004]** These multiple access technologies have been adopted in various telecommunication standards to provide a common protocol that enables different wireless devices to communicate on a municipal, national, regional, and even global level. An example telecommunication standard is 5G New Radio (NR). 5G NR is part of a continuous mobile broadband evolution promulgated by Third Generation Partnership Project (3GPP) to meet new requirements associated with latency, reliability, security, scalability (e.g., with Internet of Things (IoT)), and other requirements. Some aspects of 5G NR may be based on the 4G Long Term Evolution (LTE) standard. There exists a need for further improvements in 5G NR technology. These improvements may also be applicable to other multi-access technologies and the telecommunication standards that employ these technologies.

30   **SUMMARY**

**[0005]** Aspects of the invention are defined in the claims.

**[0006]** In the following, each of the described methods, apparatuses, examples, and aspects which does not fully correspond to the invention as defined in the claims is thus not according to the invention and is, as well as the whole following description, present for illustration purposes only or to highlight specific aspects or features of the claims.

**BRIEF DESCRIPTION OF THE DRAWINGS****[0007]**

40   FIG. 1 is a diagram illustrating an example of a wireless communications system and an access network. FIGs. 2A, 2B, 2C, and 2D are diagrams illustrating examples of a DL subframe, DL channels within the DL subframe, an UL subframe, and UL channels within the UL subframe, respectively, for a 5G/NR frame structure. FIG. 3 is a diagram illustrating an example of a base station and user equipment (UE) in an access network. FIG. 4 is a diagram illustrating a base station in communication with a UE. FIG. 5A is a diagram illustrating an example of an SS burst. FIG. 5B is a diagram illustrating an example of SS bursts for different frequency bands/carriers. FIG. 6A is a diagram illustrating a first example of an SS burst set. FIG. 6B is a diagram illustrating a second example of an SS burst set. FIG. 7 is a diagram illustrating a first exemplary call-flow diagram for a UE and a base station. FIG. 8 is a diagram illustrating a second exemplary call-flow diagram for a UE and a base station. FIG. 9 illustrates flowcharts of methods of wireless communication of a base station. FIG. 10 illustrates flowcharts of methods of wireless communication of a UE. FIG. 11 is a conceptual data flow diagram illustrating the data flow between different means/components in an exemplary base station apparatus. FIG. 12 is a diagram illustrating an example of a hardware implementation for a base station apparatus employing a processing system. FIG. 13 is a conceptual data flow diagram illustrating the data flow between different means/components in an

exemplary base station apparatus.

FIG. 14 is a diagram illustrating an example of a hardware implementation for a base station apparatus employing a processing system.

## 5 DETAILED DESCRIPTION

**[0008]** The detailed description set forth below in connection with the appended drawings is intended as a description of various configurations and is not intended to represent the only configurations in which the concepts described herein may be practiced. The detailed description includes specific details for the purpose of providing a thorough understanding of various concepts. However, it will be apparent to those skilled in the art that these concepts may be practiced without these specific details. In some instances, well known structures and components are shown in block diagram form in order to avoid obscuring such concepts.

**[0009]** Several aspects of telecommunication systems will now be presented with reference to various apparatus and methods. These apparatus and methods will be described in the following detailed description and illustrated in the accompanying drawings by various blocks, components, circuits, processes, algorithms, etc. (collectively referred to as "elements"). These elements may be implemented using electronic hardware, computer software, or any combination thereof. Whether such elements are implemented as hardware or software depends upon the particular application and design constraints imposed on the overall system.

**[0010]** By way of example, an element, or any portion of an element, or any combination of elements may be implemented as a "processing system" that includes one or more processors. Examples of processors include microprocessors, microcontrollers, graphics processing units (GPUs), central processing units (CPUs), application processors, digital signal processors (DSPs), reduced instruction set computing (RISC) processors, systems on a chip (SoC), baseband processors, field programmable gate arrays (FPGAs), programmable logic devices (PLDs), state machines, gated logic, discrete hardware circuits, and other suitable hardware configured to perform the various functionality described throughout this disclosure. One or more processors in the processing system may execute software. Software shall be construed broadly to mean instructions, instruction sets, code, code segments, program code, programs, subprograms, software components, applications, software applications, software packages, routines, subroutines, objects, executables, threads of execution, procedures, functions, etc., whether referred to as software, firmware, middleware, microcode, hardware description language, or otherwise.

**[0011]** Accordingly, in one or more example embodiments, the functions described may be implemented in hardware, software, or any combination thereof. If implemented in software, the functions may be stored on or encoded as one or more instructions or code on a computer-readable medium. Computer-readable media includes computer storage media. Storage media may be any available media that can be accessed by a computer. By way of example, and not limitation, such computer-readable media can comprise a random-access memory (RAM), a read-only memory (ROM), an electrically erasable programmable ROM (EEPROM), optical disk storage, magnetic disk storage, other magnetic storage devices, combinations of the aforementioned types of computer-readable media, or any other medium that can be used to store computer executable code in the form of instructions or data structures that can be accessed by a computer.

**[0012]** FIG. 1 is a diagram illustrating an example of a wireless communications system and an access network 100. The wireless communications system (also referred to as a wireless wide area network (WWAN)) includes base stations 102, UEs 104, and an Evolved Packet Core (EPC) 160. The base stations 102 may include macro cells (high power cellular base station) and/or small cells (low power cellular base station). The macro cells include base stations. The small cells include femtocells, picocells, and microcells.

**[0013]** The base stations 102 (collectively referred to as Evolved Universal Mobile Telecommunications System (UMTS) Terrestrial Radio Access Network (E-UTRAN)) interface with the EPC 160 through backhaul links 132 (e.g., S1 interface). In addition to other functions, the base stations 102 may perform one or more of the following functions: transfer of user data, radio channel ciphering and deciphering, integrity protection, header compression, mobility control functions (e.g., handover, dual connectivity), inter-cell interference coordination, connection setup and release, load balancing, distribution for non-access stratum (NAS) messages, NAS node selection, synchronization, radio access network (RAN) sharing, multimedia broadcast multicast service (MBMS), subscriber and equipment trace, RAN information management (RIM), paging, positioning, and delivery of warning messages. The base stations 102 may communicate directly or indirectly (e.g., through the EPC 160) with each other over backhaul links 134 (e.g., X2 interface). The backhaul links 134 may be wired or wireless.

**[0014]** The base stations 102 may wirelessly communicate with the UEs 104. Each of the base stations 102 may provide communication coverage for a respective geographic coverage area 110. There may be overlapping geographic coverage areas 110. For example, the small cell 102' may have a coverage area 110' that overlaps the coverage area 110 of one or more macro base stations 102. A network that includes both small cell and macro cells may be known as a heterogeneous network. A heterogeneous network may also include Home Evolved Node Bs (eNBs) (HeNBs), which may provide service to a restricted group known as a closed subscriber group (CSG). The communication links 120

between the base stations 102 and the UEs 104 may include uplink (UL) (also referred to as reverse link) transmissions from a UE 104 to a base station 102 and/or downlink (DL) (also referred to as forward link) transmissions from a base station 102 to a UE 104. The communication links 120 may use multiple-input and multiple-output (MIMO) antenna technology, including spatial multiplexing, beamforming, and/or transmit diversity. The communication links may be 5 through one or more carriers. The base stations 102 / UEs 104 may use spectrum up to Y MHz (e.g., 5, 10, 15, 20, 100 MHz) bandwidth per carrier allocated in a carrier aggregation of up to a total of Yx MHz (x component carriers) used for transmission in each direction. The carriers may or may not be adjacent to each other. Allocation of carriers may be 10 asymmetric with respect to DL and UL (e.g., more or less carriers may be allocated for DL than for UL). The component carriers may include a primary component carrier and one or more secondary component carriers. A primary component carrier may be referred to as a primary cell (PCell) and a secondary component carrier may be referred to as a secondary cell (SCell).

**[0015]** Certain UEs 104 may communicate with each other using device-to-device (D2D) communication link 192. The D2D communication link 192 may use the DL/UL WWAN spectrum. The D2D communication link 192 may use one or 15 more sidelink channels, such as a physical sidelink broadcast channel (PSBCH), a physical sidelink discovery channel (PSDCH), a physical sidelink shared channel (PSSCH), and a physical sidelink control channel (PSCCH). D2D communication may be through a variety of wireless D2D communications systems, such as for example, FlashLinQ, WiMedia, Bluetooth, ZigBee, Wi-Fi based on the IEEE 802.11 standard, LTE, or NR.

**[0016]** The wireless communications system may further include a Wi-Fi access point (AP) 150 in communication with 20 Wi-Fi stations (STAs) 152 via communication links 154 in a 5 GHz unlicensed frequency spectrum. When communicating in an unlicensed frequency spectrum, the STAs 152 / AP 150 may perform a clear channel assessment (CCA) prior to communicating in order to determine whether the channel is available.

**[0017]** The small cell 102' may operate in a licensed and/or an unlicensed frequency spectrum. When operating in an unlicensed frequency spectrum, the small cell 102' may employ NR and use the same 5 GHz unlicensed frequency spectrum as used by the Wi-Fi AP 150. The small cell 102', employing NR in an unlicensed frequency spectrum, may 25 boost coverage to and/or increase capacity of the access network.

**[0018]** The gNodeB (gNB) 180 may operate in millimeter wave (mmW) frequencies and/or near mmW frequencies in communication with the UE 104. When the gNB 180 operates in mmW or near mmW frequencies, the gNB 180 may be referred to as an mmW base station. Extremely high frequency (EHF) is part of the RF in the electromagnetic spectrum. EHF has a range of 30 GHz to 300 GHz and a wavelength between 1 millimeter and 10 millimeters. Radio waves in the 30 band may be referred to as a millimeter wave. Near mmW may extend down to a frequency of 3 GHz with a wavelength of 100 millimeters. The super high frequency (SHF) band extends between 3 GHz and 30 GHz, also referred to as centimeter wave. Communications using the mmW / near mmW radio frequency band has extremely high path loss and a short range. The mmW base station 180 may utilize beamforming 184 with the UE 104 to compensate for the extremely high path loss and short range.

**[0019]** The EPC 160 may include a Mobility Management Entity (MME) 162, other MMEs 164, a Serving Gateway 166, a Multimedia Broadcast Multicast Service (MBMS) Gateway 168, a Broadcast Multicast Service Center (BM-SC) 170, and a Packet Data Network (PDN) Gateway 172. The MME 162 may be in communication with a Home Subscriber Server (HSS) 174. The MME 162 is the control node that processes the signaling between the UEs 104 and the EPC 160. Generally, the MME 162 provides bearer and connection management. All user Internet protocol (IP) packets are 40 transferred through the Serving Gateway 166, which itself is connected to the PDN Gateway 172. The PDN Gateway 172 provides UE IP address allocation as well as other functions. The PDN Gateway 172 and the BM-SC 170 are connected to the IP Services 176. The IP Services 176 may include the Internet, an intranet, an IP Multimedia Subsystem (IMS), a PS Streaming Service, and/or other IP services. The BM-SC 170 may provide functions for MBMS user service provisioning and delivery. The BM-SC 170 may serve as an entry point for content provider MBMS transmission, may 45 be used to authorize and initiate MBMS Bearer Services within a public land mobile network (PLMN), and may be used to schedule MBMS transmissions. The MBMS Gateway 168 may be used to distribute MBMS traffic to the base stations 102 belonging to a Multicast Broadcast Single Frequency Network (MBSFN) area broadcasting a particular service, and may be responsible for session management (start/stop) and for collecting eMBMS related charging information.

**[0020]** The base station may also be referred to as a gNB, Node B, evolved Node B (eNB), an access point, a base 50 transceiver station, a radio base station, a radio transceiver, a transceiver function, a basic service set (BSS), an extended service set (ESS), or some other suitable terminology. The base station 102 provides an access point to the EPC 160 for a UE 104. Examples of UEs 104 include a cellular phone, a smart phone, a session initiation protocol (SIP) phone, a laptop, a personal digital assistant (PDA), a satellite radio, a global positioning system, a multimedia device, a video device, a digital audio player (e.g., MP3 player), a camera, a game console, a tablet, a smart device, a wearable device, a vehicle, an electric meter, a gas pump, a large or small kitchen appliance, a healthcare device, an implant, a display, or any other similar functioning device. Some of the UEs 104 may be referred to as IoT devices (e.g., parking meter, gas pump, toaster, vehicles, heart monitor, etc.). The UE 104 may also be referred to as a station, a mobile station, a subscriber station, a mobile unit, a subscriber unit, a wireless unit, a remote unit, a mobile device, a wireless device, a 55

wireless communications device, a remote device, a mobile subscriber station, an access terminal, a mobile terminal, a wireless terminal, a remote terminal, a handset, a user agent, a mobile client, a client, or some other suitable terminology.

**[0021]** Referring again to FIG. 1, the UE 104 / base station 180 are configured to scramble/descramble information based on an SS block index (or any part or subset of the SS block index), where the SS block index indexes a particular SS block within an SS burst within an SS burst set (198). The information is scrambled based on the SS block index before being transmitted and/or is descrambled based on the SS block index after being received.

**[0022]** FIG. 2A is a diagram 200 illustrating an example of a DL subframe within a 5G/NR frame structure. FIG. 2B is a diagram 230 illustrating an example of channels within a DL subframe. FIG. 2C is a diagram 250 illustrating an example of an UL subframe within a 5G/NR frame structure. FIG. 2D is a diagram 280 illustrating an example of channels within an UL subframe. The 5G/NR frame structure may be FDD in which for a particular set of subcarriers (carrier system bandwidth), subframes within the set of subcarriers are dedicated for either DL or UL, or may be TDD in which for a particular set of subcarriers (carrier system bandwidth), subframes within the set of subcarriers are dedicated for both DL and UL. In the examples provided by FIGs. 2A, 2C, the 5G/NR frame structure is assumed to be TDD, with subframe 4 a DL subframe and subframe 7 an UL subframe. While subframe 4 is illustrated as providing just DL and subframe 7 is illustrated as providing just UL, any particular subframe may be split into different subsets that provide both UL and DL. Note that the description *infra* applies also to a 5G/NR frame structure that is FDD.

**[0023]** Other wireless communication technologies may have a different frame structure and/or different channels. A frame (10 ms) may be divided into 10 equally sized subframes (1 ms). Each subframe may include one or more time slots. Each slot may include 7 or 14 symbols, depending on the slot configuration. For slot configuration 0, each slot may include 14 symbols, and for slot configuration 1, each slot may include 7 symbols. The number of slots within a subframe is based on the slot configuration and the numerology. For slot configuration 0, different numerologies 0 to 5 allow for 1, 2, 4, 8, 16, and 32 slots, respectively, per subframe. For slot configuration 1, different numerologies 0 to 2 allow for 2, 4, and 8 slots, respectively, per subframe. The subcarrier spacing and symbol length/duration are a function of the numerology. The subcarrier spacing may be equal to  $2^\mu * 15 \text{ kHz}$ , where  $\mu$  is the numerology 0-5. The symbol length/duration is inversely related to the subcarrier spacing. FIGs. 2A, 2C provide an example of slot configuration 1 with 7 symbols per slot and numerology 0 with 2 slots per subframe. The subcarrier spacing is 15 kHz and symbol duration is approximately 66.7  $\mu\text{s}$ .

**[0024]** A resource grid may be used to represent the frame structure. Each time slot includes a resource block (RB) (also referred to as physical RBs (PRBs)) that extends 12 consecutive subcarriers. The resource grid is divided into multiple resource elements (REs). The number of bits carried by each RE depends on the modulation scheme.

**[0025]** As illustrated in FIG. 2A, some of the REs carry reference (pilot) signals (RS) for the UE (indicated as R). The RS may include demodulation RS (DMRS) and channel state information reference signals (CSI-RS) for channel estimation at the UE. The RS may also include beam measurement RS (BRS), beam refinement RS (BRRS), and phase-noise tracking RS (PT-RS).

**[0026]** FIG. 2B illustrates an example of various channels within a DL subframe of a frame. The physical control format indicator channel (PCFICH) is within symbol 0 of slot 0, and carries a control format indicator (CFI) that indicates whether the physical downlink control channel (PDCCH) occupies 1, 2, or 3 symbols (FIG. 2B illustrates a PDCCH that occupies 3 symbols). The PDCCH carries downlink control information (DCI) within one or more control channel elements (CCEs), each CCE including nine RE groups (REGs), each REG including four consecutive REs in an OFDM symbol. A UE may be configured with a UE-specific enhanced PDCCH (ePDCCH) that also carries DCI. The ePDCCH may have 2, 4, or 8 RB pairs (FIG. 2B shows two RB pairs, each subset including one RB pair). The physical hybrid automatic repeat request (ARQ) (HARQ) indicator channel (PHICH) is also within symbol 0 of slot 0 and carries the HARQ indicator (HI) that indicates HARQ acknowledgement (ACK) / negative ACK (NACK) feedback based on the physical uplink shared channel (PUSCH). The primary synchronization channel (PSCH) may be within symbol 6 of slot 0 within subframes 0 and 5 of a frame. The PSCH carries a primary synchronization signal (PSS) that is used by a UE 104 to determine subframe/symbol timing and a physical layer identity. The secondary synchronization channel (SSCH) may be within symbol 5 of slot 0 within subframes 0 and 5 of a frame. The SSCH carries a secondary synchronization signal (SSS) that is used by a UE to determine a physical layer cell identity group number and radio frame timing. Based on the physical layer identity and the physical layer cell identity group number, the UE can determine a physical cell identifier (PCI). Based on the PCI, the UE can determine the locations of the aforementioned DL-RS. The physical broadcast channel (PBCH), which carries a master information block (MIB), may be logically grouped with the PSCH and SSCH to form a synchronization signal (SS)/PBCH block. The MIB provides a number of RBs in the DL system bandwidth, a PHICH configuration, and a system frame number (SFN). The physical downlink shared channel (PDSCH) carries user data, broadcast system information not transmitted through the PBCH such as system information blocks (SIBs), and paging messages.

**[0027]** As illustrated in FIG. 2C, some of the REs carry demodulation reference signals (DMRS) for channel estimation at the base station. The UE may additionally transmit sounding reference signals (SRS) in the last symbol of a subframe. The SRS may have a comb structure, and a UE may transmit SRS on one of the combs. The SRS may be used by a

base station for channel quality estimation to enable frequency-dependent scheduling on the UL.

**[0028]** FIG. 2D illustrates an example of various channels within an UL subframe of a frame. A physical random access channel (PRACH) may be within one or more subframes within a frame based on the PRACH configuration. The PRACH may include six consecutive RB pairs within a subframe. The PRACH allows the UE to perform initial system access and achieve UL synchronization. A physical uplink control channel (PUCCH) may be located on edges of the UL system bandwidth. The PUCCH carries uplink control information (UCI), such as scheduling requests, a channel quality indicator (CQI), a precoding matrix indicator (PMI), a rank indicator (RI), and HARQ ACK/NACK feedback. The PUSCH carries data, and may additionally be used to carry a buffer status report (BSR), a power headroom report (PHR), and/or UCI.

**[0029]** FIG. 3 is a block diagram of a base station 310 in communication with a UE 350 in an access network. In the DL, IP packets from the EPC 160 may be provided to a controller/processor 375. The controller/processor 375 implements layer 3 and layer 2 functionality. Layer 3 includes a radio resource control (RRC) layer, and layer 2 includes a packet data convergence protocol (PDCP) layer, a radio link control (RLC) layer, and a medium access control (MAC) layer. The controller/processor 375 provides RRC layer functionality associated with broadcasting of system information (e.g., MIB, SIBs), RRC connection control (e.g., RRC connection paging, RRC connection establishment, RRC connection modification, and RRC connection release), inter radio access technology (RAT) mobility, and measurement configuration for UE measurement reporting; PDCP layer functionality associated with header compression / decompression, security (ciphering, deciphering, integrity protection, integrity verification), and handover support functions; RLC layer functionality associated with the transfer of upper layer packet data units (PDUs), error correction through ARQ, concatenation, segmentation, and reassembly of RLC service data units (SDUs), re-segmentation of RLC data PDUs, and reordering of RLC data PDUs; and MAC layer functionality associated with mapping between logical channels and transport channels, multiplexing of MAC SDUs onto transport blocks (TBs), demultiplexing of MAC SDUs from TBs, scheduling information reporting, error correction through HARQ, priority handling, and logical channel prioritization.

**[0030]** The transmit (TX) processor 316 and the receive (RX) processor 370 implement layer 1 functionality associated with various signal processing functions. Layer 1, which includes a physical (PHY) layer, may include error detection on the transport channels, forward error correction (FEC) coding/decoding of the transport channels, interleaving, rate matching, mapping onto physical channels, modulation/demodulation of physical channels, and MIMO antenna processing. The TX processor 316 handles mapping to signal constellations based on various modulation schemes (e.g., binary phase-shift keying (BPSK), quadrature phase-shift keying (QPSK), M-phase-shift keying (M-PSK), M-quadrature amplitude modulation (M-QAM)). The coded and modulated symbols may then be split into parallel streams. Each stream may then be mapped to an OFDM subcarrier, multiplexed with a reference signal (e.g., pilot) in the time and/or frequency domain, and then combined together using an Inverse Fast Fourier Transform (IFFT) to produce a physical channel carrying a time domain OFDM symbol stream. The OFDM stream is spatially precoded to produce multiple spatial streams. Channel estimates from a channel estimator 374 may be used to determine the coding and modulation scheme, as well as for spatial processing. The channel estimate may be derived from a reference signal and/or channel condition feedback transmitted by the UE 350. Each spatial stream may then be provided to a different antenna 320 via a separate transmitter 318TX. Each transmitter 318TX may modulate an RF carrier with a respective spatial stream for transmission.

**[0031]** At the UE 350, each receiver 354RX receives a signal through its respective antenna 352. Each receiver 354RX recovers information modulated onto an RF carrier and provides the information to the receive (RX) processor 356. The TX processor 368 and the RX processor 356 implement layer 1 functionality associated with various signal processing functions. The RX processor 356 may perform spatial processing on the information to recover any spatial streams destined for the UE 350. If multiple spatial streams are destined for the UE 350, they may be combined by the RX processor 356 into a single OFDM symbol stream. The RX processor 356 then converts the OFDM symbol stream from the time-domain to the frequency domain using a Fast Fourier Transform (FFT). The frequency domain signal comprises a separate OFDM symbol stream for each subcarrier of the OFDM signal. The symbols on each subcarrier, and the reference signal, are recovered and demodulated by determining the most likely signal constellation points transmitted by the base station 310. These soft decisions may be based on channel estimates computed by the channel estimator 358. The soft decisions are then decoded and deinterleaved to recover the data and control signals that were originally transmitted by the base station 310 on the physical channel. The data and control signals are then provided to the controller/processor 359, which implements layer 3 and layer 2 functionality.

**[0032]** The controller/processor 359 can be associated with a memory 360 that stores program codes and data. The memory 360 may be referred to as a computer-readable medium. In the UL, the controller/processor 359 provides demultiplexing between transport and logical channels, packet reassembly, deciphering, header decompression, and control signal processing to recover IP packets from the EPC 160. The controller/processor 359 is also responsible for error detection using an ACK and/or NACK protocol to support HARQ operations.

**[0033]** Similar to the functionality described in connection with the DL transmission by the base station 310, the controller/processor 359 provides RRC layer functionality associated with system information (e.g., MIB, SIBs) acquisition, RRC connections, and measurement reporting; PDCP layer functionality associated with header compression / decompression, and security (ciphering, deciphering, integrity protection, integrity verification); RLC layer functionality

associated with the transfer of upper layer PDUs, error correction through ARQ, concatenation, segmentation, and reassembly of RLC SDUs, re-segmentation of RLC data PDUs, and reordering of RLC data PDUs; and MAC layer functionality associated with mapping between logical channels and transport channels, multiplexing of MAC SDUs onto TBs, demultiplexing of MAC SDUs from TBs, scheduling information reporting, error correction through HARQ, priority handling, and logical channel prioritization.

**[0034]** Channel estimates derived by a channel estimator 358 from a reference signal or feedback transmitted by the base station 310 may be used by the TX processor 368 to select the appropriate coding and modulation schemes, and to facilitate spatial processing. The spatial streams generated by the TX processor 368 may be provided to different antenna 352 via separate transmitters 354TX. Each transmitter 354TX may modulate an RF carrier with a respective spatial stream for transmission.

**[0035]** The UL transmission is processed at the base station 310 in a manner similar to that described in connection with the receiver function at the UE 350. Each receiver 318RX receives a signal through its respective antenna 320. Each receiver 318RX recovers information modulated onto an RF carrier and provides the information to a RX processor 370.

**[0036]** The controller/processor 375 can be associated with a memory 376 that stores program codes and data. The memory 376 may be referred to as a computer-readable medium. In the UL, the controller/processor 375 provides demultiplexing between transport and logical channels, packet reassembly, deciphering, header decompression, control signal processing to recover IP packets from the UE 350. IP packets from the controller/processor 375 may be provided to the EPC 160. The controller/processor 375 is also responsible for error detection using an ACK and/or NACK protocol to support HARQ operations.

**[0037]** FIG. 4 is a diagram 400 illustrating a base station 402 in communication with a UE 404. Referring to FIG. 4, when the UE 404 turns on, the UE 404 searches for a nearby NR network. The UE 404 discovers the base station 402, which belongs to an NR network. The base station 402 transmits an SS block including the PSS, SSS, and the PBCH (including the MIB) periodically in different transmit directions 402a - 402h. The UE 404 receives the transmission 402e including the PSS, SSS, and PBCH. Based on the received SS block, the UE 404 synchronizes to the NR network and camps on a cell associated with the base station 402.

**[0038]** As discussed *supra*, the PSS, SSS, and PBCH may be transmitted within an SS block. Each SS block has a corresponding SS block index (also referred to as SS/PBCH block index) indicated in FIG. 4 as one of 0, 1, ..., n-1. The PSS, SSS, and PBCH may be time division multiplexed and/or frequency division multiplexed with the SS block (FIG. 4 shows the PSS, SSS, and PBCH time division multiplexed within an SS block). While FIG. 4 shows the PSS, SSS, and PBCH as consecutive in time, the PSS, SSS, and PBCH may be non-consecutive in time, and therefore may be spaced from each other by one or more slots/symbols (i.e., may not be adjacent to each other in time). The SS block may include other signals/channels, and therefore other signals/channels other than the PSS, SSS, and PBCH may be multiplexed into the SS block. One or multiple SS blocks make up an SS burst. The number of SS blocks  $n$  in an SS burst may vary. SS blocks may or may not be consecutive with respect to the corresponding SS block index. SS blocks within an SS burst may or may not be the same. One or multiple SS bursts make up an SS burst set. The periodicity (period  $P$ ) of SS bursts and the number of SS bursts in an SS burst set may vary. The number of SS bursts within an SS burst set is finite. The transmission of SS burst sets may be periodic or aperiodic.

**[0039]** FIG. 5A is a diagram 500 illustrating an example of an SS burst. FIG. 5B is a diagram 550 illustrating an example of SS bursts for different frequency bands/carriers. FIG. 6A is a diagram 600 illustrating a first example of an SS burst set. FIG. 6B is a diagram 650 illustrating a second example of an SS burst set. As illustrated in the diagram 500 of FIG. 5A, an SS burst includes a plurality of SS blocks corresponding to SS block indices 0, 1, ..., 7. A subset 502 of the SS blocks (see indices 2, 5, in this example) may be preconfigured such that they may be off (i.e., not transmitted) to allow for transmission of an UL control block instead. As illustrated in the diagram 550 of FIG. 5B, different bands may have different SS burst configurations. For example, in band X, an SS burst may include SS blocks 0, 1, ..., 7, with SS blocks 2, 5 being preconfigured for on/off to allow for transmission of an UL control block instead. For another example, in band Y, an SS burst may include SS blocks 0, 1, 3, 4, 6, 7 with SS blocks 1, 4, 7 being preconfigured for on/off to allow for transmission of an UL control block instead. The illustrated SS burst may be an  $i^{\text{th}}$  SS burst within an SS burst set. As illustrated in the diagram 600 of FIG. 6A, in a first example, an SS burst set may include  $K$  different SS bursts 0, 1, ...,  $K-1$ . The time length of the SS burst set may be  $N*10$  ms, where  $N$  is an integer. Referring to FIG. 6B, the periodicity (period  $P_1$ ) of an SS burst in an SS burst set is how often an SS burst is transmitted in an SS burst set. The periodicity of beam sweeping (period  $P_2$ ) is how often a beam sweeping SS burst repeats itself in an SS burst set. A UE may use repetitive SS bursts in an SS burst set to filter a reference signal received power (RSRP) over time for the same beam direction and/or to train sub-arrays. In the example of FIG. 6B, the SS burst set has a time length of 80 ms, the periodicity  $P_1$  of the SS burst is 20 ms, and the periodicity  $P_2$  of beam sweeping is 40 ms.

**[0040]** As discussed *supra*, the SS block index may be used to indicate an SS block within an SS burst or within an SS burst set. When the SS block index is used to indicate an SS block within an SS burst, SS bursts may have an SS burst index to indicate the particular SS burst within an SS burst set. As such, an SS block index can indicate an SS

block within an SS burst within an SS burst set (e.g., SS block indices are 0, 1, ...,  $n^*K-1$  for SS blocks in an SS burst set, where there are  $n$  SS blocks per SS burst and  $K$  SS bursts in an SS burst set), or the combination of an SS block index and an SS burst index can indicate an SS block within an SS burst within an SS burst set (e.g., SS block indices are 0, 1, ...,  $n-1$  for SS blocks in each SS burst set, and SS burst indices are 0, 1, ...,  $K-1$  for SS bursts in an SS burst set). Herein, SS block index may refer to one or more indices for indicating an SS block within an SS burst within an SS burst set. A mapping function may be used to map an SS block index to a logic index. There may be a one to one mapping, with one SS block index mapped to one logic index. Alternatively, there may be a many to one mapping, with multiple SS block indices mapped to one logic index.

**[0041]** FIG. 7 is a diagram 700 illustrating a first exemplary call-flow diagram for a UE 702 and a base station 704. As illustrated in FIG. 7, at 710, the base station 704 determines an SS block index associated with an SS block for transmission at 714. As discussed *supra*, the SS block may include at least one of a PSS, an SSS, or a PBCH. The base station 704 may determine the SS block index based on which SS block is being transmitted in a particular beam of a set of beams (see FIG. 4, 402a-402h).

**[0042]** At 712, the base station 704 scrambles information based on the determined SS block index. The base station 704 may scramble the information by generating a scrambling sequence based on a sequence/scrambling initialization that is based at least in part on the SS block index. For example, the scrambling initialization for scrambling the information may be based on any subset of the SS block index. For another example, the scrambling initialization for scrambling

the information may be based on both a subset of the SS block index and the cell ID of the base station 704 ( $N_{ID}^{CELL}$ ). If the SS block index is  $m$  bits, the subset of the SS block index may include 1 to  $m$  bits. In one example, the subset of the SS block index may be  $X$  least significant bits (LSBs) of the SS block index, where  $X$  may be 2 or 3. In one example, the SS block index is 6 bits (e.g.,  $b_5b_4b_3b_2b_1b_0$ ) and the 3 LSBs (e.g.,  $b_2b_1b_0$ ) of the SS block index are used for the scrambling initialization for scrambling the information. The information before scrambling may be encoded and/or unencoded, and may be scrambled based on the generated scrambling sequence. The information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. In one configuration, the reference signal is at least one of CSI-RS, measurement RS (MRS) (also referred to as mobility RS), DMRS (for a PDCCH, a PDSCH, or a PBCH), or PT-RS. In one configuration, the data is for a PDSCH, the paging information is for a paging channel (PCH), the control information is for a PDCCH, and the broadcast information is for a PBCH. When the information includes a scrambled CRC, the control information associated with the scrambled CRC may or may not be scrambled itself based on the SS block index.

**[0043]** For example, the sequence initialization for scrambling DMRS (e.g., PBCH DMRS) may be based on the cell ID of the base station 704 and on the 3 LSBs of the SS block index. Specifically, the initialization for the PBCH DMRS

may be  $c_{init} = 2^{11} \cdot (I_{SSB} + 1) \cdot ([N_{ID}/4] + 1) + 2^6 \cdot (I_{SSB} + 1) + \text{mod}(N_{ID}, 4)$ , where  $I_{SSB}$  is the SS block index, and where for max length  $L=4$ ,  $I_{SSB} = I_{SSB} + 4HF$  where  $HF=0$  in the first half frame of a radio frame and  $HF=1$  in the second half frame of a radio frame, and for max length  $L=8$ , and max  $L=64$ ,  $I_{SSB} = I_{SSB}$ .

**[0044]** For another example, assume the information is broadcast information for a PBCH. Before the encoding/CRC process, the base station 704 may scramble the PBCH payload based on a scrambling sequence that is based on the

cell ID of the base station 704 (scrambling sequence initialization  $C_{init} = N_{ID}^{cell}$ ). Subsequently, after the encoding/CRC process, the base station 704 may scramble the encoded PBCH based on a scrambling sequence that is based on its

cell ID (scrambling sequence initialization  $C_{init} = N_{ID}^{cell}$ ) and  $X$  LSBs of the SS block index. The  $X$  LSBs of the SS block index are used to determine a sequential non-overlapping portion of the sequence. The sequence may be a Gold sequence of length  $M(2^X)$ , where  $M$  is the number of bits to be scrambled. The sequence may be partitioned into  $2^X$  non-overlapping portions. The  $X$  LSBs bits of the SS block index uniquely identify indices of each of the non-overlapping portion of the sequence, where  $X=2$  for max length  $L=4$ , and  $X=3$  for max length  $L=8$  or  $64$ . For  $X=3$ , the sequence index (e.g.,  $b_2b_1b_0$ ) used for each PBCH may be as follows (where  $M$  is the number of bits to be scrambled):

(b2)	(b1)	(b0)	Sequence Index Used for Each PBCH
0	0	0	0 ~ M-1
0	0	1	M ~ 2M-1
0	1	0	2M ~ 3M-1
0	1	1	3M ~ 4M-1

(continued)

(b2)	(b1)	(b0)	Sequence Index Used for Each PBCH
1	0	0	4M ~ 5M-1
1	0	1	5M ~ 6M-1
1	1	0	6M ~ 7M-1
1	1	1	7M ~ 8M-1

**[0045]** At 714, the base station 704 transmits the SS block and the scrambled information to the UE 702. The SS block may include the scrambled information.

**[0046]** The base station 704 may use the SS block index to scramble a CRC when encoding a DL control payload. The base station 704 may use the scrambled CRC to convey a quasi-co-location (QCL) parameter for a control channel without explicit signaling. Two antenna ports are said to be quasi co-located if properties of the channel over which a symbol on one antenna port is conveyed can be inferred from the channel over which a symbol on the other antenna port is conveyed. QCL may support beam management functionality (at least including spatial parameters), frequency/timing offset estimation functionality (at least including Doppler/delay parameters), and radio resource management (RRM) functionality (at least including average gain). In NR, all or a subset of DMRS antenna ports may be quasi co-located. The conveyed QCL parameter may indicate QCL of reference signals associated with a beam pair including a beam associated with the control channel and the corresponding SS block index. By decoding the control channel and obtaining the SS block index, the UE 702 may be able to determine the QCL parameter associated with SS block index. The control channel may include a common control channel (CCCH) and/or a UE specific control channel (e.g., dedicated control channel (DCCH)). When the UE 702 decodes such DL control channel, the UE 702 may use the decoded DL control information or may discard the decoded DL control information depending on whether the UE 702 is configured to receive such control information scrambled by the SS block index. Referring again to 710, the base station 704 may determine an SS block to be used by the UE 702 in association with QCL of reference signals. In such a configuration, at 712, the base station 704 may generate a CRC based on control information to be transmitted to the UE 702, and may scramble the CRC based on the determined SS block index. At 714, the base station 704 may send the control information and the SS-block-index scrambled CRC to the UE 702.

**[0047]** At 714, the UE 702 receives the SS block including information scrambled based on the SS block index associated with the SS block.

**[0048]** At 716, the UE 702 descrambles the scrambled information based on the SS block index. The UE 702 may descramble the information itself based on the SS block index, or may descramble a CRC associated with the information based on the SS block index. In the latter case, the UE 702 may decode the information based on the descrambled CRC.

**[0049]** When the scrambled information 714 includes a CRC scrambled based on an SS block index, the UE 702 may descramble the CRC based on the SS block index, and decode received control information based on the descrambled CRC (e.g., decode received control information, generate a CRC based on the decoded control information, and compare the generated CRC to the descrambled CRC to determine whether the control information was decoded/descrambled successfully). Subsequently, at 718, the UE may determine a QCL parameter based on the SS block index used to descramble the CRC. As discussed *supra*, the QCL parameter may indicate QCL of reference signals associated with a beam pair including a beam associated with the control channel and the corresponding SS block index.

**[0050]** FIG. 8 is a diagram 800 illustrating a second exemplary call-flow diagram for a UE and a base station. At 810, a UE 804 determines an SS block index associated with an SS block for reception. The SS block associated with the SS block index may have been previously received or may be received in the future. At 810, the UE 804 may receive an uplink grant from the base station 802, and may determine the SS block index based on the uplink grant. For example, if the UE receives an UL grant and an SS block in or associated with a beam, the UE may determine the SS block index to be the SS block index associated with the same beam as the UL grant. Alternatively, the UE 804 may receive, from the base station 802, information indicating the SS block index and, at 810, may determine the SS block index based on the received information.

**[0051]** At 812, the UE 804 scrambles information based on the determined SS block index. The UE 804 may scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the SS block index. The information before scrambling may be encoded and/or unencoded, and may be scrambled based on the generated scrambling sequence. The information includes at least one of data, control information, or a CRC associated with control information. In one configuration, the data is for a PUSCH, and the control information is for a PUCCH.

**[0052]** At 814, the UE 804 transmits the scrambled information to a base station 802. When the scrambled information includes a CRC scrambled by the SS block index, the UE 804 may transmit UL control information along with the SS-

block-index scrambled CRC. The base station 802 receives, from the UE, the information scrambled based on the SS block index.

**[0053]** At 816, the base station 802 descrambles the scrambled information based on the SS block index. When the scrambled information includes a CRC scrambled based on an SS block index, the base station 802 may descramble the CRC based on the SS block index, and decode the received UL control information based on the descrambled CRC (e.g., decode the received UL control information, generate a CRC based on the decoded UL control information, and compare the generated CRC to the descrambled CRC to determine whether the UL control information was decoded/descrambled successfully).

**[0054]** FIG. 9 illustrates flowcharts 900, 950 of methods of wireless communication of a base station. With respect to the flow chart 900, at 904, a base station determines an SS block index associated with an SS block for transmission. As discussed *supra* in relation to FIG. 4, the SS block may include at least one of a PSS, an SSS, or a PBCH. If the base station transmits  $n$  SS blocks within an SS burst, and transmits  $K$  SS bursts within an SS burst set, the base station may determine the SS block index based on which SS block is being sent within a particular SS burst of an SS burst set. As such, the SS block index may be a function of  $n$  and  $K$ , as discussed *supra* in relation to FIGs. 6A, 6B. In one example, the SS block index  $I_{SSB}$  may be one parameter within 0, 1, ...,  $n^*K-1$  for indicating the SS block within an SS burst within an SS burst set. In another example, the SS block index  $I_{SSB}$  may be two parameters, with a first parameter between 0, 1, ...,  $n-1$  (e.g.,  $s_2s_1s_0c_0$ , if 4 bits with  $n=16$ ) for indicating a particular SS block within an SS burst, and a second parameter between 0, 1, ...,  $K-1$  (e.g.,  $b_5b_4b_3b_2b_1b_0$ , if 6 bits with  $K=64$ ) to indicate a particular SS burst within an SS burst set.

**[0055]** At 906, the base station scrambles information based on at least a portion of the determined SS block index  $I_{SSB}$ . The information may be scrambled based on a subset (a portion of) of the SS block index  $I_{SSB}$ , such as for example,  $X$  LSBs of the SS block index. The information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. The reference signal may be at least one of CSI-RS, MRS, DMRS (e.g., for a PDCCH, a PDSCH, or a PBCH), or PT-RS. In one configuration, the data is for a PDSCH, the paging information is for a PCH, the control information is for a PDCCH, and the broadcast information is for a PBCH. Using at least a portion of the determined SS block index  $I_{SSB}$ , the base station may scramble one or more of the various types of information. The base station may scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index. The information before scrambling may be encoded and/or unencoded, and may be scrambled based on the generated scrambling sequence.

**[0056]** In one configuration, the scrambled information includes the CRC scrambled based on the at least the portion of the SS block index, and the CRC is for control information sent to the UE. In such a configuration, at 902, the base station may determine an SS block to be used by a UE in association with QCL of reference signals. Further, at 906, the base station may scramble the CRC based on the at least the portion of the SS block index of the determined SS block in 902 to be used by the UE in association with QCL of reference signals.

**[0057]** At 908, the base station transmits the SS block and the scrambled information. The scrambled information may be transmitted with (concurrently in time with) or without (non-concurrently in time with) the SS block. For example, when the scrambled information is the PBCH, the scrambled PBCH is sent with the SS block. However, when the scrambled information is control information with a CRC, the scrambled control information with a CRC may not be transmitted concurrently in time with the SS block.

**[0058]** With respect to the flow chart 950, at 952, a base station may send, to a UE, an uplink grant for information, where the uplink grant is associated with an SS block index. Alternatively, the base station may send, to the UE, SS block index information indicating an SS block index for scrambling information.

**[0059]** At 954, the base station receives, from the UE, the information scrambled based on at least a portion of the SS block index. The scrambled information includes at least one of data or control information. In one configuration, the data is for a PUSCH, and the control information is for a PUCCH.

**[0060]** At 956, the base station descrambles the scrambled information based on the at least the portion of the SS block index. For example, the base station may receive information from the UE that is scrambled based on the at least the portion of the SS block index, and may descramble the received information based on the at least the portion of the SS block index. For another example, the base station may receive the information with a CRC scrambled based on the at least the portion of the SS block index. The base station may descramble the CRC received from the UE, decode the information, generate a CRC based on the decoded information, and compare the generated CRC to the descrambled CRC to determine whether the information received from the UE was decoded/descrambled successfully.

**[0061]** FIG. 10 illustrates flowcharts 1000, 1050 of methods of wireless communication of a UE. With respect to the flow chart 1000, at 1002, a UE receives an SS block and information scrambled based on at least a portion of an SS block index associated with the SS block. As discussed in relation to FIG. 4, the SS block may include at least one of a PSS, an SSS, or a PBCH. The information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. In one configuration, the reference

signal is at least one of CSI-RS, MRS, DMRS (e.g., for a PDCCH, a PDSCH, or a PBCH), or PT-RS. In one configuration, the data is for a PDSCH, the paging information is for a PCH, the control information is for a PDCCH, and the broadcast information is for a PBCH.

**[0062]** At 1004, the UE descrambles the scrambled information based on the at least the portion of the SS block index. In one configuration, the scrambled information includes a CRC scrambled based on the at least the portion of the SS block index. In such a configuration, at 1004, the UE descrambles the CRC based on the at least the portion of the SS block index, and decodes received control information based on the descrambled CRC.

**[0063]** In the configuration in which the UE descrambles the CRC based on the at least the portion of the SS block index and decodes received control information based on the descrambled CRC, at 1006, the UE may determine a QCL parameter based on the at least the portion of the SS block index used to descramble the CRC.

**[0064]** With respect to the flow chart 1050, at 1052, a UE may receive an uplink grant from a base station. Alternatively, the UE may receive, from the base station, information indicating an SS block index.

**[0065]** At 1054, the UE determines the SS block index associated with an SS block for reception. The UE may determine the SS block index based on the uplink grant, or otherwise, based on the information indicating an SS block index.

**[0066]** At 1056, the UE scrambles information based on at least a portion of the determined SS block index. The information includes at least one of data or control information. In one configuration, the data is for a PUSCH, and the control information is for a PUCCH. The UE may scramble the information itself and/or may scramble a CRC associated with the information. The UE may scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index. The information before scrambling may be at least one of encoded or unencoded, and may be scrambled based on the generated scrambling sequence.

**[0067]** At 1058, the UE transmits the scrambled information to a base station. The scrambled information may include the information scrambled based on the at least the portion of the SS block index, or may include both the information and a CRC (generated based on the information) that is scrambled based on the at least the portion of the SS block index.

**[0068]** FIG. 11 is a conceptual data flow diagram 1100 illustrating the data flow between different means/components in an exemplary apparatus 1102. The apparatus may be a base station, such as the base station 180, 402, 704, 802. The apparatus includes an SS block index determination component 1108 that may be configured to determine an SS block index associated with an SS block for transmission. The SS block index determination component 1108 may provide the determined SS block index to an SS block index scrambler/descrambler component 1106. The SS block index scrambler/descrambler component 1106 may be configured to scramble information based on at least a portion of the determined SS block index. The information may include at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. After scrambling the information, the SS block index scrambler/descrambler component 1106 may provide the scrambled information to a transmission component 1110. The transmission component 1110 may be configured to transmit the SS block and the scrambled information to a UE 1150.

**[0069]** As discussed *supra*, the reference signal may be at least one of CSI-RS, MRS, DMRS (e.g., for a PDCCH, a PDSCH, or a PBCH), or PT-RS. Further, the data may be for a PDSCH, the paging information may be for a PCH, the control information may be for a PDCCH, and the broadcast information may be for a PBCH. The SS block may include at least one of a PSS, an SSS, or a PBCH.

**[0070]** The scrambled information may include the CRC scrambled based on the at least the portion of the SS block index, where the CRC is for control information sent to the UE 1150. In such a configuration, the SS Block index determination component 1108 may be configured to determine an SS block to be used by the UE 1150 in association with QCL of reference signals, and the CRC may be scrambled based on the at least the portion of the SS block index of the determined SS block to be used by the UE 1150 in association with QCL of reference signals.

**[0071]** The SS block index scrambler/descrambler component 1106 may be configured to scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index. The information before scrambling may be at least one of encoded or unencoded, and may be scrambled based on the generated scrambling sequence.

**[0072]** The apparatus 1102 may further include a reception component 1104 that is configured to receive, from the UE 1150, information scrambled based on at least a portion of an SS block index, where the scrambled information includes at least one of data or control information. The reception component 1104 may provide the received scrambled information to the SS block index scrambler/descrambler component 1106. The reception component 1104 may also provide SS block index information associated with the received scrambled information to the SS block index determination component 1108 so that the SS block index determination component 1108 may determine an SS block index associated with the received scrambled information. In such case, the SS block index determination component 1108 may provide the SS block index associated with the received scrambled information to the SS block index scrambler/descrambler component 1106. The SS block index scrambler/descrambler component 1106 may be configured to de-scramble the scrambled information based on the at least the portion of the SS block index.

[0073] As discussed *supra*, the data may be for a PUSCH, and the control information may be for a PUCCH. The transmission component 1110 may be configured to send, to the UE 1150, an uplink grant for the information, wherein the uplink grant is associated with the SS block index. Alternatively, the SS block index determination component 1108 may provide an SS block index to the transmission component 1110, and the transmission component 1110 may send, to the UE 1150, SS block index information indicating the SS block index for scrambling the information. The SS block index scrambler/descrambler component 1106 may be configured to scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on at least a portion of the SS block index, where the information before scrambling is at least one of encoded or unencoded, and the information is scrambled based on the generated scrambling sequence.

[0074] The apparatus may include additional components that perform each of the blocks of the algorithm in the aforementioned flowcharts 900, 950 of FIG. 9. As such, each block in the aforementioned flowcharts 900, 950 of FIG. 9 may be performed by a component and the apparatus may include one or more of those components. The components may be one or more hardware components specifically configured to carry out the stated processes/algorithm, implemented by a processor configured to perform the stated processes/algorithm, stored within a computer-readable medium for implementation by a processor, or some combination thereof.

[0075] FIG. 12 is a diagram 1200 illustrating an example of a hardware implementation for an apparatus 1102' employing a processing system 1214. The processing system 1214 may be implemented with a bus architecture, represented generally by the bus 1224. The bus 1224 may include any number of interconnecting buses and bridges depending on the specific application of the processing system 1214 and the overall design constraints. The bus 1224 links together various circuits including one or more processors and/or hardware components, represented by the processor 1204, the components 1104, 1106, 1108, 1110 and the computer-readable medium / memory 1206. The bus 1224 may also link various other circuits such as timing sources, peripherals, voltage regulators, and power management circuits, which are well known in the art, and therefore, will not be described any further.

[0076] The processing system 1214 may be coupled to a transceiver 1210. The transceiver 1210 is coupled to one or more antennas 1220. The transceiver 1210 provides a means for communicating with various other apparatus over a transmission medium. The transceiver 1210 receives a signal from the one or more antennas 1220, extracts information from the received signal, and provides the extracted information to the processing system 1214, specifically the reception component 1104. In addition, the transceiver 1210 receives information from the processing system 1214, specifically the transmission component 1110, and based on the received information, generates a signal to be applied to the one or more antennas 1220. The processing system 1214 includes a processor 1204 coupled to a computer-readable medium / memory 1206. The processor 1204 is responsible for general processing, including the execution of software stored on the computer-readable medium / memory 1206. The software, when executed by the processor 1204, causes the processing system 1214 to perform the various functions described *supra* for any particular apparatus. The computer-readable medium / memory 1206 may also be used for storing data that is manipulated by the processor 1204 when executing software. The processing system 1214 further includes at least one of the components 1104, 1106, 1108, 1110. The components may be software components running in the processor 1204, resident/stored in the computer readable medium / memory 1206, one or more hardware components coupled to the processor 1204, or some combination thereof. The processing system 1214 may be a component of the base station 310 and may include the memory 376 and/or at least one of the TX processor 316, the RX processor 370, and the controller/processor 375.

[0077] In one configuration, the apparatus 1102/1102' for wireless communication is a base station and may include means for determining an SS block index associated with an SS block for transmission. The addition, the apparatus may include means for scrambling information based on at least a portion of the determined SS block index, where the information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. Further, the apparatus may include means for transmitting the SS block and the scrambled information. In one configuration, the scrambled information includes the CRC scrambled based on the at least the portion of the SS block index, and the CRC is for control information sent to the UE. In such a configuration, the apparatus may further include means for determining an SS block to be used by a UE in association with QCL of reference signals. The CRC may be scrambled based on the at least the portion of the SS block index of the determined SS block to be used by the UE in association with QCL of reference signals. In one configuration, the means for scrambling the information may be configured to generate a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index, where the information before scrambling is at least one of encoded or unencoded, and the information is scrambled based on the generated scrambling sequence.

[0078] In one configuration, the apparatus 1102/1102' for wireless communication is a base station and may include means for receiving, from a UE, information scrambled based on at least a portion of an SS block index, where the scrambled information includes at least one of data or control information. The apparatus may further include means for descrambling the scrambled information based on the at least the portion of the SS block index. In one configuration, the apparatus further includes means for sending, to the UE, an uplink grant for the information, where the uplink grant

is associated with the SS block index. In one configuration, the apparatus may further include means for sending, to the UE, SS block index information indicating the SS block index for scrambling the information.

**[0079]** The aforementioned means may be one or more of the aforementioned components of the apparatus 1102 and/or the processing system 1214 of the apparatus 1102' configured to perform the functions recited by the aforementioned means. As described *supra*, the processing system 1214 may include the TX Processor 316, the RX Processor 370, and the controller/processor 375. As such, in one configuration, the aforementioned means may be the TX Processor 316, the RX Processor 370, and the controller/processor 375 configured to perform the functions recited by the aforementioned means.

**[0080]** FIG. 13 is a conceptual data flow diagram 1300 illustrating the data flow between different means/components in an exemplary apparatus 1302. The apparatus may be a UE, such as the UE 104, 404, 702, 804. The apparatus includes a reception component 1304 configured to receive an SS block and information scrambled based on at least a portion of an SS block index associated with the SS block. The SS block may include at least one of a PSS, an SSS, or a PBCH. The information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. The reception component 1304 may provide the scrambled information to an SS block index scrambler/descrambler component 1306. The SS block index scrambler/descrambler component 1306 may be configured to descramble the scrambled information based on the at least the portion of the SS block index.

**[0081]** The reference signal may be at least one of CSI-RS, MRS, DMRS (e.g., for a PDCCH, a PDSCH, or a PBCH), or PT-RS. The data may be for a PDSCH, the paging information may be for a PCH, the control information may be for a PDCCH, and the broadcast information may be for a PBCH.

**[0082]** The scrambled information may include the CRC scrambled based on the at least the portion of the SS block index. In such a configuration, the SS block index scrambler/descrambler component 1306 may be configured to descramble the scrambled information based on the at least the portion of the SS block index by descrambling the CRC based on the at least the portion of the SS block index, and decoding received control information based on the descrambled CRC. The apparatus may further include a QCL component 1312 that is configured to determine a QCL parameter based on the at least the portion of the SS block index used to descramble the CRC.

**[0083]** The apparatus may further include an SS block index determination component 1308 that is configured to determine an SS block index associated with an SS block for reception. The SS block index determination component 1308 may receive SS block index information from the reception component 1304 in order to determine the SS block index. The SS block index determination component 1308 may provide the determined SS block index to an SS block index scrambler/descrambler component 1306. The SS block index scrambler/descrambler component 1306 may be configured to scramble information based on the at least the portion of the determined SS block index. The information may include at least one of data, control information, or a CRC associated with control information. The SS block index scrambler/descrambler component 1306 may provide the scrambled information to a transmission component 1310.

**[0084]** The transmission component 1310 may be configured to transmit the scrambled information to a base station 1350. The data may be for a PUSCH, and the control information may be for a PUCCH. The reception component 1304 may be configured to receive an uplink grant from the base station 1350. The SS block index determination component 1308 may determine the SS block index based on the uplink grant. Alternatively, the reception component 1304 may be configured to receive, from the base station 1350, SS block index information indicating the SS block index.

**[0085]** The reception component 1304 may provide the SS block index information to the SS block index determination component 1308 so that the SS block index determination component 1308 may determine the SS block index. The SS block index scrambler/descrambler component 1306 may be configured to scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index. The information before scrambling may be at least one of encoded or unencoded, and the information may be scrambled based on the generated scrambling sequence.

**[0086]** The apparatus may include additional components that perform each of the blocks of the algorithm in the aforementioned flowcharts of FIG. 10. As such, each block in the aforementioned flowcharts of FIG. 10 may be performed by a component and the apparatus may include one or more of those components. The components may be one or more hardware components specifically configured to carry out the stated processes/algorithm, implemented by a processor configured to perform the stated processes/algorithm, stored within a computer-readable medium for implementation by a processor, or some combination thereof.

**[0086]** FIG. 14 is a diagram 1400 illustrating an example of a hardware implementation for an apparatus 1302' employing a processing system 1414. The processing system 1414 may be implemented with a bus architecture, represented generally by the bus 1424. The bus 1424 may include any number of interconnecting buses and bridges depending on the specific application of the processing system 1414 and the overall design constraints. The bus 1424 links together various circuits including one or more processors and/or hardware components, represented by the processor 1404, the components 1304, 1306, 1308, 1310, 1312 and the computer-readable medium / memory 1406. The bus 1424 may also link various other circuits such as timing sources, peripherals, voltage regulators, and power management circuits,

which are well known in the art, and therefore, will not be described any further.

**[0087]** The processing system 1414 may be coupled to a transceiver 1410. The transceiver 1410 is coupled to one or more antennas 1420. The transceiver 1410 provides a means for communicating with various other apparatus over a transmission medium. The transceiver 1410 receives a signal from the one or more antennas 1420, extracts information from the received signal, and provides the extracted information to the processing system 1414, specifically the reception component 1304. In addition, the transceiver 1410 receives information from the processing system 1414, specifically the transmission component 1310, and based on the received information, generates a signal to be applied to the one or more antennas 1420. The processing system 1414 includes a processor 1404 coupled to a computer-readable medium / memory 1406. The processor 1404 is responsible for general processing, including the execution of software stored on the computer-readable medium / memory 1406. The software, when executed by the processor 1404, causes the processing system 1414 to perform the various functions described *supra* for any particular apparatus. The computer-readable medium / memory 1406 may also be used for storing data that is manipulated by the processor 1404 when executing software. The processing system 1414 further includes at least one of the components 1304, 1306, 1308, 1310, 1312. The components may be software components running in the processor 1404, resident/stored in the computer readable medium / memory 1406, one or more hardware components coupled to the processor 1404, or some combination thereof. The processing system 1414 may be a component of the UE 350 and may include the memory 360 and/or at least one of the TX processor 368, the RX processor 356, and the controller/processor 359.

**[0088]** In one configuration, the apparatus 1302/1302' for wireless communication is a UE and may include means for receiving an SS block and information scrambled based on at least a portion of an SS block index associated with the SS block. The information includes at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. The apparatus may further include means for descrambling the scrambled information based on the at least the portion of the SS block index. In one configuration, the scrambled information includes the CRC scrambled based on the at least the portion of the SS block index, and the means for descrambling the scrambled information based on the at least the portion of the SS block index is configured to descramble the CRC based on the at least the portion of the SS block index, and to decode received control information based on the descrambled CRC. In such a configuration, the apparatus may further include means for determining a QCL parameter based on the at least the portion of the SS block index used to descramble the CRC.

**[0089]** In one configuration, the apparatus 1302/1302' for wireless communication is a UE and may include means for determining an SS block index associated with an SS block for reception. The apparatus may further include means for scrambling information based on at least a portion of the determined SS block index. The information may include at least one of data, control information, or a CRC associated with control information. The apparatus may further include means for transmitting the scrambled information to a base station.

**[0090]** In one configuration, the apparatus may further include means for receiving an uplink grant from the base station, where the SS block index is determined based on the uplink grant. In one configuration, the apparatus may further include means for receiving, from the base station, information indicating the SS block index. In one configuration, the means for scrambling the information may be configured to generate a scrambling sequence based on a sequence initialization that is based at least in part on at least a portion of the SS block index, where the information before scrambling is at least one of encoded or unencoded, and the information is scrambled based on the generated scrambling sequence.

**[0091]** The aforementioned means may be one or more of the aforementioned components of the apparatus 1302 and/or the processing system 1414 of the apparatus 1302' configured to perform the functions recited by the aforementioned means. As described *supra*, the processing system 1414 may include the TX Processor 368, the RX Processor 356, and the controller/processor 359. As such, in one configuration, the aforementioned means may be the TX Processor 368, the RX Processor 356, and the controller/processor 359 configured to perform the functions recited by the aforementioned means.

**[0092]** As discussed *supra*, a base station / UE may scramble/descramble information based on at least a portion (subset) of an SS block index, where the SS block index indexes a particular SS block within an SS burst within an SS burst set. The SS block index may be one or multiple values to indicate a particular SS block index within an SS burst within an SS burst set. The information may be scrambled before being transmitted or may be descrambled after being received. For a base station transmitting information, the information may be at least one of a reference signal, data, paging information, control information, broadcast information, or a CRC associated with control information. For a UE transmitting information, the information may be at least one of data, control information, or a CRC associated with control information.

**[0093]** It is understood that the specific order or hierarchy of blocks in the processes / flowcharts disclosed is an illustration of exemplary approaches. Based upon design preferences, it is understood that the specific order or hierarchy of blocks in the processes / flowcharts may be rearranged. Further, some blocks may be combined or omitted. The accompanying method claims present elements of the various blocks in a sample order, and are not meant to be limited to the specific order or hierarchy presented.

[0094] The previous description is provided to enable any person skilled in the art to practice the various aspects described herein. Various modifications to these aspects will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other aspects. Thus, the claims are not intended to be limited to the aspects shown herein, but is to be accorded the full scope consistent with the language claims, wherein reference to an element in the singular is not intended to mean "one and only one" unless specifically so stated, but rather "one or more." The word "exemplary" is used herein to mean "serving as an example, instance, or illustration." Any aspect described herein as "exemplary" is not necessarily to be construed as preferred or advantageous over other aspects. Unless specifically stated otherwise, the term "some" refers to one or more. Combinations such as "at least one of A, B, or C," "one or more of A, B, or C," "at least one of A, B, and C," "one or more of A, B, and C," and "A, B, C, or any combination thereof" include any combination of A, B, and/or C, and may include multiples of A, multiples of B, or multiples of C. Specifically, combinations such as "at least one of A, B, or C," "one or more of A, B, or C," "at least one of A, B, and C," "one or more of A, B, and C," and "A, B, C, or any combination thereof" may be A only, B only, C only, A and B, A and C, B and C, or A and B and C, where any such combinations may contain one or more member or members of A, B, or C. The words "module," "mechanism," "element," "device," and the like may not be a substitute for the word "means." As such, no claim element is to be construed as a means plus function unless the element is expressly recited using the phrase "means for."

## Claims

20 1. A method (950) of wireless communication at a base station, the method comprising:

receiving (954), from a user equipment, UE, information scrambled based on at least a portion of a synchronization signal, SS block index, the scrambled information including at least one of data associated with a physical uplink shared channel, PUSCH, or control information associated with a physical uplink control channel, PUCCH; and  
descrambling (956) the scrambled information based on the at least the portion of the SS block index.

30 2. The method of claim 1, wherein the portion of the SS block index comprises a number of least significant bits of the SS block index.

35 3. The method of claim 1, further comprising sending, to the UE, an uplink grant for the information, the uplink grant being associated with the SS block index, wherein the SS block index indexes a particular SS block within an SS burst within an SS burst set.

40 4. The method of claim 1, further comprising sending, to the UE, SS block index information indicating the at least the portion of the SS block index for scrambling the information.

45 5. A method (1050) of wireless communication at a user equipment, UE, the method comprising:

determining (1054) a synchronization signal, SS, block index associated with an SS block; scrambling (1056) information based on at least a portion of the determined SS block index, the information including at least one of data associated with a physical uplink shared channel, PUSCH, control information associated with a physical uplink control channel ,PUCCH, or a cyclic redundancy check, CRC, associated with control information; and  
transmitting (1058) the scrambled information to a base station.

50 6. The method of claim 5, further comprising receiving an uplink grant from the base station, wherein the SS block index is determined based on the uplink grant, wherein the SS block index indexes a particular SS block within an SS burst within an SS burst set.

7. The method of claim 5, further comprising receiving, from the base station, information indicating the SS block index.

55 8. The method of claim 5, wherein the scrambling the information comprises generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index, wherein the information before scrambling is at least one of encoded or unencoded, and is scrambled based on the generated scrambling sequence.

9. An apparatus (1214) for wireless communication, the apparatus being a base station, comprising:

5            a memory (1206); and  
   at least one processor (1204) coupled to the memory (1206) and configured to: receive, from a user equipment, UE, information scrambled based on at least a portion of a synchronization signal, SS, block index, the scrambled information including at least one of data associated with a physical uplink shared channel, PUSCH, or control information associated with a physical uplink control channel, PUCCH; and  
   descramble the scrambled information based on the at least the portion of the SS block index.

10          10. The apparatus of claim 9, wherein the at least one processor is further configured to send, to the UE, an uplink grant for the information, the uplink grant being associated with the SS block index, wherein the SS block index indexes a particular SS block within an SS burst within an SS burst set.

15          11. The apparatus of claim 9, wherein the at least one processor is further configured to send, to the UE, SS block index information indicating the at least the portion of the SS block index for scrambling the information.

12. An apparatus (1414) for wireless communication, the apparatus being a user equipment, UE, comprising:

20            a memory (1406); and  
   at least one processor (1404) coupled to the memory (1406) and configured to:  
                  determine a synchronization signal, SS, block index associated with an SS block;  
                  scramble information based on at least a portion of the determined SS block index, the information including at least one of data associated with a physical uplink shared channel, PUSCH, control information associated with a physical uplink control channel, PUCCH, or a cyclic redundancy check, CRC, associated with control information; and  
                  transmit the scrambled information to a base station.

25          13. The apparatus of claim 12, wherein the at least one processor is further configured to receive, from the base station, information indicating the SS block index.

30          14. The apparatus of claim 12, wherein the at least one processor is configured to scramble the information by generating a scrambling sequence based on a sequence initialization that is based at least in part on the at least the portion of the SS block index, wherein the information before scrambling is at least one of encoded or unencoded, and is scrambled based on the generated scrambling sequence.

### Patentansprüche

40          1. Ein Verfahren (950) für eine drahtlose Kommunikation an einer Basisstation, wobei das Verfahren aufweist:  
                  Empfangen (954), von einem Benutzergerät (User Equipment bzw. UE), von Informationen, die basierend auf wenigstens einem Teil eines Synchronisationssignal (SS)-Blockindex verwürfelt sind, wobei die verwürfelten Informationen Daten, die mit einem gemeinsamen physikalischen Aufwärtsstreckekanal (PUSCH) assoziiert sind, und/oder Steuerinformationen, die mit einem physikalischen Aufwärtsstreckesteuerkanal (PUCCH) assoziiert sind, enthalten, und  
                  Entwürfeln (956) der verwürfelten Informationen basierend auf dem wenigstens einen Teil des SS-Blockindex.

45          2. Verfahren nach Anspruch 1, wobei der Teil des SS-Blockindex eine Anzahl von niedrigstwertigen Bits des SS-Blockindex aufweist.

50          3. Verfahren nach Anspruch 1, das weiterhin das Senden, an das UE, einer Aufwärtsstreckegewährung für die Informationen aufweist, wobei die Aufwärtsstreckegewährung mit dem SS-Blockindex assoziiert ist, wobei der SS-Blockindex einen bestimmten SS-Block in einem SS-Burst in einem SS-Burstsatz indiziert.

55          4. Verfahren nach Anspruch 1, das weiterhin das Senden, an das UE, von SS-Blockindexinformationen, die den wenigstens einen Teil des SS-Blockindex für das Verwürfeln der Informationen angeben, aufweist.

5. Ein Verfahren (1050) für eine drahtlose Kommunikation an einem Benutzergerät (User Equipment bzw. UE), wobei das Verfahren aufweist:
  - Bestimmen (1054) eines Synchronisationssignal (SS)-Blockindex, der mit einem SS-Block assoziiert ist,
  - Verwürfeln (1056) von Informationen basierend auf wenigstens einem Teil des bestimmten SS-Blockindex, wobei die Informationen Daten, die mit einem gemeinsamen physikalischen Aufwärtsstreckekanal (PUSCH) assoziiert sind, Steuerinformationen, die mit einem physikalischen Aufwärtsstreckesteuerkanal (PUCCH) assoziiert sind, und/oder eine zyklische Redundanzprüfung (Cyclic Redundancy Check bzw. CRC), die mit Steuerinformationen assoziiert ist, enthalten, und
  - Senden (1058) der verwürfelten Informationen an eine Basisstation.
6. Verfahren nach Anspruch 5, das weiterhin das Empfangen einer Aufwärtsstreckegewährung von der Basisstation aufweist, wobei der SS-Blockindex basierend auf der Aufwärtsstreckegewährung bestimmt wird, wobei der SS-Blockindex einen bestimmten SS-Block in einem SS-Burst in einem SS-Burstsatz indiziert.
7. Verfahren nach Anspruch 5, das weiterhin das Empfangen, von der Basisstation, von Informationen, die den SS-Blockindex angeben, aufweist.
8. Verfahren nach Anspruch 5, wobei das Verwürfeln der Informationen das Generieren einer Verwürfungssequenz basierend auf einer Sequenzinitialisierung, die wenigstens teilweise auf dem wenigstens einen Teil des SS-Blockindex basiert, aufweist, wobei die Informationen vor dem Verwürfeln codiert und/oder uncodiert sind und basierend auf der generierten Verwürfungssequenz verwürfelt werden.
9. Eine Vorrichtung (1214) für eine drahtlose Kommunikation, wobei die Vorrichtung eine Basisstation ist, aufweisend:
  - einen Speicher (1206), und
  - wenigstens einen Prozessor (1204), der mit dem Speicher (1206) gekoppelt ist und konfiguriert ist zum Empfangen, von einem Benutzergerät (User Equipment bzw. UE), von Informationen, die basierend auf wenigstens einem Teil eines Synchronisationssignal (SS)-Blockindex verwürfelt sind, wobei die verwürfelten Informationen Daten, die mit einem gemeinsamen physikalischen Aufwärtsstreckekanal (PUSCH) assoziiert sind, und/oder Steuerinformationen, die mit einem physikalischen Aufwärtsstreckesteuerkanal (PUCCH) assoziiert sind, enthalten, und
  - Entwürfeln der verwürfelten Informationen basierend auf dem wenigstens einen Teil des SS-Blockindex.
10. Vorrichtung nach Anspruch 9, wobei der wenigstens eine Prozessor weiterhin konfiguriert ist zum Senden, an das UE, einer Aufwärtsstreckegewährung für die Informationen, wobei die Aufwärtsstreckegewährung mit dem SS-Blockindex assoziiert ist, wobei der SS-Blockindex einen bestimmten SS-Block in einem SS-Burst in einem SS-Burstsatz indiziert.
11. Vorrichtung nach Anspruch 9, wobei der wenigstens eine Prozessor weiterhin konfiguriert ist zum Senden, an das UE, von SS-Blockindexinformationen, die den wenigstens einen Teil des SS-Blockindex für das Verwürfeln der Informationen angeben.
12. Eine Vorrichtung (1414) für eine drahtlose Kommunikation, wobei die Vorrichtung ein Benutzergerät (User Equipment bzw. UE) ist, aufweisend:
  - einen Speicher (1406), und
  - wenigstens einen Prozessor (1404), der mit dem Speicher (1406) gekoppelt ist und konfiguriert ist zum:
    - Bestimmen eines Synchronisationssignal (SS)-Blockindex, der mit einem SS-Block assoziiert ist,
    - Verwürfeln von Informationen basierend auf wenigstens einem Teil des bestimmten SS-Blockindex, wobei die Informationen Daten, die mit einem gemeinsamen physikalischen Aufwärtsstreckekanal (PUSCH) assoziiert sind, Steuerinformationen, die mit einem physikalischen Aufwärtsstreckesteuerkanal (PUCCH) assoziiert sind, und/oder eine zyklische Redundanzprüfung (Cyclic Redundancy Check bzw. CRC), die mit Steuerinformationen assoziiert ist, enthalten, und
    - Senden der verwürfelten Informationen an eine Basisstation.
13. Vorrichtung nach Anspruch 12, wobei der wenigstens eine Prozessor weiterhin konfiguriert ist zum Empfangen, von

der Basisstation, von Informationen, die den SS-Blockindex angeben.

5           **14.** Vorrichtung nach Anspruch 12, wobei der wenigstens eine Prozessor konfiguriert ist zum Verwürfeln der Informationen durch das Generieren einer Verwürfungssequenz basierend auf einer Sequenzinitialisierung, die wenigstens teilweise auf dem wenigstens einen Teil des SS-Blockindex basiert, wobei die Informationen vor dem Verwürfeln codiert und/oder uncodiert sind und basierend auf der generierten Verwürfungssequenz verwürfelt werden.

## Revendications

10           **1.** Procédé (950) de communication sans fil au niveau d'une station de base, le procédé comprenant :

15           une réception (954), à partir d'un équipement d'utilisateur, UE, d'informations brouillées sur la base d'au moins une partie d'un indice de bloc de signal de synchronisation, SS, les informations brouillées incluant au moins un élément parmi des données associées à un canal partagé de liaison montante physique, PUSCH, ou des informations de commande associées à un canal de commande de liaison montante physique, PUCCH ; et un désembrouillage (956) des informations brouillées sur la base de l'au moins une partie de l'indice de bloc de SS.

20           **2.** Procédé selon la revendication 1, dans lequel la partie de l'indice de bloc de SS comprend un certain nombre de bits les moins significatifs de l'indice de bloc de SS.

25           **3.** Procédé selon la revendication 1, comprenant en outre un envoi, vers l'UE, d'un octroi de liaison montante pour les informations, l'octroi de liaison montante étant associé à l'indice de bloc de SS, dans lequel l'indice de bloc de SS répertorie un bloc de SS particulier au sein d'une rafale de SS au sein d'un ensemble de rafales de SS.

30           **4.** Procédé selon la revendication 1, comprenant en outre un envoi, vers l'UE, d'informations d'indice de bloc de SS indiquant l'au moins une partie de l'indice de bloc de SS pour un brouillage des informations.

35           **5.** Procédé (1050) de communication sans fil au niveau d'un équipement d'utilisateur, UE, le procédé comprenant :

40           une détermination (1054) d'un indice de bloc de signal de synchronisation, SS, associé à un bloc de SS ; un brouillage (1056) d'informations sur la base d'au moins une partie de l'indice de bloc de SS déterminé, les informations incluant au moins un élément parmi des données associées à un canal partagé de liaison montante physique, PUSCH, des informations de commande associées à un canal de commande de liaison montante physique, PUCCH, ou une vérification de redondance cyclique, CRC, associée à des informations de commande ; et une transmission (1058) des informations brouillées à une station de base.

45           **6.** Procédé selon la revendication 5, comprenant en outre une réception d'un octroi de liaison montante à partir de la station de base, dans lequel l'indice de bloc de SS est déterminé sur la base de l'octroi de liaison montante, dans lequel l'indice de bloc de SS répertorie un bloc de SS particulier au sein d'une rafale de SS au sein d'un ensemble de rafales de SS.

50           **7.** Procédé selon la revendication 5, comprenant en outre une réception, à partir de la station de base, d'informations indiquant l'indice de bloc de SS.

55           **8.** Procédé selon la revendication 5, dans lequel le brouillage des informations comprend une génération d'une séquence de brouillage sur la base d'une initialisation de séquence qui est basée au moins en partie sur l'au moins une partie de l'indice de bloc de SS, dans lequel les informations avant brouillage sont au moins une parmi codées ou non codées, et sont brouillées sur la base de la séquence de brouillage générée.

60           **9.** Appareil (1214) de communication sans fil, l'appareil étant une station de base, comprenant :

65           une mémoire (1206) ; et au moins un processeur (1204) couplé à la mémoire (1206) et configuré pour : recevoir, à partir d'un équipement d'utilisateur, UE, des informations brouillées sur la base d'au moins une partie d'un indice de bloc de signal de synchronisation, SS, les informations brouillées incluant au moins un élément parmi des données associées à

un canal partagé de liaison montante physique, PUSCH, ou des informations de commande associées à un canal de commande de liaison montante physique, PUCCH ; et  
désembrouiller les informations brouillées sur la base de l'au moins une partie de l'indice de bloc de SS.

5       **10.** Appareil selon la revendication 9, dans lequel l'au moins un processeur est en outre configuré pour envoyer, vers l'UE, un octroi de liaison montante pour les informations, l'octroi de liaison montante étant associé à l'indice de bloc de SS, dans lequel l'indice de bloc de SS répertorie un bloc de SS particulier au sein d'une rafale de SS au sein d'un ensemble de rafales de SS.

10      **11.** Appareil selon la revendication 9, dans lequel l'au moins un processeur est en outre configuré pour envoyer, vers l'UE, des informations d'indice de bloc de SS indiquant l'au moins une partie de l'indice de bloc de SS pour un brouillage des informations.

15      **12.** Appareil (1414) de communication sans fil, l'appareil étant un équipement d'utilisateur, UE, comprenant :

une mémoire (1406) ; et  
au moins un processeur (1404) couplé à la mémoire (1406) et configuré pour :

20      déterminer un indice de bloc de signal de synchronisation, SS, associé à un bloc de SS ;  
brouiller des informations sur la base d'au moins une partie de l'indice de bloc de SS déterminé, les informations incluant au moins un élément parmi des données associées à un canal partagé de liaison montante physique, PUSCH, des informations de commande associées à un canal de commande de liaison montante physique, PUCCH, ou une vérification de redondance cyclique, CRC, associée à des informations de commande ; et  
25      transmettre les informations brouillées à une station de base.

**13.** Appareil selon la revendication 12, dans lequel l'au moins un processeur est en outre configuré pour recevoir, à partir de la station de base, des informations indiquant l'indice de bloc de SS.

30      **14.** Appareil selon la revendication 12, dans lequel l'au moins un processeur est configuré pour brouiller les informations en générant une séquence de brouillage sur la base d'une initialisation de séquence qui est basée au moins en partie sur l'au moins une partie de l'indice de bloc de SS, dans lequel les informations avant brouillage sont au moins une parmi codées ou non codées, et sont brouillées sur la base de la séquence de brouillage générée.

35

40

45

50

55

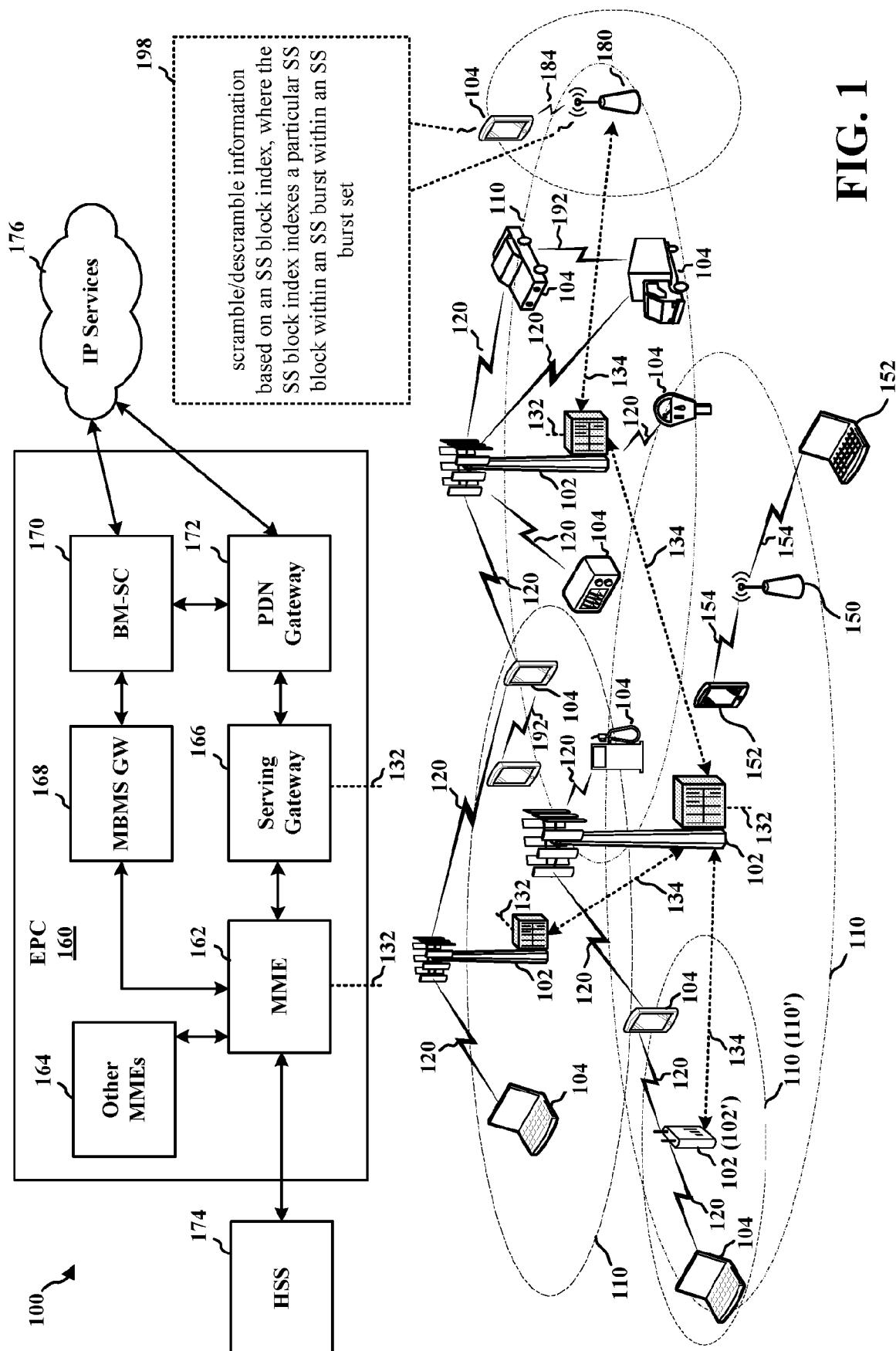


FIG. 1

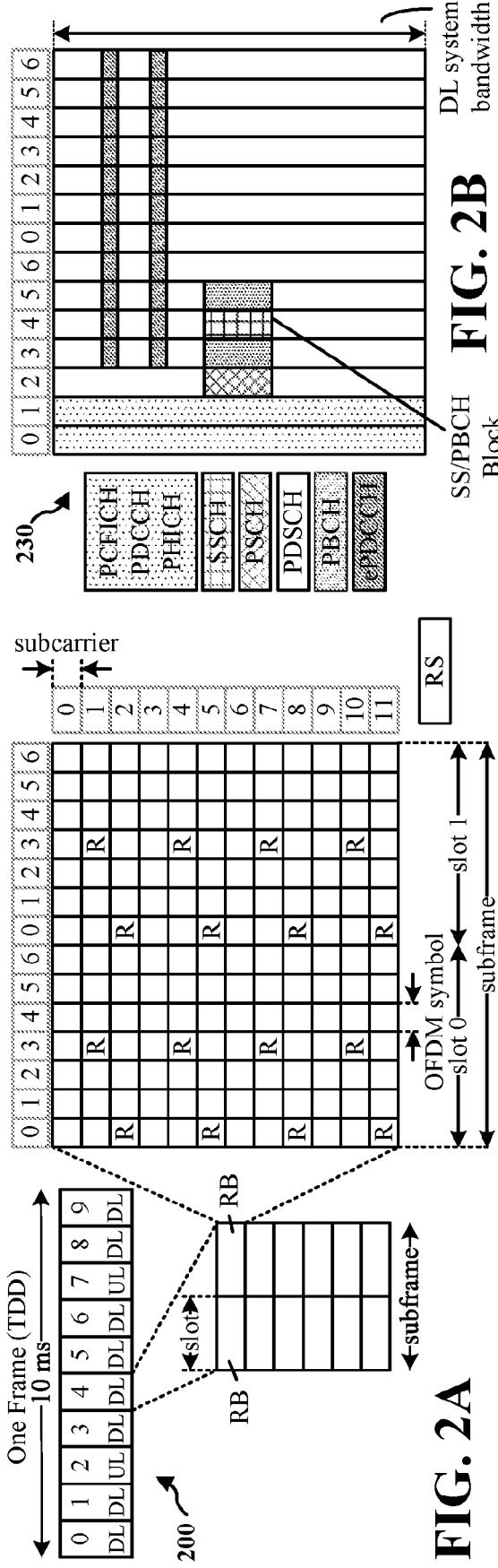
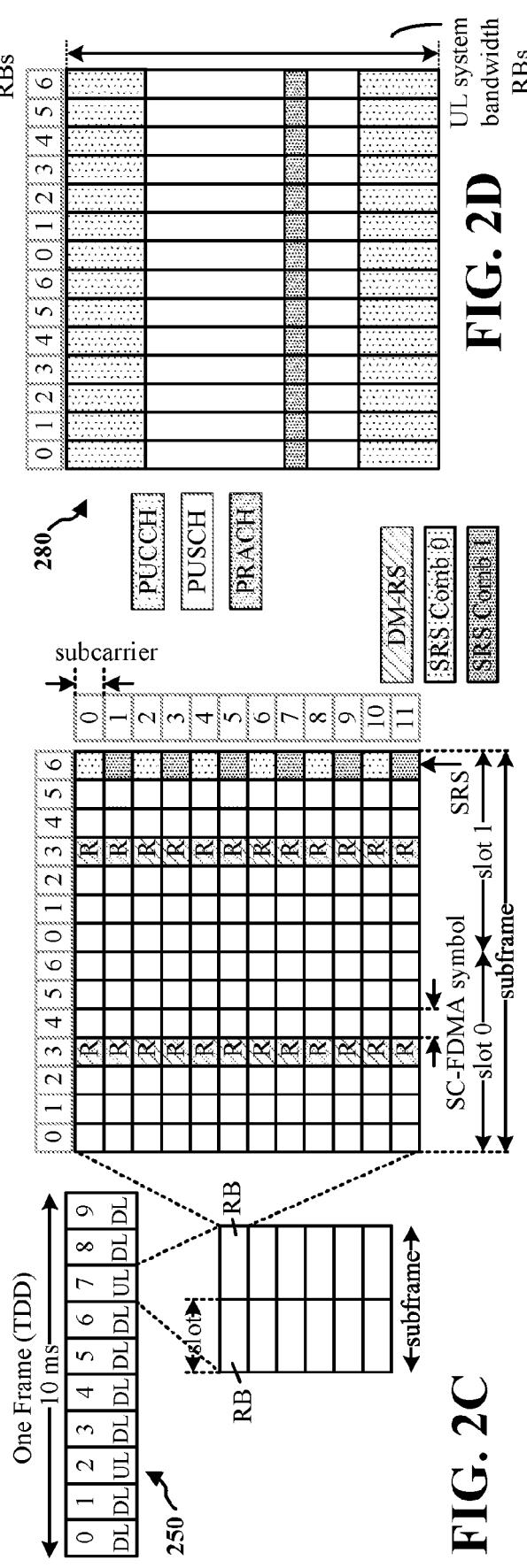


FIG. 2A



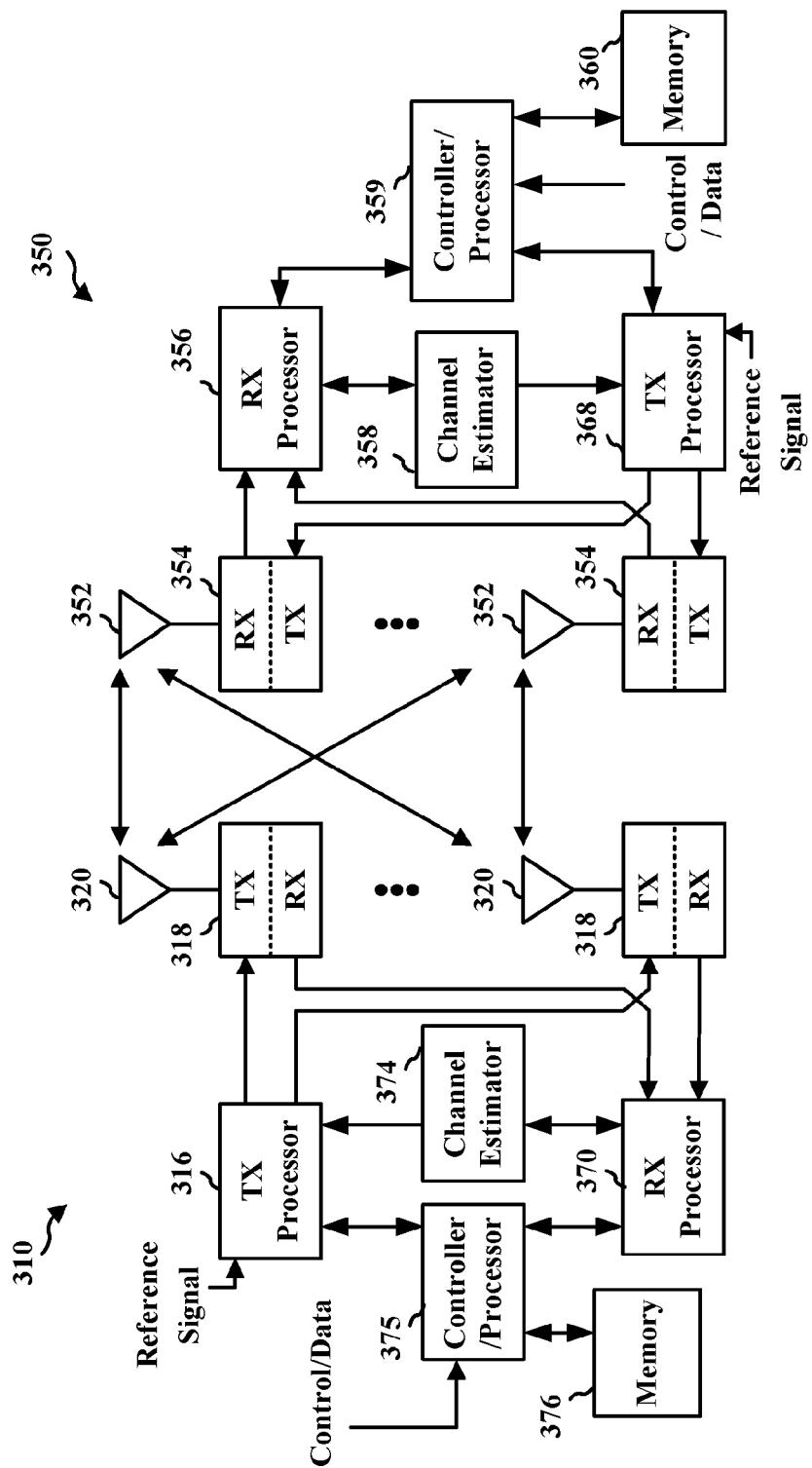


FIG. 3

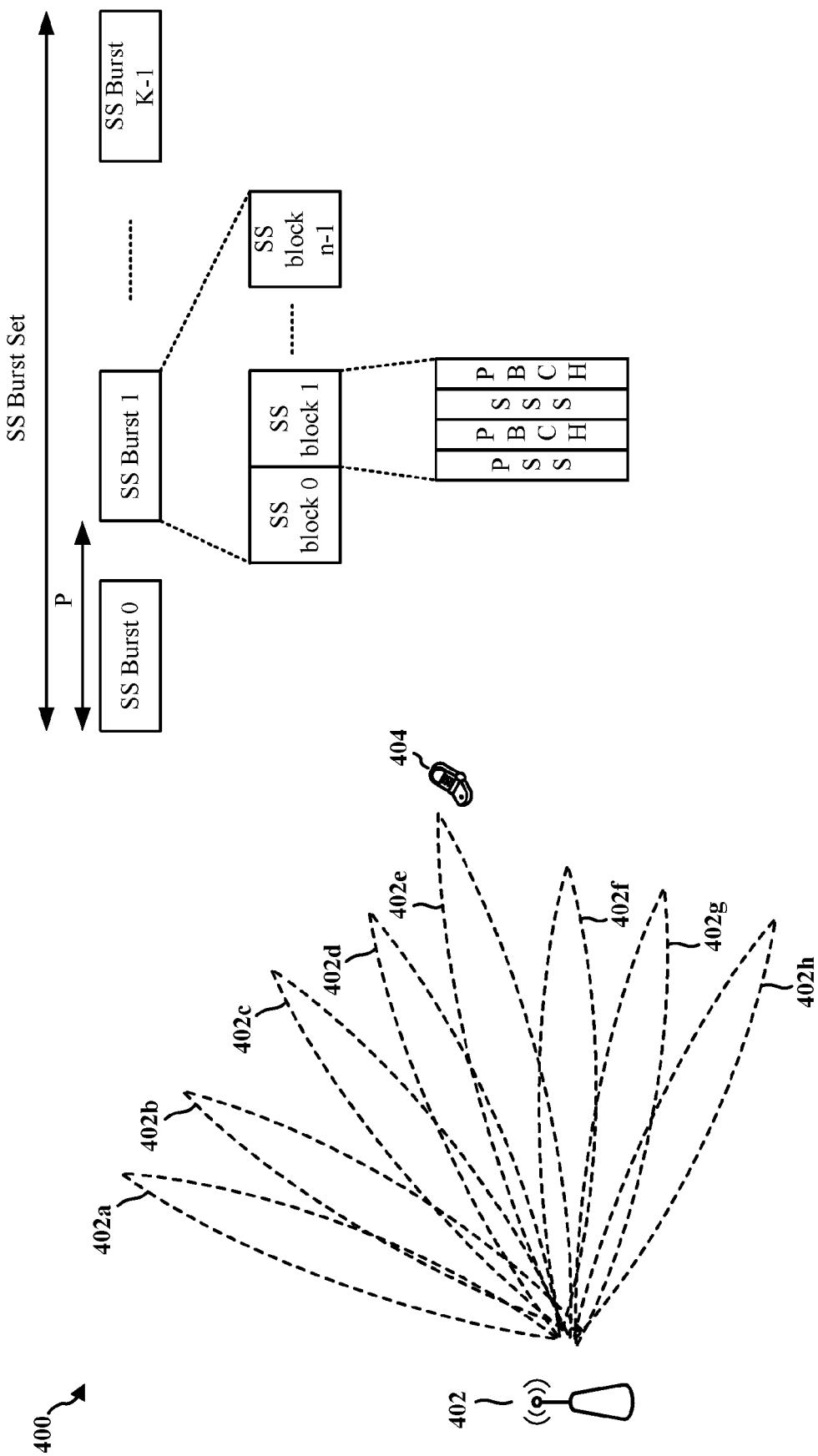


FIG. 4

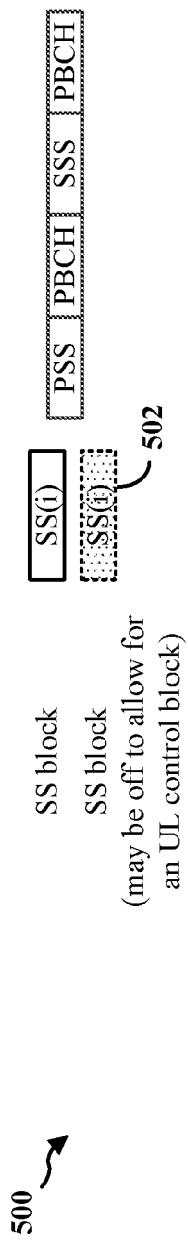


FIG. 5A

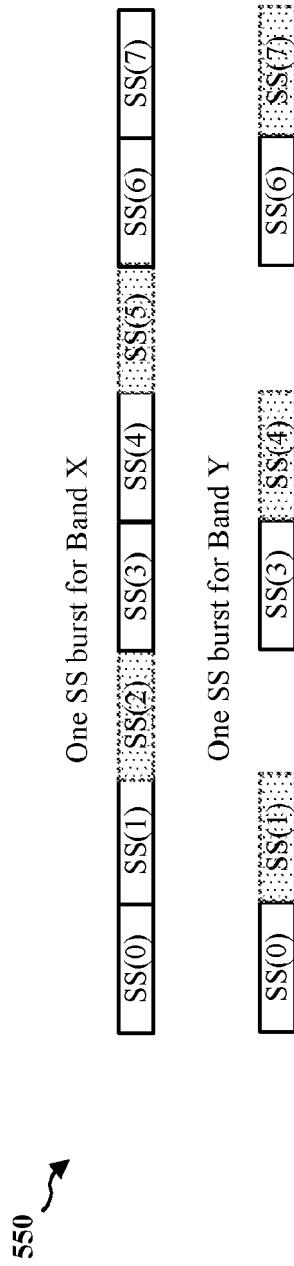


FIG. 5B

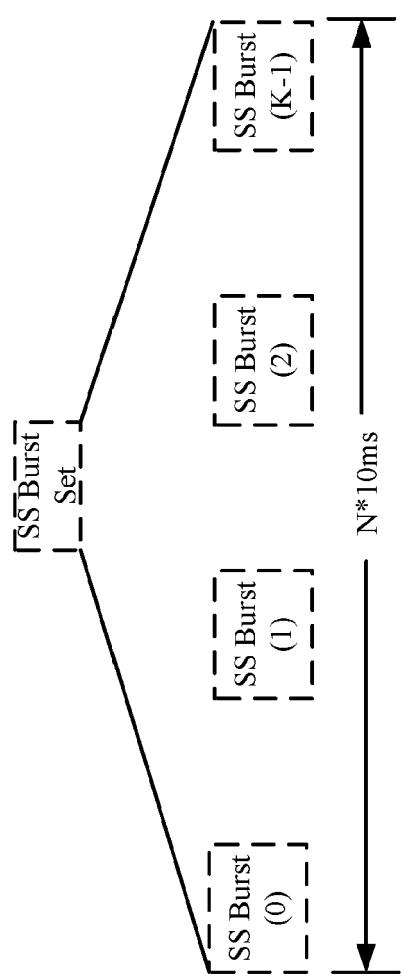


FIG. 6A

600

650

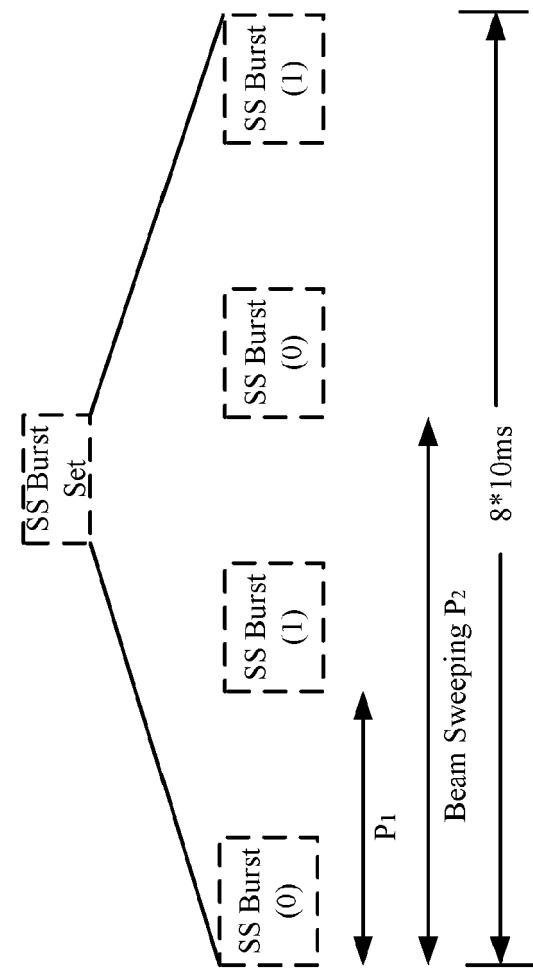


FIG. 6B

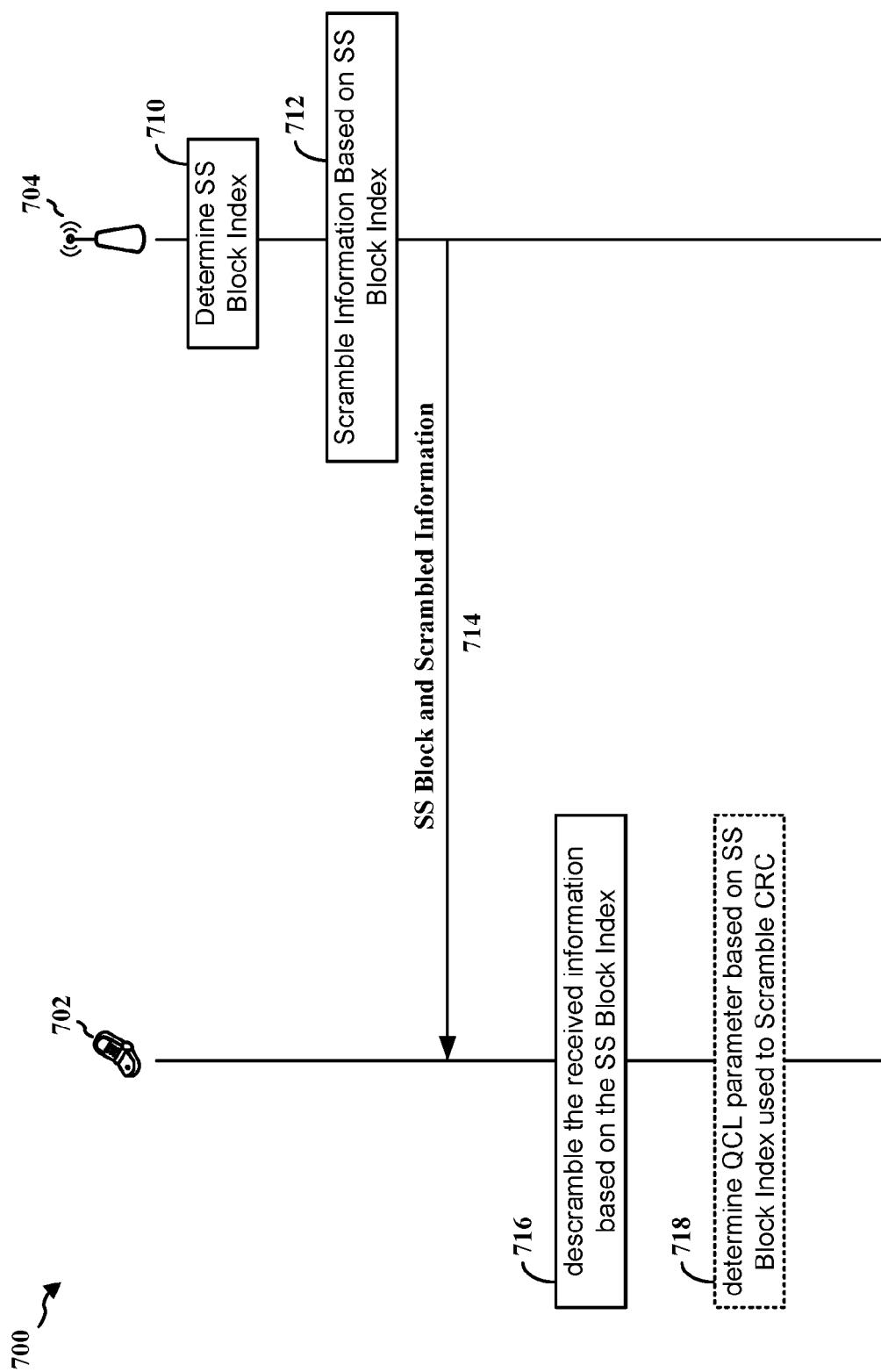


FIG. 7

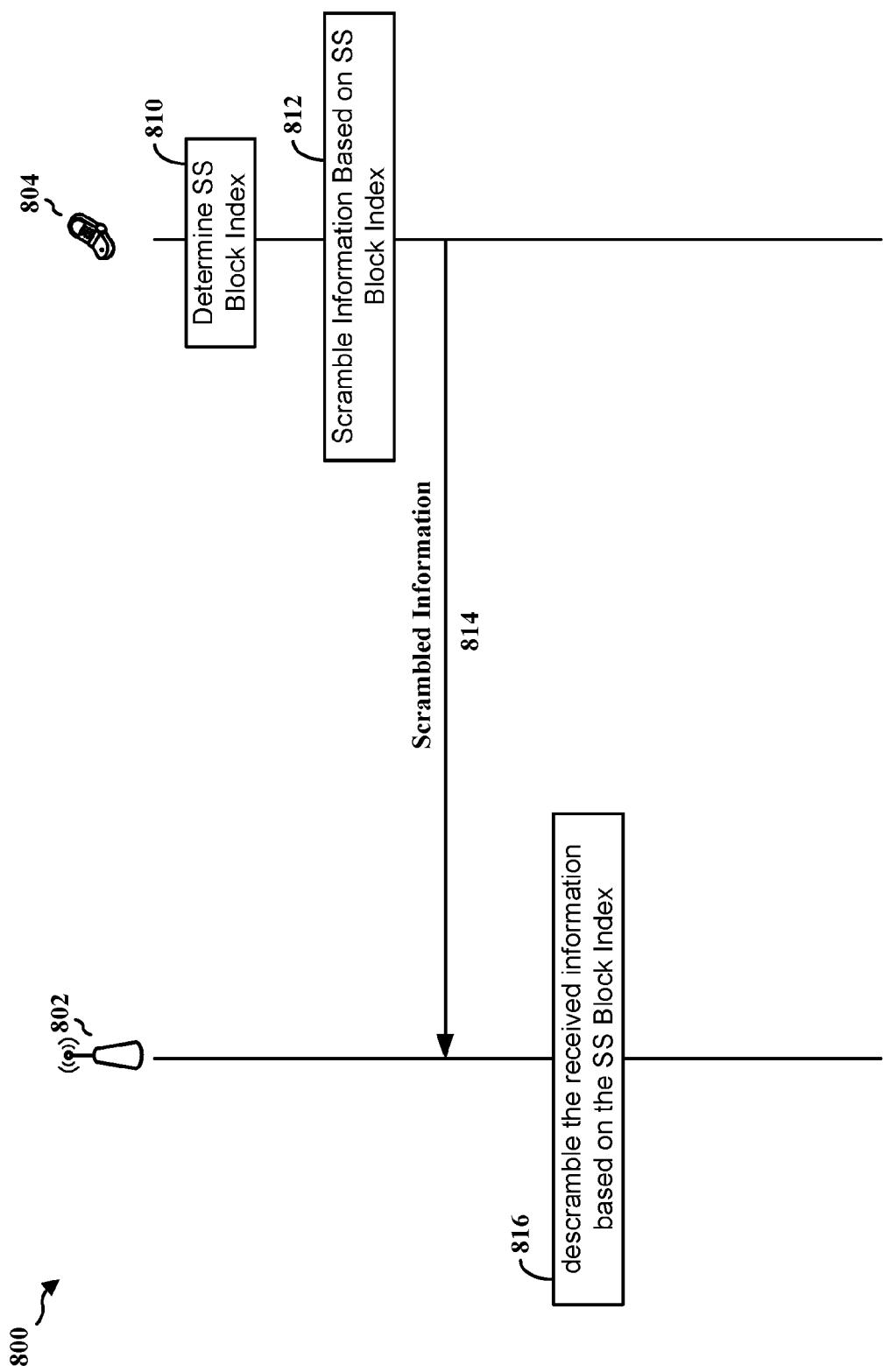


FIG. 8

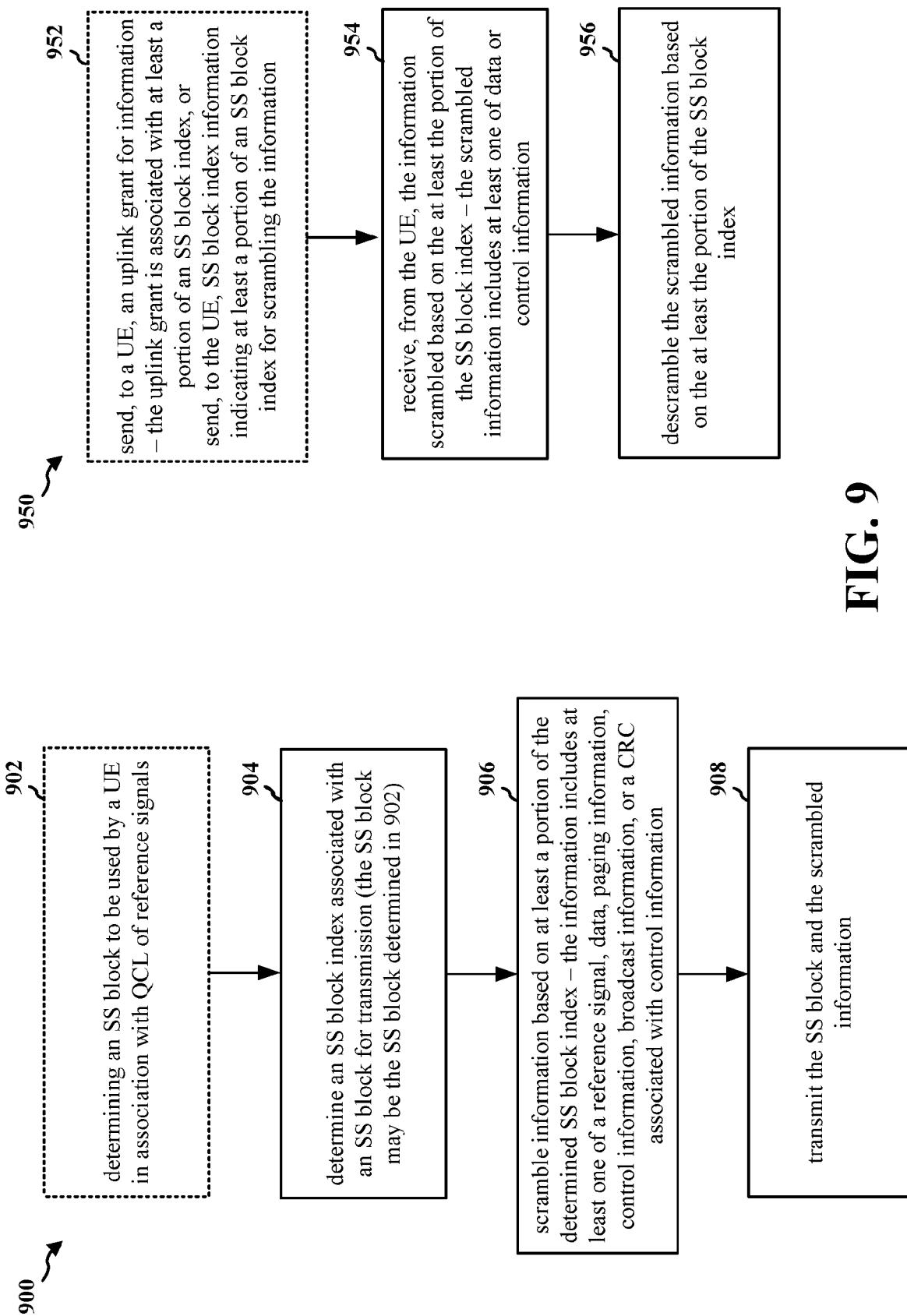


FIG. 9

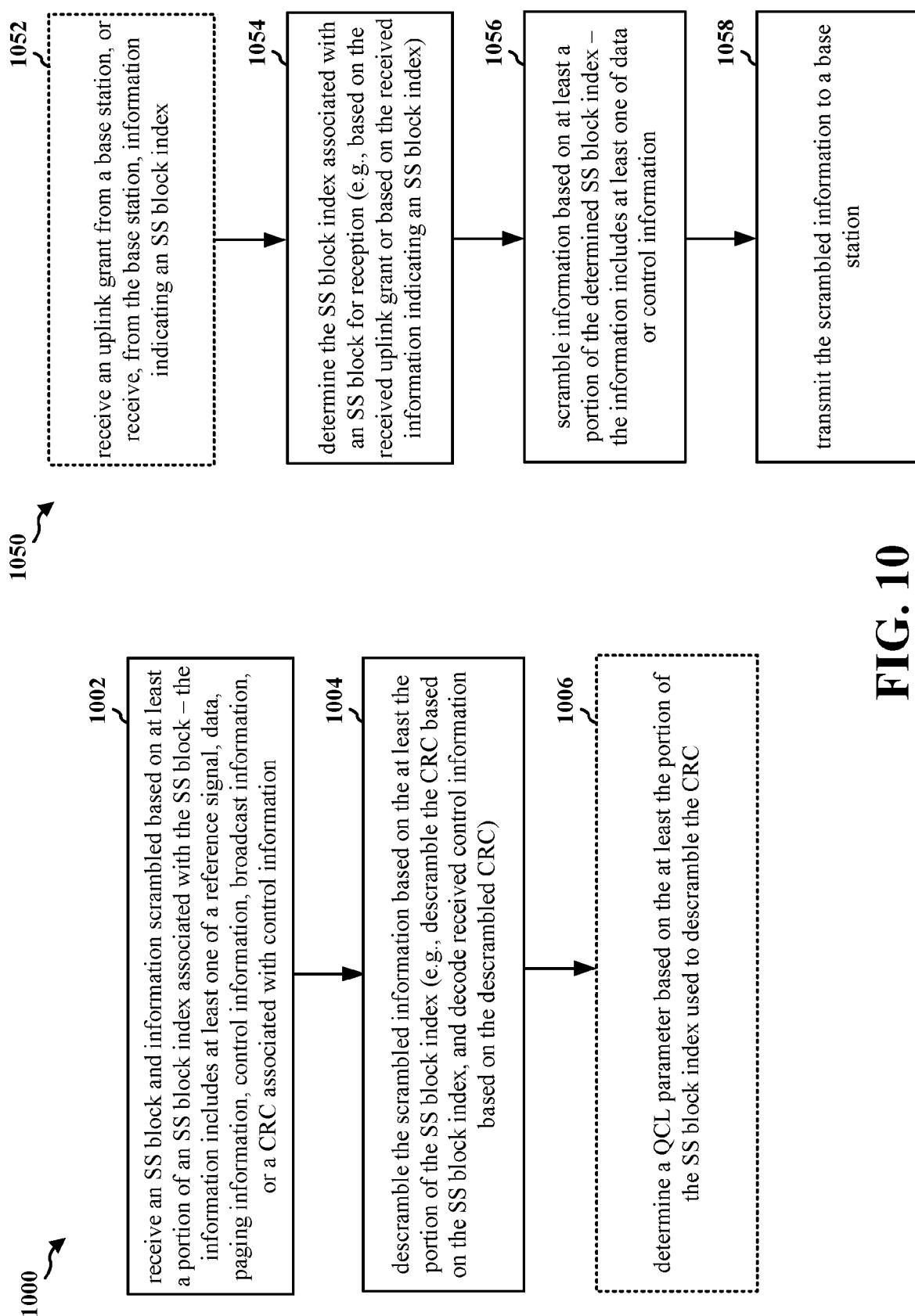


FIG. 10

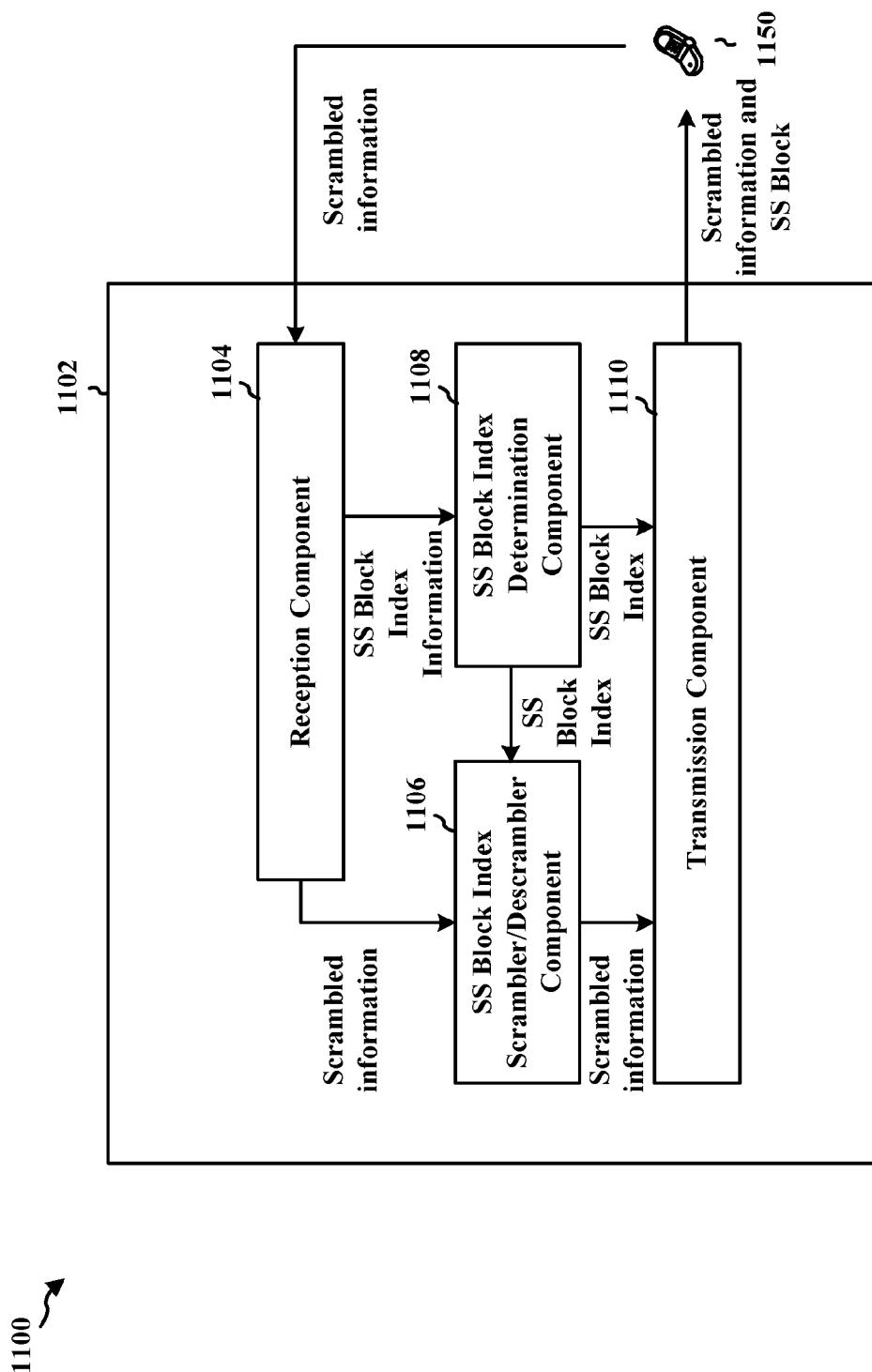


FIG. 11

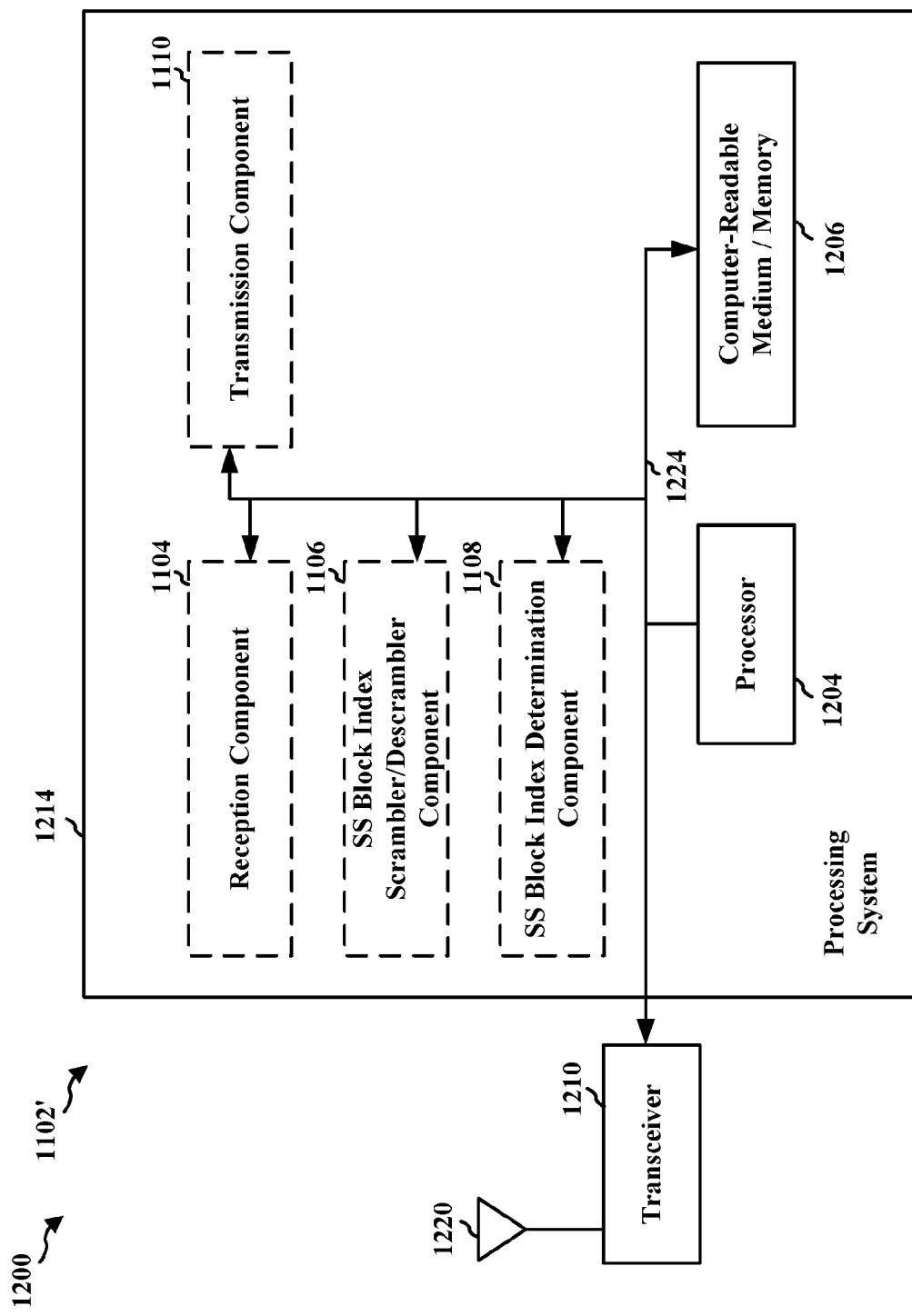


FIG. 12

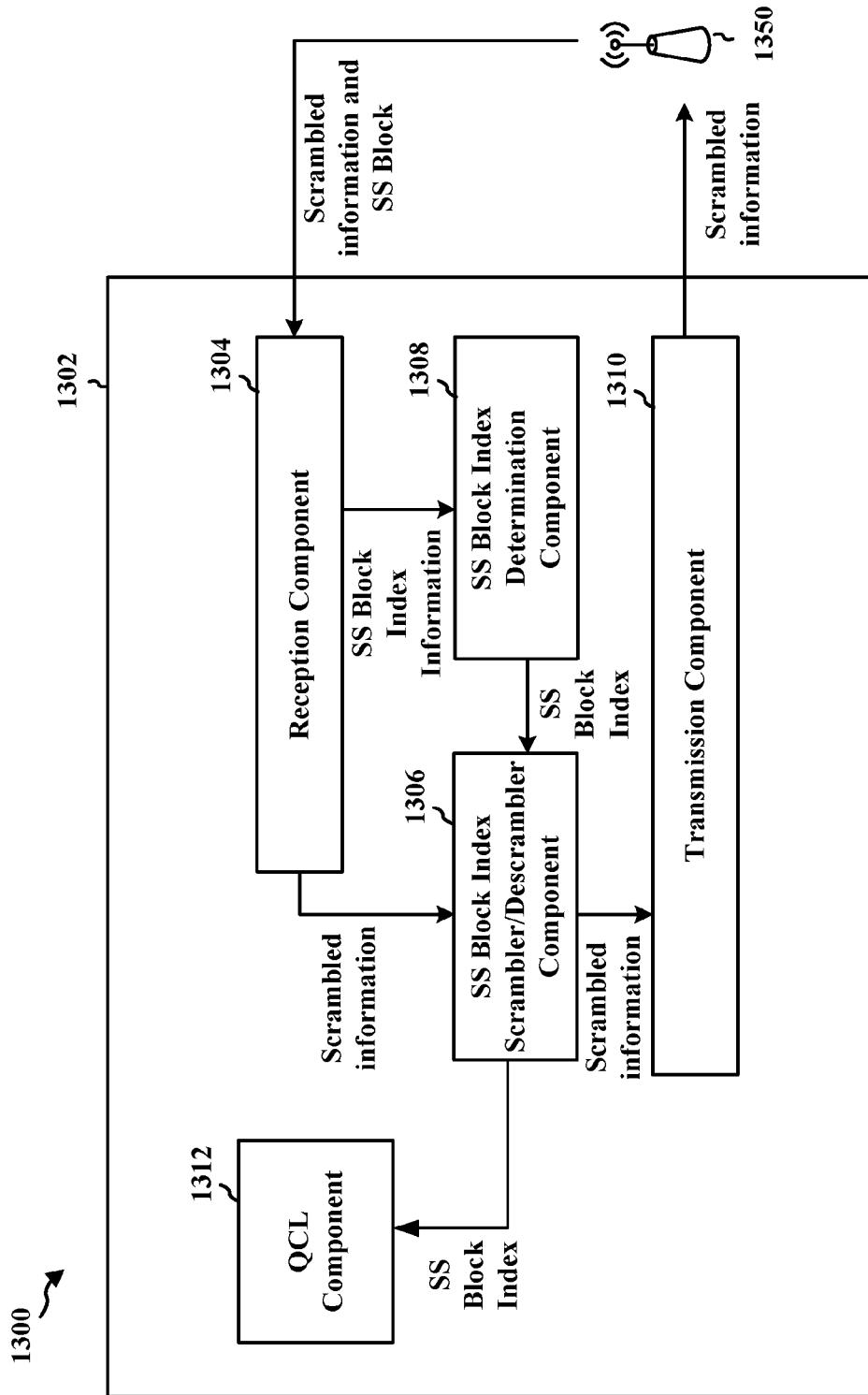


FIG. 13

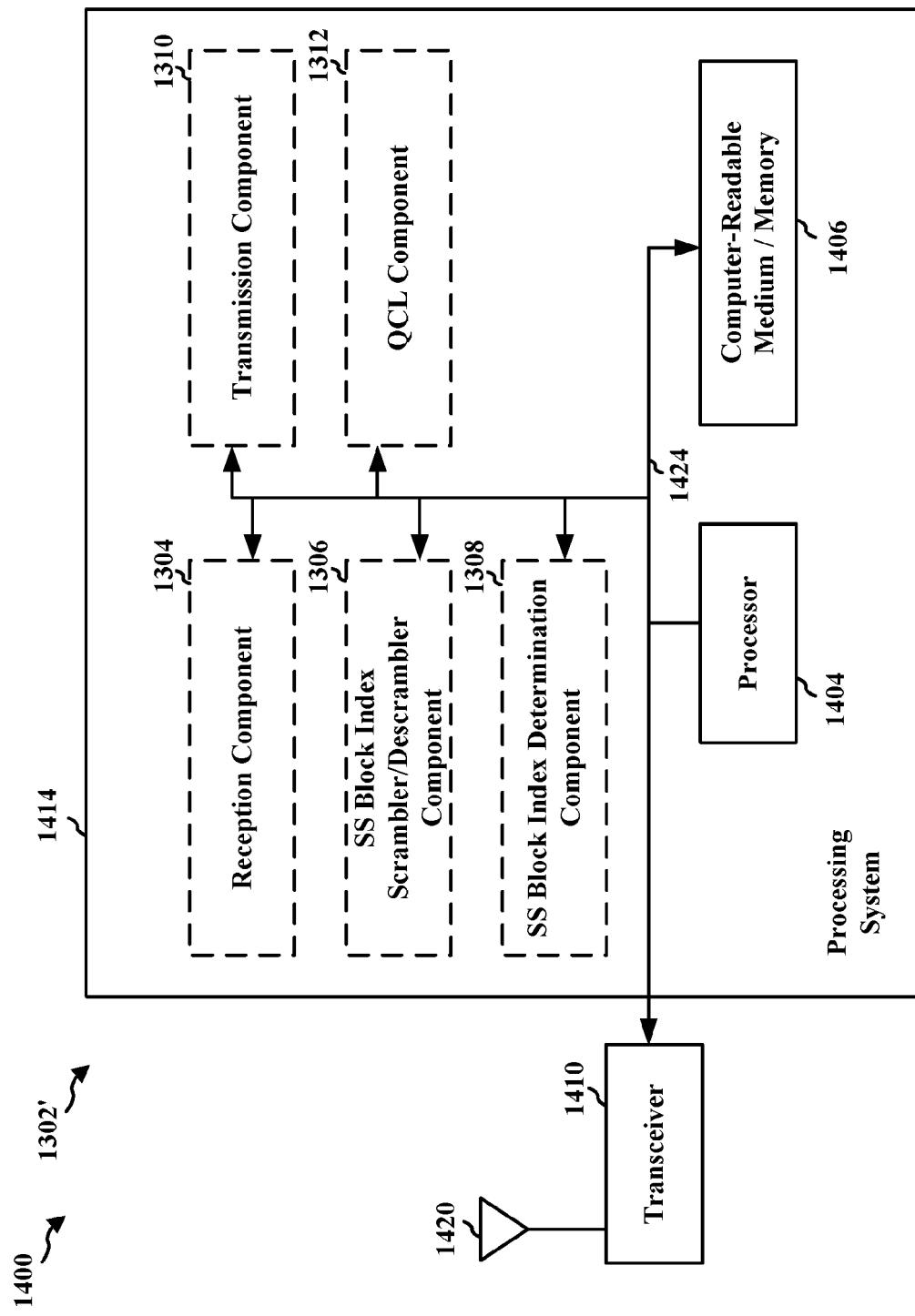


FIG. 14

**REFERENCES CITED IN THE DESCRIPTION**

*This list of references cited by the applicant is for the reader's convenience only. It does not form part of the European patent document. Even though great care has been taken in compiling the references, errors or omissions cannot be excluded and the EPO disclaims all liability in this regard.*

**Non-patent literature cited in the description**

- Discussion on synchronization procedure in NR.  
3GPP Draft; R1-1701850, 12 February 2017 [0003]