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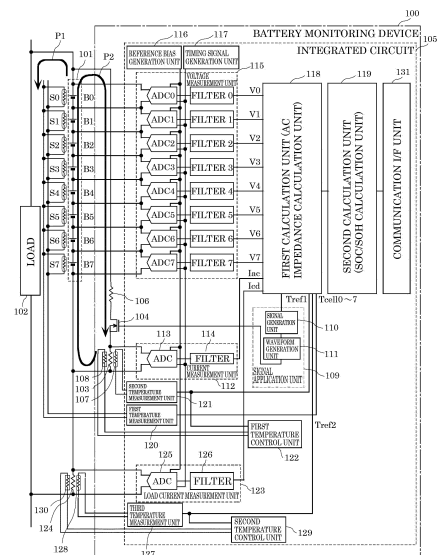
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(54) **BATTERY MONITORING DEVICE, INTEGRATED CIRCUIT, AND BATTERY MONITORING SYSTEM**

(57) A battery monitoring device (100) includes a first reference resistor (103) disposed in a path (P2) different from a path (P1) of current flowing from a battery to a load (102); a transistor (104) for passing current from the battery to the first reference resistor (103); and an integrated circuit (105). The integrated circuit (105) includes: a current measurement unit (112) that measures a first current flowing through the first reference resistor (103); a voltage measurement unit (115) that measures a first voltage of the battery (B); and a first calculation unit (118) that calculates an AC impedance of the battery based on the first current measured and the first voltage measured.

FIG. 1



Description

TECHNICAL FIELD

5 **[0001]** The present disclosure relates to a battery monitoring device that monitors the state of a battery.

BACKGROUND ART

10 **[0002]** Automobiles that run by using a secondary battery as a power supply, such as hybrid electric vehicles (HEVs) or electric vehicles (EVs), have been developed. Techniques are also known in which a battery management system (BMS) is used to estimate the amount of remaining battery power and to detect abnormalities for the safe use of the secondary battery. As such a BMS, PTL 1 discloses a battery monitoring device that can monitor the battery state in real time.

15 Citation List

Patent Literature

20 **[0003]** PTL 1: Japanese Patent No. 5403437

SUMMARY OF THE INVENTION

TECHNICAL PROBLEMS

25 **[0004]** The present disclosure provides a battery monitoring device, an integrated circuit, and a battery monitoring system capable of measuring the AC impedance of a battery irrespective of whether or not the battery is being charged or discharged.

SOLUTIONS TO PROBLEMS

30 **[0005]** A battery monitoring device according to one aspect of the present disclosure includes: a resistor disposed in a path different from a path of current flowing from a battery to a load; a transistor for passing current from the battery to the resistor; and an integrated circuit, wherein the integrated circuit includes: a current measurement unit that measures a first current flowing through the resistor; a voltage measurement unit that measures a first voltage of the battery; and
35 a first calculation unit that calculates an AC impedance of the battery based on the first current measured and the first voltage measured.

40 **[0006]** An integrated circuit according to one aspect of the present disclosure includes: a signal application unit that applies a control signal to a control terminal of a transistor for passing a current from a battery to a resistor disposed in a path different from a path of current flowing from the battery to a load; a current measurement unit that measures a first current flowing through the resistor; a voltage measurement unit that measures a first voltage of the battery; and a first calculation unit that calculates an AC impedance of the battery based on the first current measured and the first voltage measured.

45 **[0007]** A battery monitoring system according to one aspect of the present disclosure includes: a plurality of battery monitoring devices each of which is the above-described battery monitoring device; and an integrated control unit that obtains the AC impedance from each of the plurality of battery monitoring devices.

50 **[0008]** A battery monitoring system according to one aspect of the present disclosure includes: the above-described battery monitoring device; and a server device disposed at a location away from the battery monitoring device, wherein the server device obtains the AC impedance from the battery monitoring device.

ADVANTAGEOUS EFFECTS OF INVENTION

55 **[0009]** An aspect of the present disclosure realizes a battery monitoring device, an integrated circuit, and a battery monitoring system capable of measuring the AC impedance of a battery irrespective of whether or not the battery is being charged or discharged.

BRIEF DESCRIPTION OF DRAWINGS

[0010]

FIG. 1 is a block diagram of a functional configuration of a battery monitoring device according to Embodiment 1.
 FIG. 2 is a diagram illustrating an equivalent circuit of a battery.
 FIG. 3 illustrates a relationship between changes in AC impedance of the battery and deterioration of the battery.
 FIG. 4 is a flowchart of operations of the battery monitoring device according to the embodiment.
 FIG. 5 is a diagram for describing a method of generating a control signal.
 FIG. 6 is a diagram illustrating a detailed configuration of a first calculation unit.
 FIG. 7 is a first diagram for describing a method of generating a control signal according to a variation.
 FIG. 8 is a second diagram for describing a method of generating a control signal according to a variation.
 FIG. 9 is a diagram for describing a method of generating a burst waveform.
 FIG. 10 is a block diagram illustrating a functional configuration of a battery monitoring device that monitors single battery.
 FIG. 11 is a block diagram illustrating a functional configuration of a battery monitoring system according to Embodiment 2.
 FIG. 12 illustrates an outline of a battery monitoring system according to Embodiment 3.

DESCRIPTION OF EXEMPLARY EMBODIMENTS

[0011] Hereinafter, embodiments will be described with reference to the Drawings. It should be noted that each of the subsequently-described embodiments shows a generic or specific example of the present disclosure. The numerical values, shapes, materials, structural components, the arrangement and connection of the structural components, steps, the processing order of the steps, etc. shown in the following embodiments are mere examples, and thus are not intended to limit the present disclosure. Furthermore, among the structural components described in the following embodiments, structural components not recited in any one of the independent claims are described as optional structural components.

[0012] It should be noted that the respective figures are schematic diagrams, and thus are not necessarily accurate illustrations. Furthermore, in the figures, elements which are substantially the same are given the same reference signs, and overlapping description may be omitted or simplified.

EMBODIMENT 1

[Configuration]

[0013] First, the configuration of a battery monitoring device according to Embodiment 1 will be described. FIG. 1 is a block diagram of a functional configuration of the battery monitoring device according to Embodiment 1.

[0014] Battery monitoring device 100 illustrated in FIG. 1 monitors the state of battery pack 101. Battery pack 101 includes batteries B0 to B7 (hereafter, any one of batteries B0 to B7 will be referred to as battery B). Batteries B are, in other words, battery cells. Batteries B are specifically lithium-ion batteries, but may be other types of batteries such as nickel-metal hydride batteries. Battery pack 101 functions as a power supply for load 102, and supplies power to load 102. Exemplary load 102 is, although not limited to, a motor of an EV. Instead of load 102, a charging device for charging battery pack 101 may be connected at the position of load 102.

[0015] Specifically, battery monitoring device 100 can calculate and monitor the AC impedance of battery B. FIG. 2 is a diagram illustrating an equivalent circuit of battery B.

[0016] As illustrated in FIG. 2, battery B can be regarded as circuitry in which resistor R0, resistor R1, and capacitive element C1 connected in parallel, resistor R2 and capacitive element C2 connected in parallel, and so on, are serially connected. A circuit parameter in this circuitry (the resistance of the resistors, or the capacitance of the capacitive elements) changes as battery B deteriorates. That is, the AC impedance of battery B changes as battery B deteriorates. FIG. 3 illustrates the relationship between changes in the AC impedance of battery B and the deterioration of battery B. FIG. 3 is a diagram called a Cole-Cole plot, which is also called a Nyquist plot.

[0017] The AC impedance of battery B has an initial characteristic indicated by a solid line in FIG. 3. If the electrode performance of battery B deteriorates, the characteristic of the AC impedance of battery B changes into a characteristic indicated by a dashed line in FIG. 3. If the electrolyte performance of battery B deteriorates, the characteristic of the AC impedance of battery B changes into a characteristic indicated by a dashed and single-dotted line in FIG. 3.

[0018] As above, the degree of deterioration of battery B is related to the AC impedance of battery B. Battery monitoring device 100 can therefore determine the degree of deterioration of battery B by calculating and monitoring the AC impedance of battery B. Once the degree of deterioration is determined, information processing can be performed such as presenting a message that prompts to replace deteriorated battery B. The degree of deterioration of battery B is represented as, for example, a parameter called state of health (SOH).

[0019] As illustrated in foregoing FIG. 1, in battery monitoring device 100, the AC impedance is measured using first reference resistor 103 disposed in path P2 (in other words, a second path) different from path P1 (in other words, a first

path) of current flowing from battery pack 101 to load 102. This enables battery monitoring device 100 to calculate the AC impedance of battery B irrespective of whether or not battery B is being charged or discharged. For example, battery monitoring device 100 can calculate the present AC impedance of battery B while battery B is being charged. Battery monitoring device 100 can also calculate the present AC impedance of battery B while battery B is being discharged.

[0020] A detailed configuration of battery monitoring device 100 as above will be described below with reference to foregoing FIG. 1. Battery monitoring device 100 includes first reference resistor 103, transistor 104, integrated circuit (battery monitoring circuit) 105, load resistor 106, temperature sensor 107, and heater 108.

[0021] First reference resistor 103 is disposed in path P2 different from path P1 of the current flowing from battery pack 101 to load 102. That is, first reference resistor 103 does not receive the current flowing through load 102. First reference resistor 103 is, for example, a discrete part provided externally to integrated circuit 105.

[0022] Transistor 104 is used to pass current from battery pack 101 to first reference resistor 103. Transistor 104 is a field effect transistor (FET), for example, but may be a bipolar transistor. Transistor 104 has its drain connected to load resistor 106, its source connected to first reference resistor 103, and its gate (that is, a control terminal) connected to signal application unit 109.

[0023] Integrated circuit 105 includes signal application unit 109, current measurement unit 112, voltage measurement unit 115, reference bias generation unit 116, timing signal generation unit 117, first calculation unit 118, second calculation unit 119, first temperature measurement unit 120, second temperature measurement unit 121, first temperature control unit 122, load current measurement unit 123, third temperature measurement unit 127, second temperature control unit 129, and communication interface unit 131.

[0024] Signal application unit 109 applies a control signal to the control terminal of transistor 104. Signal application unit 109 includes signal generation unit 110 and waveform generation unit 111.

[0025] Current measurement unit 112 measures the current I_{ac} (an example of a first current) flowing through first reference resistor 103. Specifically, current measurement unit 112 measures the voltage at both ends of first reference resistor 103. Based on the resistance of first reference resistor 103, first calculation unit 118 can recognize the voltage at both ends of first reference resistor 103 as the current I_{ac} .

[0026] Specifically, current measurement unit 112 includes: analog-to-digital (AD) converter 113 (shown as ADC) that converts the voltage at both ends of first reference resistor 103 (that is, an analog signal) into a digital signal; and filter 114 connected to an output of AD converter 113. AD converter 113 is a delta-sigma AD converter, for example, and filter 114 is a decimation filter. AD converter 113 is implemented by, for example, an AD converter having the same AD conversion characteristics as AD converters 0 to 7 (that is, an identical product). This can reduce measurement errors occurring between AD converters 0 to 7 and AD converter 113 due to AD conversion.

[0027] Voltage measurement unit 115 measures the voltages V_0 to V_7 (examples of a first voltage) of batteries B0 to B7 in battery pack 101. Voltage measurement unit 115 includes: AD converters 0 to 7 that convert the voltages V_0 to V_7 of batteries B0 to B7 into digital signals; and filters 0 to 7 connected to outputs of AD converters 0 to 7. Each of AD converters 0 to 7 is a delta-sigma AD converter, for example, and each of filters 0 to 7 is a decimation filter.

[0028] AD converters 0 to 7 in integrated circuit 105 all have the same AD conversion characteristics. AD conversion characteristics are parameters such as resolution (the number of bits). Specifically, AD converters 0 to 7 are implemented by an identical AD converter product. This can reduce measurement errors occurring among the voltages V_0 to V_7 due to AD conversion.

[0029] Reference bias generation unit 116 supplies a common reference voltage to AD converters 0 to 7, AD converter 113, and AD converter 125. Reference bias generation unit 116 can reduce AD conversion errors due to variations in reference voltage.

[0030] Timing signal generation unit 117 supplies, to AD converters 0 to 7, AD converter 113, and AD converter 125, a timing signal for synchronizing measurement times of AD converters 0 to 7, AD converter 113, and AD converter 125. Timing signal generation unit 117 enables measuring the voltages V_0 to V_7 , the current I_{ac} , and a current I_{cd} at a single time.

[0031] First calculation unit 118 calculates the AC impedances of batteries B0 to B7 based on the current I_{ac} measured by current measurement unit 112 and the voltages V_0 to V_7 measured by voltage measurement unit 115. First calculation unit 118 is, in other words, an AC impedance calculation unit. A detailed configuration of first calculation unit 118 will be described below.

[0032] Second calculation unit 119 calculates at least one of the state of charge (SOC) and the SOH of each of batteries B0 to B7 using the AC impedance calculated by first calculation unit 118. Second calculation unit 119 is, in other words, an SOC/SOH calculation unit.

[0033] First temperature measurement unit 120 measures the temperatures T_{cell0} to T_{cell7} of batteries B0 to B7 using temperature sensors S0 to S7 provided in one-to-one correspondence for batteries B0 to B7. Temperature sensors S0 to S7 are thermistor-based temperature sensors, for example, but may be temperature sensors based on other

elements such as a thermocouple.

[0034] Second temperature measurement unit 121 measures the temperature Tref1 of first reference resistor 103 using temperature sensor 107 provided near first reference resistor 103. Temperature sensor 107 is a thermistor-based temperature sensor, for example, but may be a temperature sensor based on other elements such as a thermocouple.

[0035] First temperature control unit 122 controls the temperature of first reference resistor 103 to be constant. Specifically, first temperature control unit 122 obtains the temperature Tref1 of first reference resistor 103 measured by second temperature measurement unit 121 and controls heater 108 so that the temperature Tref1 obtained is kept constant.

[0036] Load current measurement unit 123 measures the current Icd flowing through load 102. Specifically, load current measurement unit 123 measures the voltage at both ends of second reference resistor 124. Based on the resistance of second reference resistor 124, first calculation unit 118 can recognize the voltage at both ends of second reference resistor 124 as the current Icd.

[0037] Specifically, load current measurement unit 123 includes: an AD converter 125 that converts the voltage (that is, an analog signal) at both ends of second reference resistor 124 into a digital signal; and filter 126 connected to an output of AD converter 125. AD converter 125 is a delta-sigma AD converter, for example, and filter 126 is a decimation filter. AD converter 125 is implemented by, for example, an AD converter having the same AD conversion characteristics as AD converters 0 to 7 (that is, an identical product). This can reduce errors occurring between AD converters 0 to 7 and AD converter 125 due to AD conversion.

[0038] Third temperature measurement unit 127 measures the temperature Tref2 of second reference resistor 124 using temperature sensor 128 provided near second reference resistor 124. Temperature sensor 128 is a thermistor-based temperature sensor, for example, but may be a temperature sensor based on other elements such as a thermocouple.

[0039] Second temperature control unit 129 controls the temperature of second reference resistor 124 to be constant. Specifically, second temperature control unit 129 obtains the temperature Tref2 of second reference resistor 124 measured by third temperature measurement unit 127 and controls heater 130 so that the temperature Tref2 obtained is kept constant.

[0040] Communication interface unit 131 is a communication circuit for battery monitoring device 100 to communicate with other battery monitoring devices or external devices. For example, communication interface unit 131 is used for transmitting the SOH calculated by second calculation unit or the like to an external device. Communication interface unit 131 may perform wireless or wired communication. Any communication standard may be adopted for communication performed by communication interface unit 131.

[Operations]

[0041] Now, operations of battery monitoring device 100 will be described. FIG. 4 is a flowchart of operations of battery monitoring device 100.

[0042] First, signal application unit 109 applies a control signal to the control terminal of transistor 104 (S11). As a result, transistor 104 is turned on to pass current to first reference resistor 103.

[0043] In the above operation, signal application unit 109 generates a control signal having multiple frequency components. FIG. 5 is a diagram for describing a method of generating the control signal.

[0044] For example, signal generation unit 110 in signal application unit 109 generates a sine wave of a frequency f1, a sine wave of a frequency f2, and a sine wave of a frequency f3, combines these sine waves, and outputs the resultant combined signal. Fourier-transforming the combined signal reveals three frequency components: the frequency f1, the frequency f2, and the frequency f3.

[0045] The signal output from signal generation unit 110 is a digital signal, which is converted by waveform generation unit 111 into an analog signal. That is, waveform generation unit 111 is a digital-to-analog (DA) converter, for example.

[0046] According to the control signal as above, current including multiple frequency components flows through first reference resistor 103. First calculation unit 118 can therefore calculate an AC impedance for each of the frequencies.

[0047] Current measurement unit 112 then measures the current Iac flowing through first reference resistor 103 (S12). Specifically, current measurement unit 112 measures the voltage at both ends of first reference resistor 103. The voltage at both ends of first reference resistor 103 is converted by AD converter 113 into a digital signal, which is output to first calculation unit 118 via filter 114.

[0048] Voltage measurement unit 115 then measures the voltages V0 to V7 of batteries B0 to B7 (S13). The voltages V0 to V7 are converted by AD converters 0 to 7 into digital signals, which are output to first calculation unit 118 via filters 0 to 7.

[0049] First calculation unit 118 then calculates the AC impedances of batteries B0 to B7 based on the current Iac measured and the voltages V0 to V7 measured (S14). FIG. 6 is a diagram illustrating a detailed configuration of first calculation unit 118.

[0050] As illustrated in FIG. 6, specifically, first calculation unit 118 includes phase shift unit 118a, conversion unit 118b, integration unit 118c, impedance calculation unit 118d, temperature correction unit 118e, and Kalman filter unit 118f. FIG. 6 also illustrates signal generation unit 110 and second calculation unit 119.

[0051] Signal generation unit 110 generates a sine wave (an example of a first signal). Phase shift unit 118a shifts the sine wave 90 degrees to generate a cosine wave (an example of a second signal). Phase shift unit 118a is implemented by a delay circuit, for example. In battery monitoring device 100, signal generation unit 110 (that is, signal generation unit 110 for generating the control signal) in signal application unit 109 also serves as a signal generation unit for generating the sine wave for obtaining a complex current and complex voltages. Alternatively, the sine wave for obtaining the complex voltages may be generated by a signal generation unit different from signal generation unit 110.

[0052] Conversion unit 118b converts the current I_{ac} into a complex current (an example of a second current) by multiplying the current I_{ac} by each of the sine wave generated by signal generation unit 110 and the cosine wave generated by phase shift unit 118a. Conversion unit 118b also converts the voltages V_0 to V_7 into complex voltages (examples of a second voltage) by multiplying the voltages V_0 to V_7 by each of the sine wave generated by signal generation unit 110 and the cosine wave generated by phase shift unit 118a. Conversion unit 118b is implemented by a multiplication circuit, for example.

[0053] Integration unit 118c performs averaging processing on the complex current and averaging processing on the complex voltages. Integration unit 118c is implemented by an integration circuit, for example.

[0054] Impedance calculation unit 118d calculates AC impedances (shown as high-frequency impedances in FIG. 6) based on the complex current subjected to the averaging processing and the complex voltages subjected to the averaging processing. Impedance calculation unit 118d calculates each AC impedance by dividing the complex voltage subjected to the averaging processing by the complex current subjected to the averaging processing. For example, as the AC impedance of battery B0, an impedance real part Z_{0re} and an impedance imaginary part Z_{0im} are output. Impedance calculation unit 118d is implemented by a dividing circuit, for example.

[0055] Temperature correction unit 118e corrects the AC impedances based on the temperature T_{ref1} of first reference resistor 103 measured by second temperature measurement unit 121. For example, temperature correction unit 118e corrects the AC impedances in the environment at the temperature T_{ref1} to acquire AC impedances that would be obtained in an environment at a reference temperature.

[0056] As described above, first reference resistor 103 is disposed in path P2 different from path P1 of the current flowing from battery B to load 102. Battery monitoring device 100 can therefore calculate the AC impedance of battery B irrespective of whether or not battery B is being charged or discharged. The AC impedance of battery B is continuously measured, for example, but may be measured periodically (that is, intermittently) or in response to an instruction from an external device (for example, an integrated control unit to be described below).

[Calculating SOH and SOC]

[0057] Second calculation unit 119 calculates the SOHs of batteries B0 to B7 by estimating a circuit parameter of the equivalent circuits of batteries B0 to B7 based on the AC impedances calculated by first calculation unit 118. Calculating the SOHs based on the circuit parameter estimation may adopt known methods.

[0058] Calculating the SOHs (that is, estimating the circuit parameter) may use a low-frequency impedance in addition to the AC impedances (in other words, high-frequency impedances). The low-frequency impedance is an AC impedance for a frequency lower than the frequency for the AC impedances calculated by first calculation unit 118. By taking the low-frequency impedance into account in estimating the circuit parameter, the accuracy of SOH calculation can be increased.

[0059] The low-frequency impedance is calculated by Kalman filter unit 118f. Kalman filter unit 118f calculates the low-frequency AC impedance using at least one of a charging current during the charging of batteries B0 to B7 and a discharging current during the discharging of batteries B0 to B7.

[0060] Calculating the SOHs (that is, estimating the circuit parameter) may also use the temperatures T_{cell0} to T_{cell7} of batteries B0 to B7 measured by first temperature measurement unit 120. By taking the temperatures T_{cell0} to T_{cell7} of batteries B0 to B7 into account in estimating the circuit parameter, the accuracy of SOH calculation can be increased.

[0061] Second calculation unit 119 may also calculate the SOC of batteries B0 to B7 by estimating the circuit parameter of the equivalent circuits of batteries B0 to B7 based on the AC impedances calculated by first calculation unit 118. Calculating the SOC based on circuit parameter estimation may adopt known methods.

[Variations of control signal generation]

[0062] The method of generating the control signal having multiple frequency components by signal application unit 109 is not limited to the one illustrated in FIG. 5. FIG. 7 is a first diagram for describing a method of generating the control signal according to a variation.

[0063] In the example of FIG. 7, signal generation unit 110 outputs a rectangular wave as the control signal. Signal generation unit 110 in this case has a configuration similar to that of a pulse generator, and can change the frequency and the voltage value of the control signal. Fourier-transforming the rectangular wave reveals multiple frequency components.

[0064] The signal output from signal generation unit 110 is a digital signal, which is converted by waveform generation unit 111 into an analog signal. In the example of FIG. 7, waveform generation unit 111 may be eliminated.

[0065] FIG. 8 is a second diagram for describing a method of generating the control signal according to a variation. In the example of FIG. 8, signal generation unit 110 outputs a sine wave as the control signal.

[0066] Waveform generation unit 111 includes burst waveform generation unit 111a. Based on the sine wave output from signal generation unit 110, burst waveform generation unit 111a generates a control signal having a burst waveform. FIG. 9 is a diagram for describing a method of generating the burst waveform.

[0067] The burst waveform is a waveform that includes a rectangular wave only in part of the time domain. As illustrated in FIG. 9, for example, the sine wave takes positive values in first intervals and takes negative values in second intervals, and burst waveform generation unit 111a selectively outputs the rectangular wave only in first intervals. That is, the burst waveform alternately has a first interval in which a rectangular wave is output and a second interval in which no rectangular wave is output. The control signal in this case has multiple frequency components in the first intervals.

[0068] According to the control signal having the burst waveform as above, a rectangular wave is intermittently output. This reduces power consumption.

[Variations of the number of batteries]

[0069] Although battery pack 101 includes eight batteries B0 to B7, battery pack 101 may include more or less than eight batteries. Battery monitoring device 100 may also monitor single battery B. FIG. 10 is a block diagram illustrating a functional configuration of battery monitoring device 100c that monitors single battery B0.

[0070] Thus, any number of integrated circuits 105 and any number of battery monitoring devices 100 may be used to scalably implement a mechanism that measures any number of battery cells.

[Advantageous effects]

[0071] As described above, battery monitoring device 100 includes: first reference resistor 103 disposed in path P2 different from path P1 of current flowing from battery B to load 102; transistor 104 for passing current from battery B to first reference resistor 103; and integrated circuit 105. Integrated circuit 105 includes: signal application unit 109 that applies a control signal to a control terminal of transistor 104; current measurement unit 112 that measures a first current flowing through first reference resistor 103; voltage measurement unit 115 that measures a first voltage of battery B; and first calculation unit 118 that calculates an AC impedance of battery B based on the first current measured and the first voltage measured.

[0072] Because first reference resistor 103 is disposed in path P2 different from path P1 of the current flowing from battery B to load 102, battery monitoring device 100 as above can measure the AC impedance of battery B irrespective of whether battery B is being charged or discharged.

[0073] Signal application unit 109 applies the control signal having multiple frequency components to the control terminal.

[0074] Signal application unit 109 as above enables first calculation unit 118 to calculate the AC impedance for each of multiple frequencies.

[0075] For example, signal application unit 109 is capable of changing a frequency and a voltage value of the control signal.

[0076] Signal application unit 109 as above enables arbitrarily adjusting the current flowing through first reference resistor 103.

[0077] For example, the control signal has a burst waveform.

[0078] The control signal having the burst waveform as above causes a rectangular wave to be intermittently output, leading to reduced power consumption.

[0079] For example, integrated circuit 105 includes: signal generation unit 110 that generates a first signal that is a sine wave; and phase shift unit 118a that shifts a phase of the first signal 90 degrees to generate a second signal. First calculation unit 118 converts the first current into a second current that is a complex current by multiplying each of the first signal and the second signal generated and the first current measured, converts the first voltage into a second voltage that is a complex voltage by multiplying each of the first signal and the second signal generated and the first voltage measured, and calculates the AC impedance based on the second current and the second voltage.

[0080] Battery monitoring device 100 as above can calculate the AC impedance based on the complex voltage and the complex current.

[0081] For example, first calculation unit 118 performs averaging processing on the second current and averaging processing on the second voltage, and calculates the AC impedance based on the second current subjected to the averaging processing and the second voltage subjected to the averaging processing.

[0082] Through averaging, battery monitoring device 100 as above can increase the accuracy of calculating the AC impedance.

[0083] For example, integrated circuit 105 further includes second calculation unit 119 that calculates at least one of an SOC and an SOH of battery B using the AC impedance calculated.

[0084] Battery monitoring device 100 as above can calculate at least one of the SOC and the SOH.

[0085] For example, first calculation unit 118 calculates the AC impedance for each of multiple frequencies, and second calculation unit 119 calculates the SOH using the AC impedance for each of the multiple frequencies.

[0086] Battery monitoring device 100 as above can calculate the SOH using the AC impedance for each of the multiple frequencies.

[0087] For example, integrated circuit 105 further includes first temperature measurement unit 120 that measures a temperature of battery B. Second calculation unit 119 calculates the SOH using the AC impedance calculated and the temperature of battery B measured.

[0088] Battery monitoring device 100 as above can increase the accuracy of calculating the SOH by taking the temperature of battery B into account in estimating a circuit parameter of an equivalent circuit of battery B.

[0089] For example, first calculation unit 118 further includes Kalman filter unit 118f that calculates a low-frequency AC impedance using at least one of a charging current during charging of battery B and a discharging current during discharging of battery B. Second calculation unit 119 calculates the SOH using the AC impedance calculated and the low-frequency AC impedance calculated.

[0090] Battery monitoring device 100 as above can increase the accuracy of calculating the SOH by taking the low-frequency AC impedance of battery B into account in estimating the circuit parameter of the equivalent circuit of battery B.

[0091] For example, integrated circuit 105 further includes second temperature measurement unit 121 that measures a temperature of first reference resistor 103. First calculation unit 118 calculates the AC impedance by further using the temperature of first reference resistor 103 measured.

[0092] Battery monitoring device 100 as above can correct the AC impedance using the temperature of first reference resistor 103.

[0093] For example, integrated circuit 105 has first temperature control unit 122 that controls the temperature of first reference resistor 103 to be constant.

[0094] Battery monitoring device 100 as above can calculate the AC impedance while the temperature of first reference resistor 103 is kept constant.

[0095] For example, first calculation unit 118 calculates the AC impedance during charging of battery B.

[0096] Battery monitoring device 100 as above can calculate the AC impedance during charging of battery B.

[0097] For example, first calculation unit 118 calculates the AC impedance during discharging of battery B.

[0098] Battery monitoring device 100 as above can calculate the AC impedance during discharging of battery B.

[0099] For example, first calculation unit 118 calculates the AC impedance during a stoppage of charging and discharging of battery B.

[0100] Battery monitoring device 100 as above can calculate the AC impedance during a stoppage of charging and discharging of battery B.

[0101] For example, battery B is one of batteries B0 to B7 in battery pack 101.

[0102] Battery monitoring device 100 as above can calculate the AC impedance of battery B in battery pack 101.

[0103] Integrated circuit 105 includes: transistor 104 for passing a current from battery B to first reference resistor 103 disposed in path P2 different from path P1 of current flowing from battery B to load 102; and integrated circuit 105.

Integrated circuit 105 includes: signal application unit 109 that applies a control signal to a control terminal of transistor 104; current measurement unit 112 that measures a first current flowing through first reference resistor 103; voltage measurement unit 115 that measures a first voltage of battery B; and first calculation unit 118 that calculates an AC impedance of battery B based on the first current measured and the first voltage measured.

[0104] Because first reference resistor 103 is disposed in path P2 different from path P1 of current flowing from battery B to load 102, integrated circuit 105 as above can measure the AC impedance of battery B irrespective of whether or not battery B is being charged or discharged.

[0105] For example, battery B is one of batteries B0 to B7 in battery pack 101, and voltage measurement unit 115 includes AD converters 0 to 7 for measuring voltages of respective batteries B0 to B7. Integrated circuit 105 further includes reference bias generation unit 116 that supplies a common reference voltage to AD converters 0 to 7.

[0106] Integrated circuit 105 as above can reduce AD conversion errors due to variations in reference voltage.

[0107] For example, battery B is one of batteries B0 to B7 in battery pack 101, and voltage measurement unit 115 includes AD converters 0 to 7 for measuring voltages V0 to V7 of respective batteries B0 to B7. Integrated circuit 105 further includes timing signal generation unit 117 that supplies, to each of AD converters 0 to 7, a timing signal for

synchronizing measurement times of AD converters 0 to 7.

[0108] Integrated circuit 105 as above can measure the voltages V0 to V7 at a single time.

[0109] For example, AD converters 0 to 7 have identical AD conversion characteristics.

[0110] Integrated circuit 105 as above can reduce AD conversion variations of AD converters 0 to 7.

EMBODIMENT 2

[Configuration and operations]

[0111] In Embodiment 2, a battery monitoring system that includes at least multiple battery monitoring devices 100 will be described. FIG. 11 is a block diagram illustrating a functional configuration of the battery monitoring system according to Embodiment 2.

[0112] As illustrated in FIG. 11, battery monitoring system 200 includes multiple battery monitoring devices (specifically, battery monitoring devices 100, 100a, and 100b), integrated control unit 201, and switch 202. Battery monitoring device 100 monitors battery pack 101, battery monitoring device 100a monitors battery pack 101a, and battery monitoring device 100b monitors battery pack 101b. Battery packs 101, 101a, and 101b are serially connected.

[0113] Integrated circuit 105 in battery monitoring device 100 includes communication interface unit 131. Battery monitoring device 100a is configured as with battery monitoring device 100, and integrated circuit 105a in battery monitoring device 100a includes communication interface unit 131a. Battery monitoring device 100b is configured as with battery monitoring device 100, and integrated circuit 105b in battery monitoring device 100b includes communication interface unit 131b.

[0114] Communication interface units 131, 131a, and 131b are daisy-chained. Communication interface unit 131 is connected to communication interface unit 131a via communication line 203, and communication interface unit 131a is connected to communication interface unit 131b via communication line 204. Communication interface unit 131b is connected to integrated control unit 201 via communication line 205.

[0115] Through communication lines 203, 204, and 205 as above, integrated control unit 201 can obtain the AC impedances of battery pack 101 from battery monitoring device 100, obtain the AC impedances of battery pack 101a from battery monitoring device 100a, and obtain the AC impedances of battery pack 101a from battery monitoring device 100b. It is to be noted that integrated control unit 201 may obtain, from each of battery monitoring devices 100, 100a, and 100b, AC impedance-based information different from the AC impedances (for example, SOHs).

[0116] Integrated control unit 201 performs various sorts of control based on the AC impedances obtained from battery monitoring devices 100, 100a, and 100b. For example, if integrated control unit 201 determines that at least one of battery packs 101, 101a, and 101b has deteriorated to below a predetermined criterion based on the AC impedances, integrated control unit 201 opens switch 202 to electrically disconnect load 102 from battery packs 101, 101a, and 101b. This can prevent an accident such as a battery pack fire due to the use of a deteriorated battery pack. Integrated control unit 201 is implemented by, for example, a microcomputer that includes a processor and memory.

[0117] Although communication interface units 131, 131a, and 131b are daisy-chained in battery monitoring system 200, this is merely an exemplary manner of connection. In battery monitoring system 200, communication interface units 131, 131a, and 131b may each be directly connected with integrated control unit 201. That is, the communication interfaces may be connected in any manner.

[Advantageous effects]

[0118] As described above, battery monitoring system 200 includes: multiple battery monitoring devices; and integrated control unit 201 that obtains AC impedances from each of the multiple battery monitoring devices.

[0119] Integrated control unit 201 can thus perform various sorts of control based on the AC impedances obtained from each of the battery monitoring devices.

EMBODIMENT 3

[Configuration and operations]

[0120] In Embodiment 3, a battery monitoring system that includes a cloud server will be described. FIG. 12 illustrates an outline of the battery monitoring system according to Embodiment 3.

[0121] As illustrated in FIG. 12, battery monitoring system 300 includes battery monitoring device 100 and server device 301. Server device 301 is located remote from battery monitoring device 100. Server device 301 is what is called a cloud server. For example, server device 301 is communicatively connected with other server devices over cloud network 302.

[0122] For example, battery monitoring device 100 is provided in automobile 400 such as an EV and monitors battery pack 101 used to drive motor 401 of automobile 400. Communication interface unit 131 in battery monitoring device 100, for example, transmits the AC impedances of battery pack 101 calculated to server device 301 through wireless communication. A relay device not shown may intervene between communication interface unit 131 and server device 301.

[0123] Server device 301 obtains the AC impedances from battery monitoring device 100 and stores them in storage (not shown) in server device 301. Server device 301 performs various sorts of control based on the AC impedances obtained from battery monitoring device 100.

[0124] For example, if server device 301 determines that battery pack 101 has deteriorated to below a predetermined criterion based on the AC impedances, server device 301 transmits information for stopping the use of battery pack 101 to battery monitoring device 100. This can prevent an accident such as battery pack 101 catching fire due to the use of deteriorated battery pack 101.

[Advantageous effects]

[0125] As described above, battery monitoring system 300 includes battery monitoring device 100 and server device 301 located remote from battery monitoring device 100. Server device 301 obtains AC impedances from battery monitoring device 100.

[0126] Server device 301 can thus perform various sorts of control based on the AC impedances obtained from battery monitoring device 100.

OTHER EMBODIMENTS

[0127] Although an embodiment has been described up to this point, the present disclosure is not limited to the above-described embodiment.

[0128] For example, although a battery monitoring device that monitors a battery used in vehicles such as EVs is described in the foregoing embodiment, the battery monitoring device is may monitor a battery for any purpose.

[0129] Furthermore, the circuit configurations described in the foregoing embodiment are mere examples, and the present disclosure is not limited to the above-described circuit configurations. Specifically, the present disclosure also encompasses a circuit capable of realizing the characteristic functions of the present disclosure in the same manner as the above-described circuit configurations. For example, the present disclosure also encompasses a circuit configuration in which an element such as a switching element (transistor), a resistive element, or a capacitive element is connected in series or in parallel to a certain element, within a scope that realizes the same functions as the above-described circuit configurations.

[0130] Furthermore, in the foregoing embodiment, the structural components included in the integrated circuit are implemented using hardware. However, part of the structural components included in the integrated circuit may be implemented by executing a software program suitable for the structural component. Part of the structural components included in the integrated circuit may be implemented by means of a program executor, such as a CPU or a processor, reading and executing the software program recorded on a recording medium such as a hard disk or a semiconductor memory.

[0131] Furthermore, in the foregoing embodiment, processes executed by a specific processing unit may be executed by another processing unit. Moreover, in the operations described in the foregoing embodiment, the order of processes may be changed or processes may be executed in parallel.

[0132] Aside from the above, the present disclosure also encompasses forms obtained by various modification to respective embodiments that can be conceived by a person of skill in the art, or forms realized by any combination of structural components and functions in the respective embodiments without departing from the spirit of the present disclosure.

REFERENCE MARKS IN THE DRAWINGS

[0133]

100, 100a, 100b, 100c	battery monitoring device
101, 101a, 101b	battery pack
102	load
103	first reference resistor
104	transistor
105, 105a, 105b	integrated circuit (battery monitoring circuit)

	106	load resistor
	107, 128	temperature sensor
	108, 130	heater
	109	signal application unit
5	110	signal generation unit
	111	waveform generation unit
	111a	burst waveform generation unit
	112	current measurement unit
	113, 125	AD converter
10	114, 126	filter
	115	voltage measurement unit
	116	reference bias generation unit
	117	timing signal generation unit
	118	first calculation unit
15	118a	phase shift unit
	118b	conversion unit
	118c	integration unit
	118d	impedance calculation unit
	118e	temperature correction unit
20	118f	Kalman filter unit
	119	second calculation unit
	120	first temperature measurement unit
	121	second temperature measurement unit
	122	first temperature control unit
25	123	load current measurement unit
	124	second reference resistor
	127	third temperature measurement unit
	129	second temperature control unit
	131, 131a, 131b	communication interface unit
30	200, 300	battery monitoring system
	201	integrated control unit
	202	switch
	203, 204, 205	communication line
	301	server device
35	302	cloud network
	400	automobile
	401	motor
	B, B0 - B7	battery
	Iac, Iad	current
40	V0 - V7	voltage

Claims

45 1. A battery monitoring device, comprising:

a resistor disposed in a path different from a path of current flowing from a battery to a load;
a transistor for passing current from the battery to the resistor; and
an integrated circuit, wherein
50 the integrated circuit includes:

a current measurement unit that measures a first current flowing through the resistor;
a voltage measurement unit that measures a first voltage of the battery; and
a first calculation unit that calculates an AC impedance of the battery based on the first current measured
55 and the first voltage measured.

2. The battery monitoring device according to claim 1, wherein
the integrated circuit further includes a signal application unit that applies a control signal to a control terminal of

the transistor, and
the signal application unit applies the control signal having a plurality of frequency components to the control terminal.

3. The battery monitoring device according to claim 2, wherein
the signal application unit is capable of changing a frequency and a voltage value of the control signal.
4. The battery monitoring device according to claim 2 or claim 3, wherein
the control signal has a burst waveform.
5. The battery monitoring device according to any one of claims 1 to 4, wherein
the integrated circuit further includes a signal generation unit that generates a first signal which is a sine wave; and
a phase shift unit that shifts a phase of the first signal by 90 degrees to generate a second signal.
6. The battery monitoring device according to claim 5, wherein
the first calculation unit:

converts the first current into a second current which is a complex current, by multiplying each of the first signal
and the second signal generated and the first current measured; and
calculates the AC impedance based on the second current.
7. The battery monitoring device according to claim 5 or claim 6, wherein
the first calculation unit:

converts the first voltage into a second voltage which is a complex voltage, by multiplying each of the first signal
and the second signal generated and the first voltage measured; and
calculates the AC impedance based on the second voltage.
8. The battery monitoring device according to any one of claims 1 to 7, wherein
the integrated circuit further includes a second calculation unit that calculates at least one of a state of charge (SOC)
or a state of health (SOH) of the battery using the AC impedance calculated.
9. The battery monitoring device according to any one of claims 1 to 8, wherein
the first calculation unit further includes a Kalman filter unit that calculates a low frequency impedance using at least
one of a charging current during charging of the battery or a discharging current during discharging of the battery.
10. The battery monitoring device according to any one of claims 1 to 9, wherein
the integrated circuit further includes a second temperature measurement unit that measures a temperature of the
resistor, and
the first calculation unit calculates the AC impedance by further using the temperature of the resistor that was
measured.
11. The battery monitoring device according to any one of claims 1 to 10, wherein
the integrated circuit further includes a temperature control unit that controls a temperature of the resistor to be
constant.
12. The battery monitoring device according to any one of claims 1 to 11, wherein
the first calculation unit calculates the AC impedance during charging of the battery.
13. The battery monitoring device according to any one of claims 1 to 12, wherein
the first calculation unit calculates the AC impedance during discharging of the battery.
14. The battery monitoring device according to any one of claims 1 to 13, wherein
the first calculation unit calculates the AC impedance during a stoppage of charging and discharging of the battery.
15. The battery monitoring device according to any one of claims 1 to 14, wherein
the battery is one of a plurality of batteries included in a battery pack.
16. An integrated circuit, comprising:

a signal application unit that applies a control signal to a control terminal of a transistor for passing a current from a battery to a resistor disposed in a path different from a path of current flowing from the battery to a load;
a current measurement unit that measures a first current flowing through the resistor;
a voltage measurement unit that measures a first voltage of the battery; and
a first calculation unit that calculates an AC impedance of the battery based on the first current measured and the first voltage measured.

17. The integrated circuit according to claim 16, wherein
the battery is one of a plurality of batteries included in a battery pack, and
the voltage measurement unit includes a plurality of analog-to-digital (AD) converters each for measuring the voltage of a different one of the plurality of batteries,
the integrated circuit further comprising:
a reference bias generation unit that supplies a common reference voltage to the plurality of AD converters.

18. The integrated circuit according to claim 16 or claim 17, wherein
the battery is one of a plurality of batteries included in a battery pack, and
the voltage measurement unit includes a plurality of analog-to-digital (AD) converters each for measuring the voltage of a different one of the plurality of batteries,
the integrated circuit further comprising:
a timing signal generation unit that supplies each of the plurality of AD converters with a timing signal for synchronizing measurement timings of the plurality of AD converters.

19. A battery monitoring system, comprising:

a plurality of battery monitoring devices each of which is the battery monitoring device according to any one of claims 1 to 15; and
an integrated control unit that obtains the AC impedance from each of the plurality of battery monitoring devices.

20. A battery monitoring system, comprising:

the battery monitoring device according to any one of claims 1 to 15; and
a server device disposed at a location away from the battery monitoring device, wherein
the server device obtains the AC impedance from the battery monitoring device.

FIG. 1

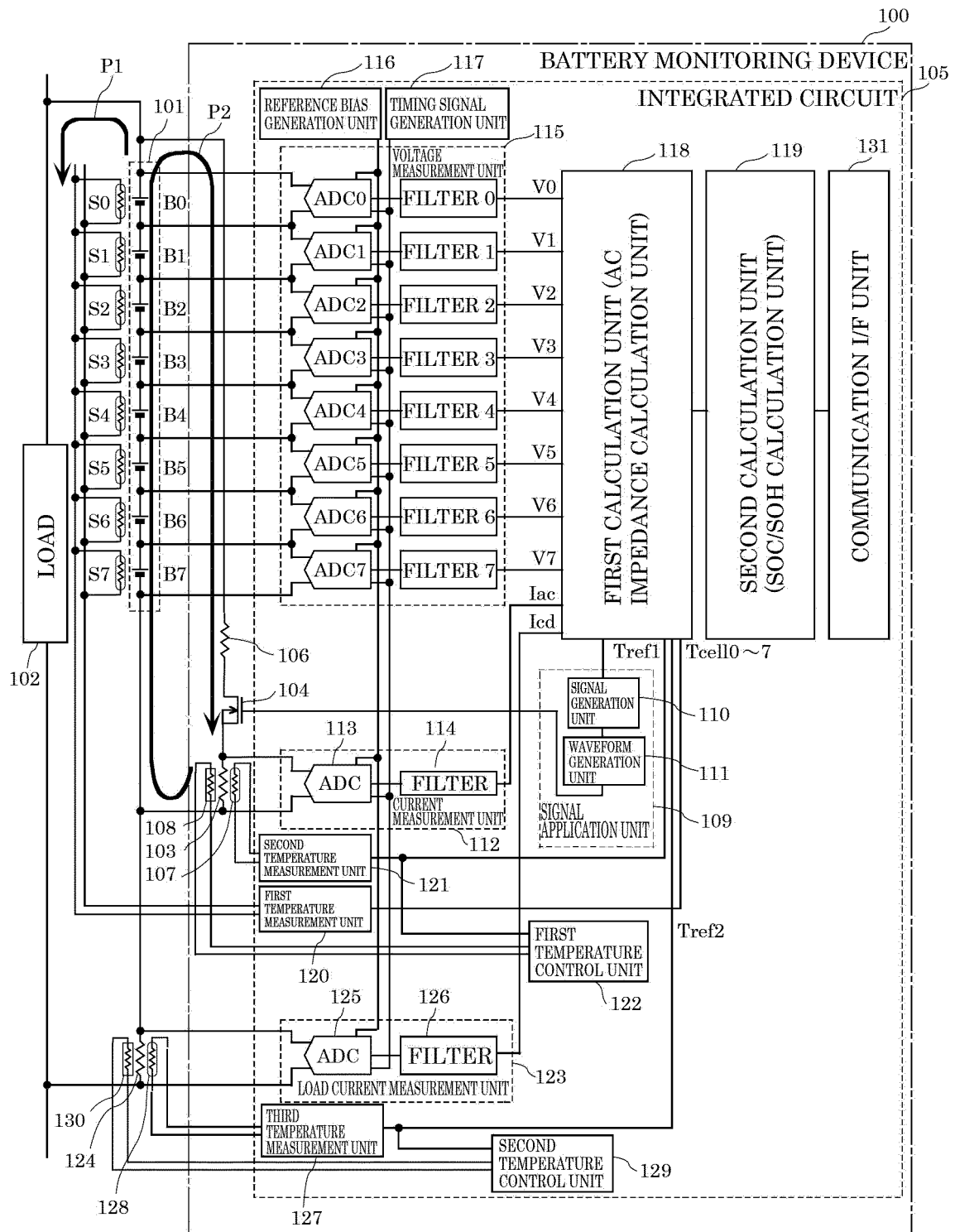


FIG. 2

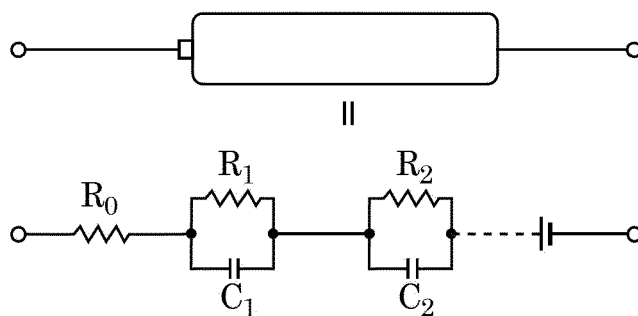


FIG. 3

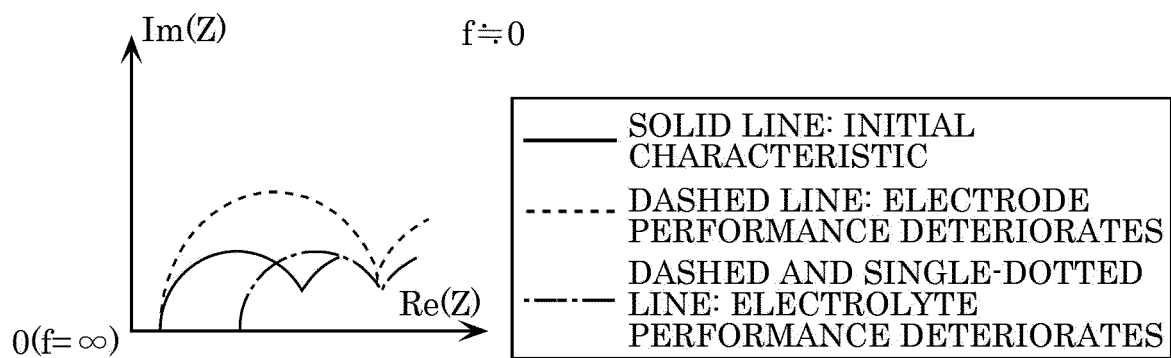


FIG. 4

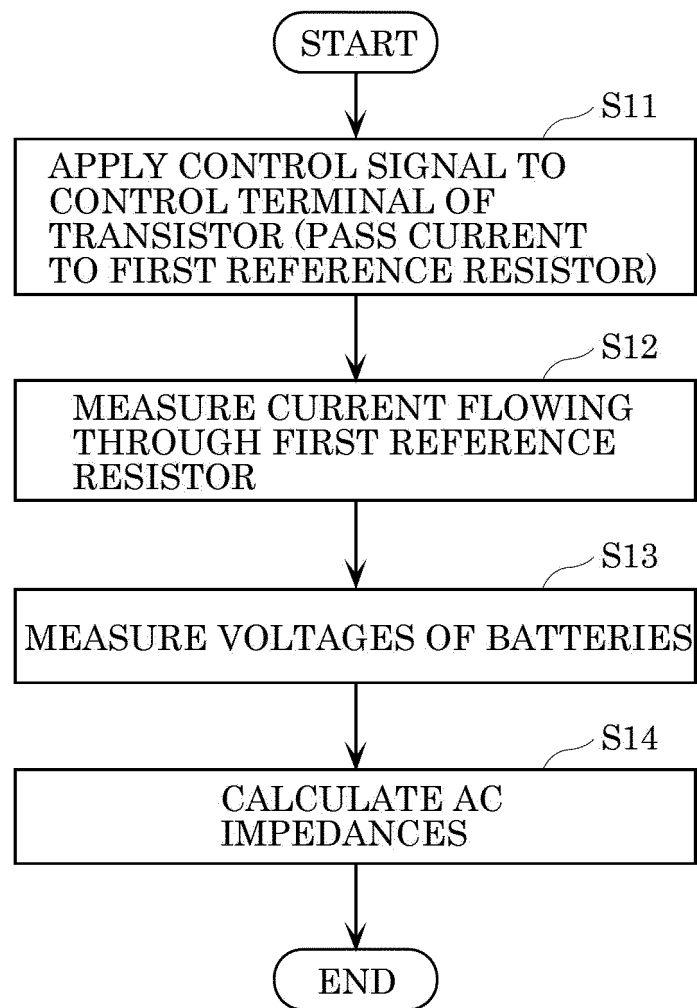


FIG. 5

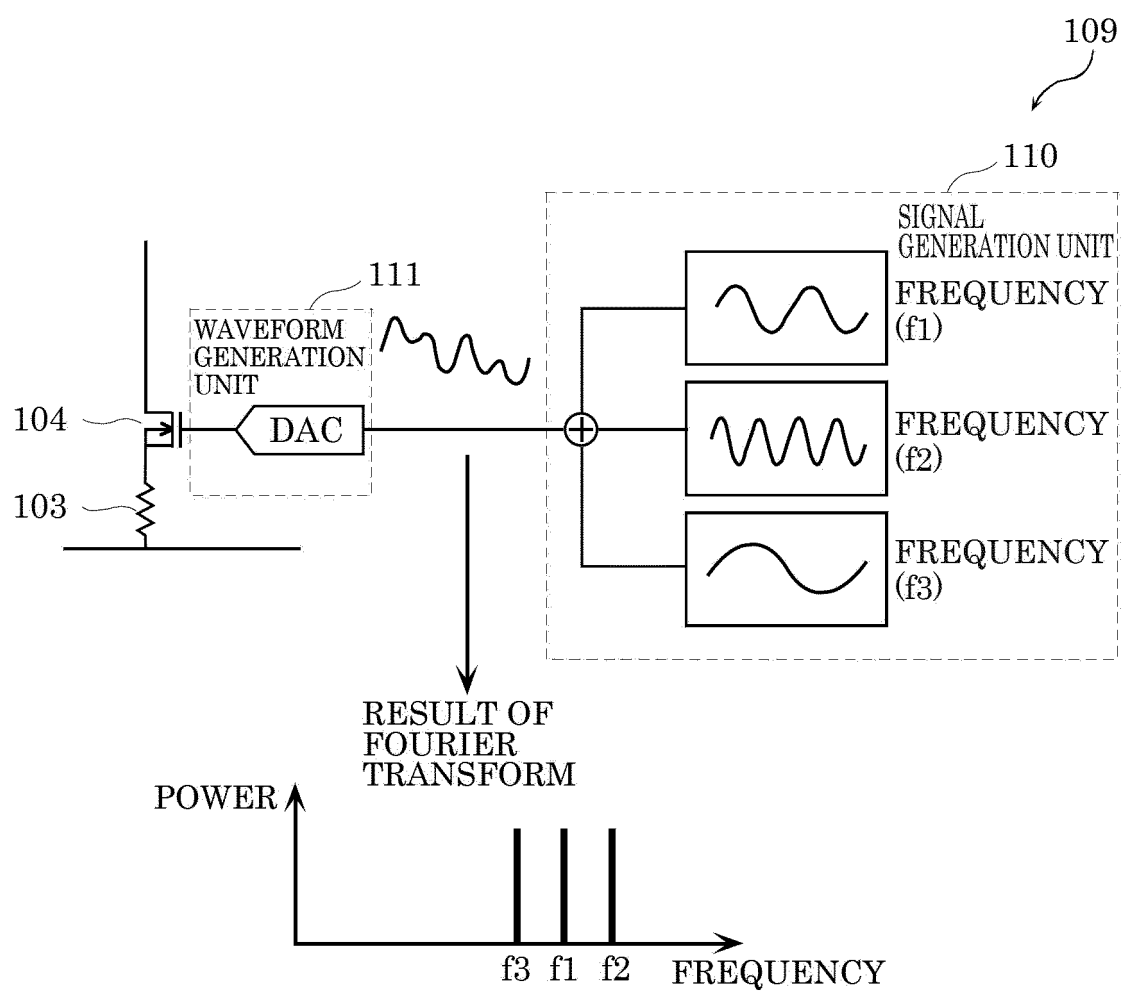


FIG. 6

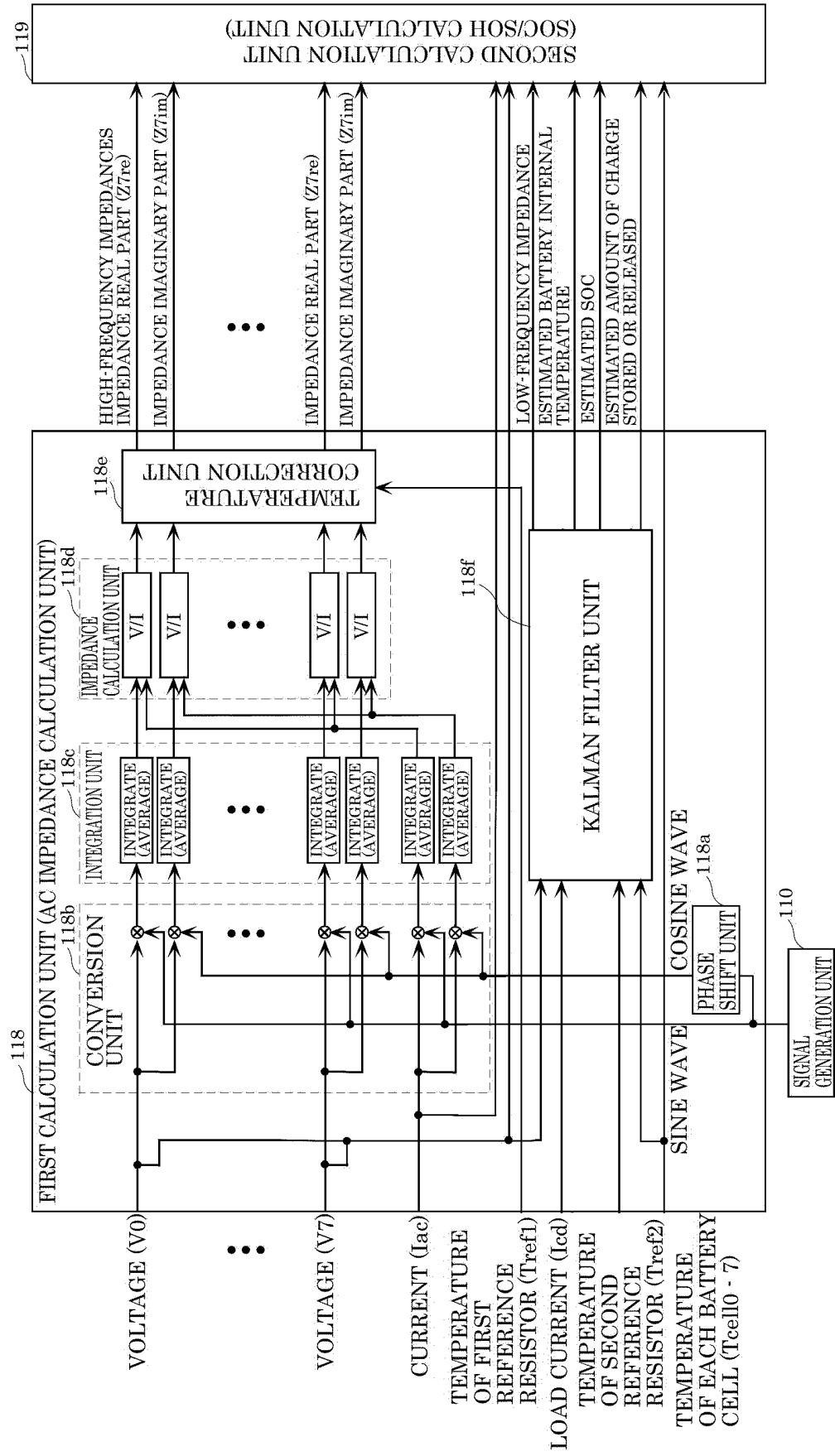


FIG. 7

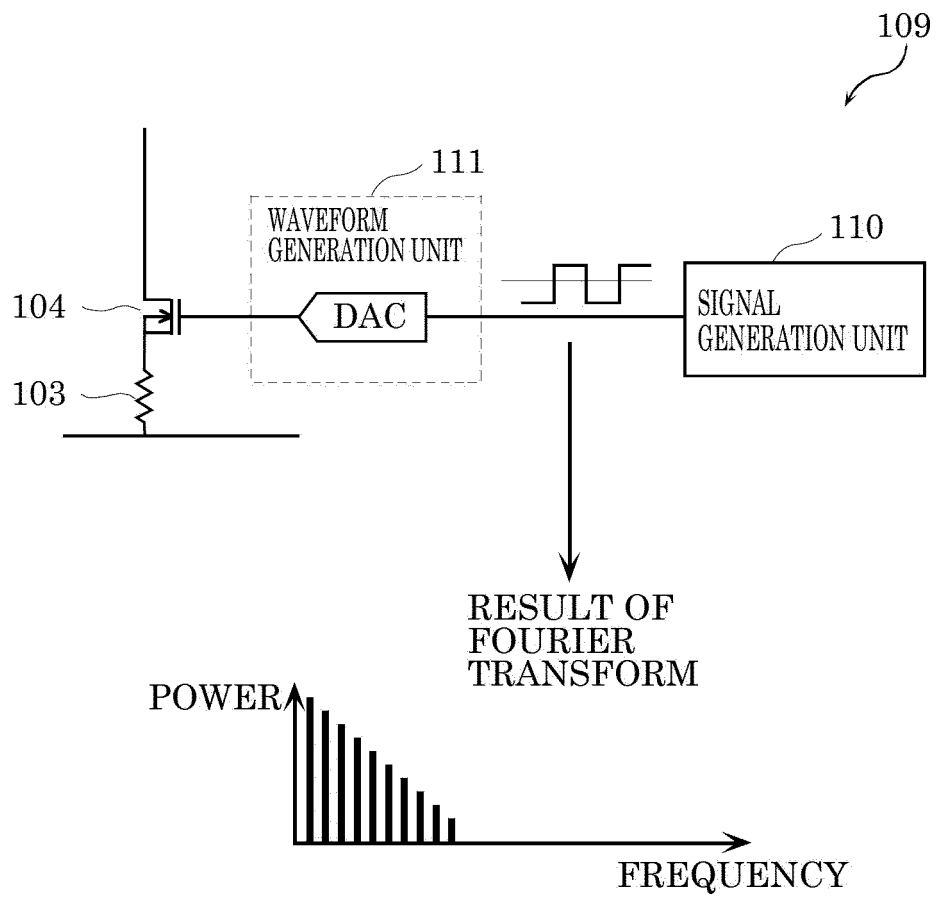


FIG. 8

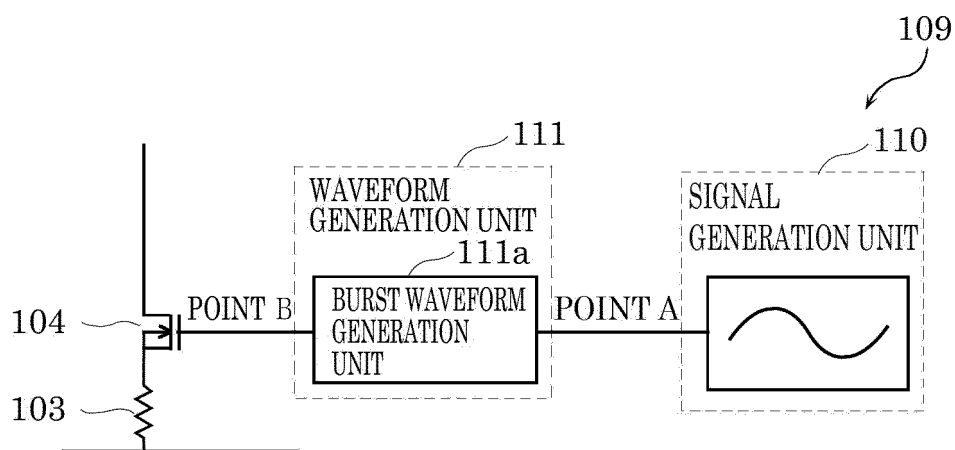


FIG. 9

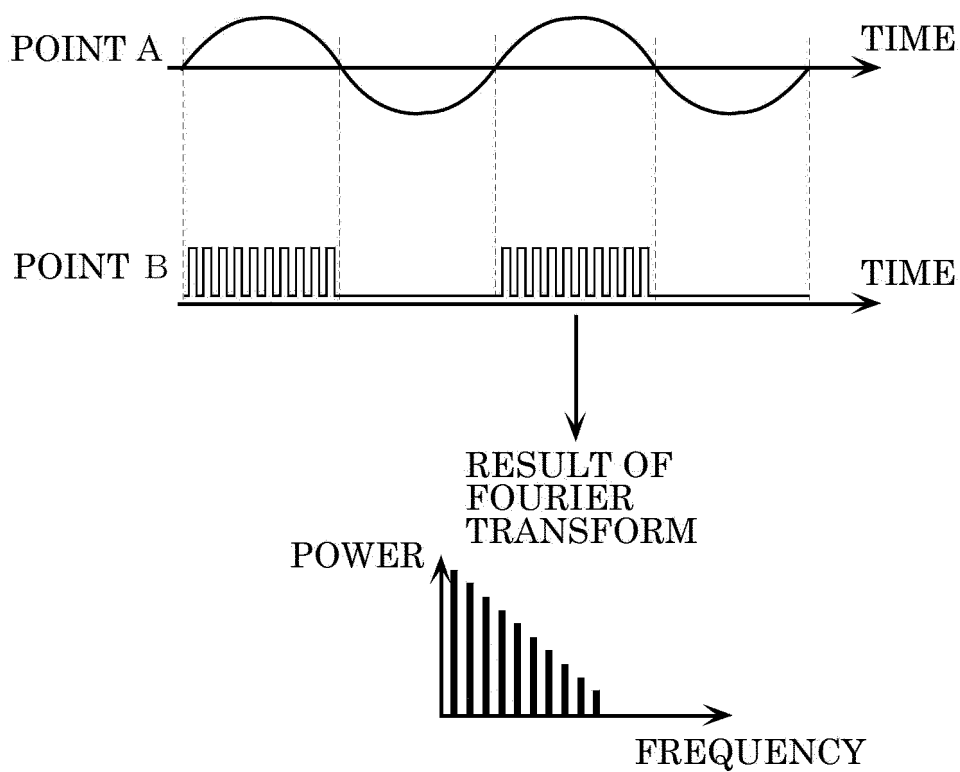


FIG. 10

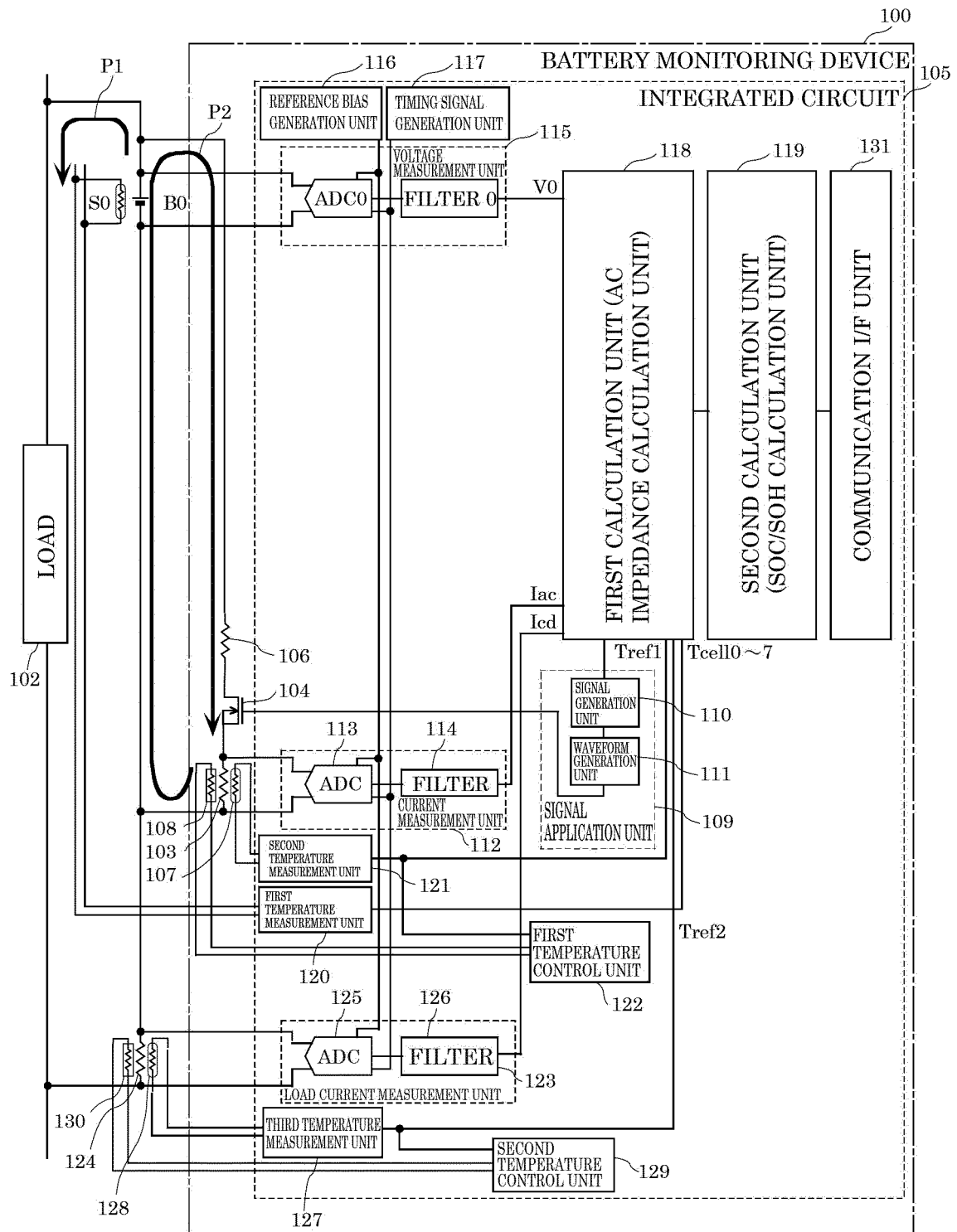


FIG. 11

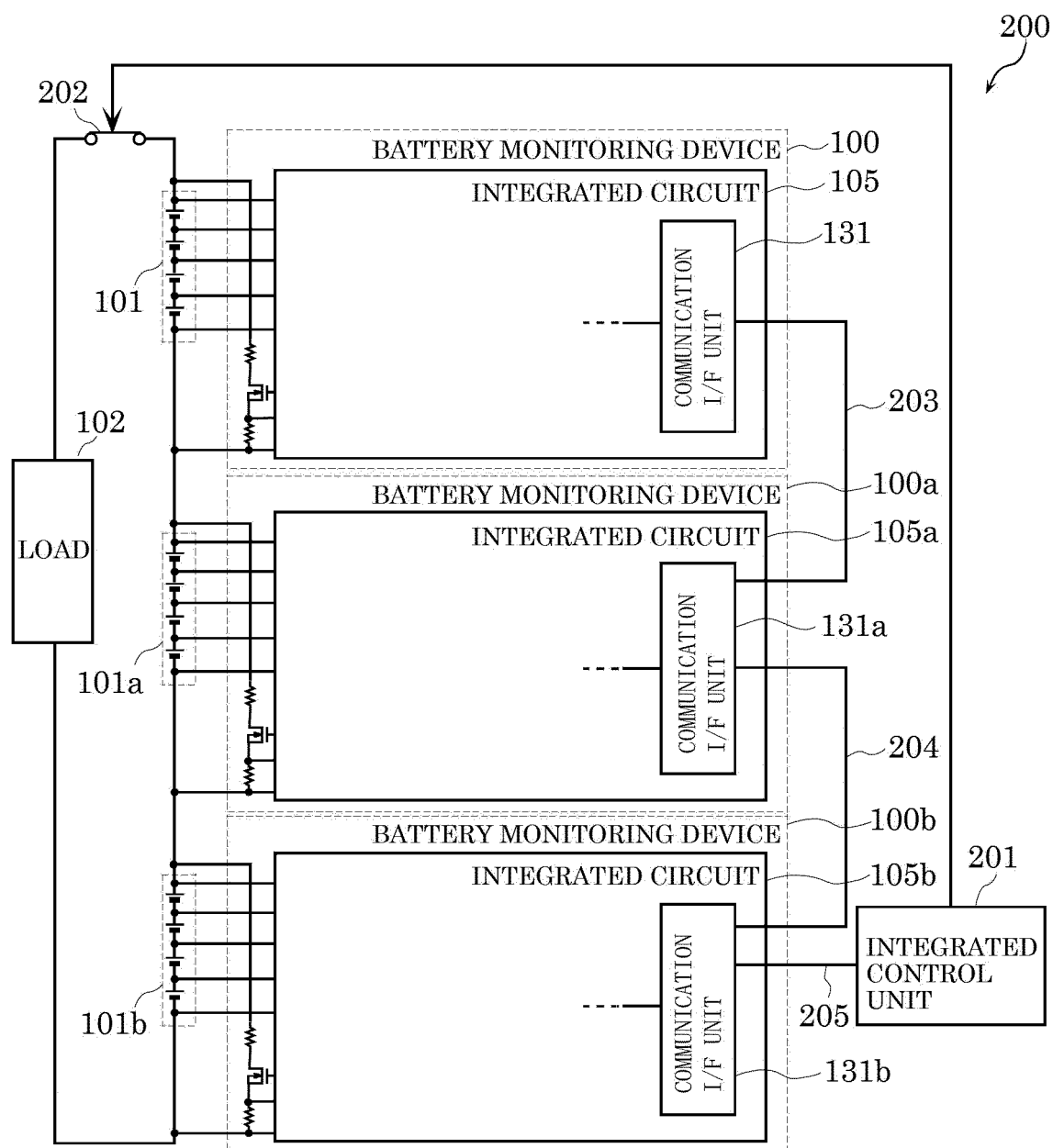
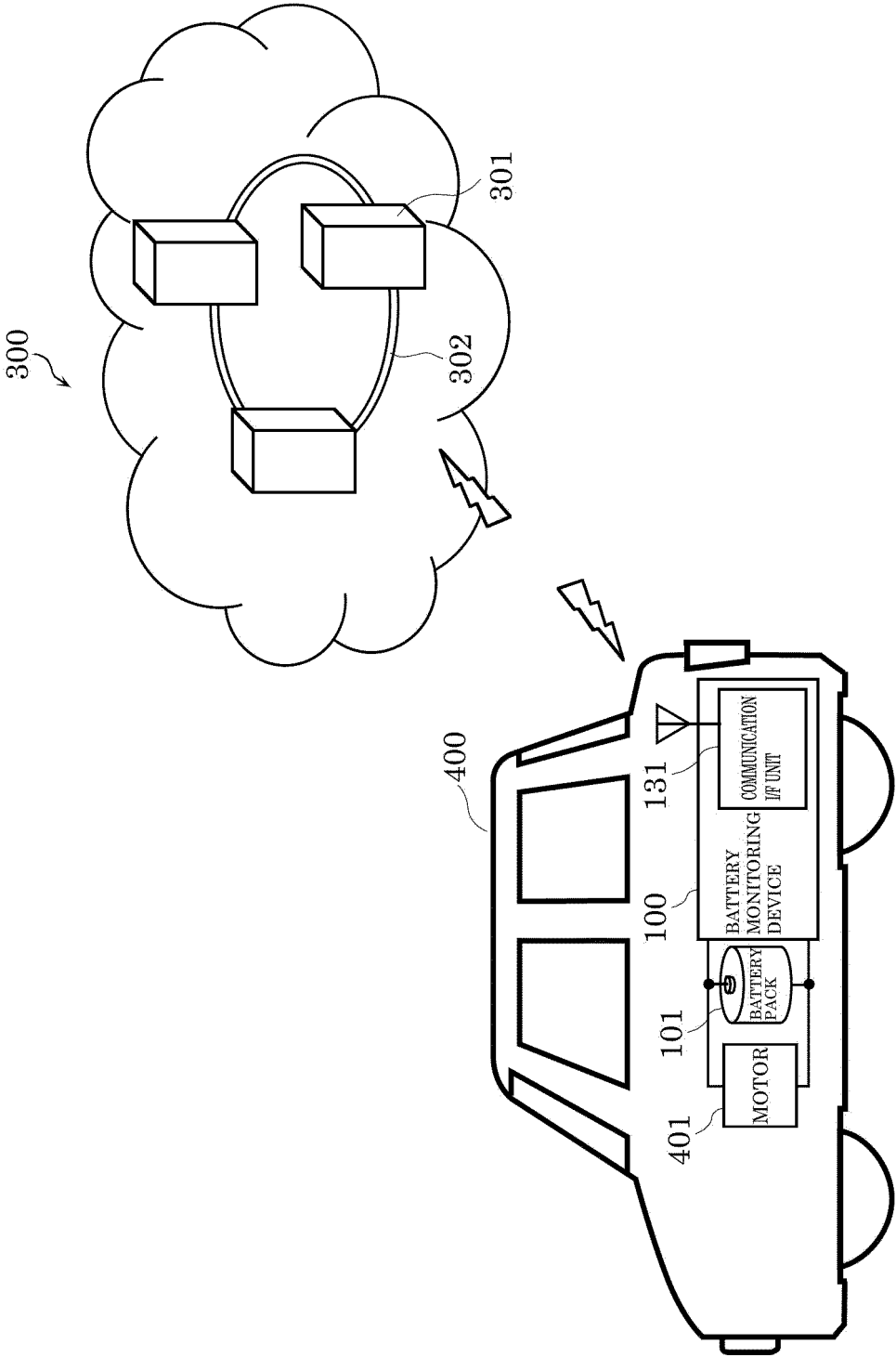


FIG. 12



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP2019/020801

A. CLASSIFICATION OF SUBJECT MATTER

Int. Cl. G01R31/389 (2019.01) i, H01M10/48 (2006.01) i, H02J7/00 (2006.01) i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int. Cl. G01R31/389, H01M10/48, H02J7/00

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Published examined utility model applications of Japan 1922-1996

Published unexamined utility model applications of Japan 1971-2019

Registered utility model specifications of Japan 1996-2019

Published registered utility model applications of Japan 1994-2019

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP 2017-78658 A (DENSO CORP.) 27 April 2017, entire text, all drawings & US 2017/0117596 A1 & DE 102016119893 A & CN 106611885 A	1-20
A	JP 2013-64697 A (NEC CORP.) 11 April 2013, entire text, all drawings (Family: none)	1-20
A	JP 2013-54003 A (YOKOGAWA ELECTRIC CORP.) 21 March 2013, entire text, all drawings (Family: none)	1-20
A	JP 2003-90869 A (YOKOGAWA ELECTRIC CORP.) 28 March 2003, entire text, all drawings (Family: none)	1-20
A	JP 2015-94726 A (UNIVERSITY TOKAI) 18 May 2015, entire text, all drawings (Family: none)	1-20



Further documents are listed in the continuation of Box C.



See patent family annex.

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Date of the actual completion of the international search
19.06.2019Date of mailing of the international search report
02.07.2019Name and mailing address of the ISA/
Japan Patent Office
3-4-3, Kasumigaseki, Chiyoda-ku,
Tokyo 100-8915, Japan

Authorized officer

Telephone No.

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Patent documents cited in the description

- JP 5403437 B [0003]