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(54) **COMMUNICATION GATEWAY MODULE**

(57) The invention relates to a communication gateway module (300) having: a wireless interface (301), a wired bus interface (302) comprising wired bus terminals (302a, 302b), e.g. a DALI bus interface, a mains supply interface (303) comprising mains voltage terminals (303a, 303b) and a control circuit (304). The control circuit is configured to determine whether a mains voltage is

supplied to the mains supply terminals (303a, 303b) and to power the gateway module (300) in the positive case, and to determine whether a bus with non-zero quiescent state is connected to the wired bus terminals (302a, 302b), and to power the gateway module (300) off the wired bus terminals (302a, 302b) in case no mains voltage is supplied at mains voltage terminals (303a, 303b).

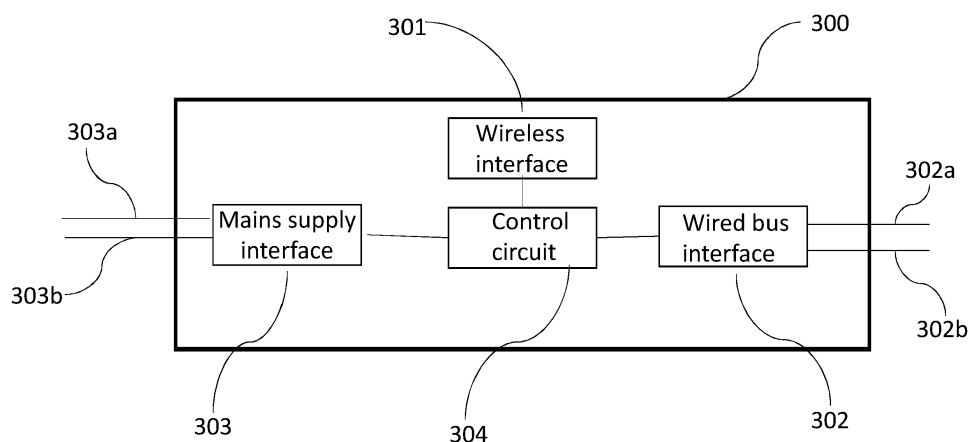


Fig. 3

Description

TECHNICAL FIELD OF THE INVENTION

5 **[0001]** The invention relates, in general, to LED lighting wireless control modules. In particular, the invention relates to a communication gateway module for translating between a wired (e.g.) DALI bus communication and a (non-DALI) wireless communication.

BACKGROUND OF THE INVENTION

10 **[0002]** LED drivers, which comprise a control interface, allow their light output to be dynamically controlled. For example, the DALI control interface is one of the most common control interfaces used in LED lighting. In particular, a DALI interface allows a digital signal to be sent over two wires from a controller to a LED driver (see Fig. 1(a)).

[0003] The DALI control interface can be extended to include wireless devices as well. In this case, the two wires interface can be replaced by a wireless intermediate gateway, as shown in Fig. 1(b).

15 **[0004]** In Fig. 1(a), only the DALI wires are represented. However, most of the devices in the market are powered either via the DALI wires themselves or they possess their own power supply, for example from a mains converter (see Fig. 2).

[0005] Devices with internal power supply may or may not have integrated DALI power supply. Devices with integrated DALI power supply provide energy to the DALI bus exclusively or in conjunction with another DALI power supply. The only restriction imposed by DALI in terms of paralleling DALI power supplies is that the total current circulating on the DALI bus cannot exceed the bus limit or 250 mA and the peak transient current of 450 mA. So, multiple power supplies can provide energy to the same bus.

20 **[0006]** However, inherently, these devices, in particular the communication gateway modules, when designed to be supplied via DALI or via an own power supply, are limited in their application.

25 **[0007]** Thus, it is an objective to provide for an improved power supply for a wired bus/wireless communication gateway module.

SUMMARY OF THE INVENTION

30 **[0008]** The object of the present invention is achieved by the solution provided in the enclosed independent claims. Advantageous implementations of the present invention are further defined in the dependent claims.

[0009] According to a first aspect, the invention, relates to a communication gateway module having: a wireless interface, a wired bus interface comprising wired bus terminals, e.g. a DALI bus interface, a mains supply interface comprising mains voltage terminals; and a control circuit. The control circuit is configured to determine whether a mains voltage is supplied to the mains supply terminals and to power the gateway module in the positive case, and to determine whether a bus with non-zero quiescent state, such as e.g. a DALI bus, is connected to the wired bus terminals, and to power the gateway module off the wired bus terminals in case no mains voltage is supplied at mains voltage terminals.

35 **[0010]** This provides the advantage that the gateway module can be powered both by the mains supply or the wired bus interface, e.g. a DALI power supply.

[0011] In an embodiment, the communication gateway module further comprises a bus voltage supply unit for supplying the bus at the wired bus terminals with electrical power off the mains voltage terminals.

[0012] This provides the advantage that power for supplying the bus can be taken from the mains voltage terminals if necessary.

40 **[0013]** In an embodiment, the communication gateway module comprises a first current limiting circuit configured to limit any current drawn from the wired bus terminals.

[0014] In an embodiment, the communication gateway module comprises a second current limiting circuitry configured to limit any current supplied to the wired bus terminals.

[0015] In an embodiment, the communication gateway module comprises a circuitry for commutating the polarity of the voltage of the electrical power supplied to the bus at the wired bus terminals.

50 **[0016]** In an embodiment, the communication gateway module comprises an inverting stage configured to operate as a rectifier when supply is drawn from the bus terminals.

[0017] According to a second aspect, the invention relates to an LED lighting system. The LED lighting system comprises a communication gateway module of the first aspect or any of its embodiments, and an LED driver.

55 **[0018]** According to a third aspect, the invention relates to an LED luminaire. The luminaire comprises a communication gateway module of the first aspect or any of its embodiments; an LED driver; and a light source comprising one or more LEDs or OLEDs, the light source being operable by the LED driver.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019] The invention will be explained in the followings together with the figures.

- 5 Fig. 1 shows a DALI interface according to prior art;
- Fig. 2 shows a DALI interface according to prior art;
- Fig. 3 shows a communication gateway module according to an embodiment; and
- 10 Fig. 4 shows a communication gateway module according to an embodiment;
- Fig. 5 shows a flow chart of a communication gateway module according to an embodiment;
- 15 Fig. 6 shows a first current limiting circuit and a second current limiting circuit of a communication gateway module according to an embodiment; and
- Fig. 7 shows a first current limiting circuit, a second current limiting circuit and an inverter/rectifying circuit of a communication gateway module according to an embodiment.
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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0020] Aspects of the present invention are described herein in the context of a communication gateway module.

[0021] The present invention is described more fully hereinafter with reference to the accompanying drawings, in which various aspects of the present invention are shown. This invention however may be embodied in many different forms and should not be construed as limited to the various aspects of the present invention presented through this disclosure. Rather, these aspects are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the present invention to those skilled in the art. The various aspects of the present invention illustrated in the drawings may not be drawn to scale. Rather, the dimensions of the various features may be expanded or reduced for clarity. In addition, some of the drawings may be simplified for clarity. Thus, the drawings may not depict all of the components of a given apparatus.

[0022] Various aspects of a communication gateway module will be presented. However, as those skilled in the art will readily appreciate, these aspects may be extended to aspects of communication gateway modules without departing from the invention.

35 **[0023]** The term "LED luminaire" shall mean a luminaire with a light source comprising one or more LEDs or OLEDs. LEDs are well-known in the art, and therefore, will only briefly be discussed to provide a complete description of the invention.

[0024] It is further understood that the aspect of the present invention might contain integrated circuits that are readily manufacturable using conventional semiconductor technologies, such as complementary metal-oxide semiconductor technology, short "CMOS". In addition, the aspects of the present invention may be implemented with other manufacturing processes for making optical as well as electrical devices. Reference will now be made in detail to implementations of the exemplary aspects as illustrated in the accompanying drawings. The same reference signs will be used throughout the drawings and the following detailed descriptions to refer to the same or like parts.

[0025] Now referring to Fig. 3, a communication gateway module 300 is shown according to an embodiment.

45 **[0026]** The communication gateway module 300 comprises a wireless interface 301 (antenna not shown), a wired bus interface 302 comprising wired bus terminals 302a, 302b, e.g. a DALI bus interface, a mains supply interface 303 comprising mains voltage terminals 303a, 303b; and a control circuit 304.

[0027] The control circuit 304 controls the power supply of the communication gateway module 300 and the communication over the interfaces 301 and 302.

50 **[0028]** The control circuit is connected to the interfaces 301, 302 and the mains supply interface 303.

[0029] Further, the communication gateway module 300 is configured to determine whether a mains voltage is supplied to the mains supply terminals 303a, 303b and to power the gateway module 300 in the positive case.

[0030] Still further, the communication gateway module 300 is configured to determine whether a bus with non-zero quiescent state is connected to the wired bus terminals 302a, 302b, and to power the gateway module 300 off the wired bus terminals 302a, 302b in case no mains voltage is supplied at mains voltage terminals 303a, 303b.

[0031] This provides the advantage that the communication gateway module 300 is configured to select its power source regardless of the sources available, DALI, internal power supply or both.

[0032] Moreover, if a DALI bus is connected, and the DALI bus has a bus supply by a dedicated bus supply unit or

an integrated DALI power supply, for example in an operating device for lighting means, the communication gateway module 300 can draw limited power from the DALI bus (limited meaning within the limitations of the DALI specification).

[0033] Further, the communication gateway module 300 is provided with mains supply terminals 303a and 303b. The control circuit 304 in the communication gateway module 300 can determine whether mains and/or DALI power supply is available at the respective terminals. In case mains power supply is available, the priority is on the mains power supply.

[0034] Moreover, the communication gateway module 300 is configured to select the supply source according to any one of the combinations shown in the table below and can possess an integrated DALI Power Supply (IDPS), see circuit 403 on Figure 4, as well.

AC Mains	Powered DALI Bus	Supply options	IDPS
Present	Present	Via mains	Optional ON/OFF
Present	Absent	Via mains	ON
Absent	Present	Via DALI	OFF

[0035] In this manner, advantageously, all possible system scenarios are covered by a single configurable device or communication gateway module 300.

[0036] Preferably, the communication gateway module 300 is provided with a circuitry to power the DALI bus, provided that it is itself provided with mains supply voltage.

[0037] Fig. 4 shows a communication gateway module 300 according to an embodiment.

[0038] In Fig. 4, an AC/DC converter 401 is arranged for being supplied with AC mains voltage from the terminals 303a and 303b and for outputting a DC voltage. The DV voltage is supplied e.g. to a low voltage power supply unit 402. Moreover, it provides energy to the control circuit 304 and its IDPS designated.

[0039] The low voltage power supply (LVPS) 402 is configured to power the control circuit 304, off the DC voltage supplied by the AC/DC converter (401). The control circuit 304 may comprise a microcontroller and RF transceiver, both of which can be integrated as system-on-chip (SoC).

[0040] A first current limiting circuit 403 is connected directly to the output of the AC/DC converter 401 and represents the IDPS according to this embodiment. It can be configured to limit the amount of current provided to the DALI bus according to the DALI specification.

[0041] A second current limiting circuitry 404 limits the current in opposite direction to the first one, limiting the amount of current drawn from the DALI bus, when the DALI bus is used as main power source. Both circuits can be controllable by the control circuit 304 such that the control circuit can e.g. also disable them.

[0042] Both current limiting circuitries can be bypassed by a semiconductor (e.g. diode) in anti-parallel position, to allow the current to flow in both directions.

[0043] The control circuit 304 has a power supply and a RF control responsible for the DALI communications, RF communications and the control of the current limiting circuits 403, 404.

[0044] A DALI TX and DALI RX interface 302 is configured to receive and transmit according to DALI specifications. It is controlled by the control unit 304.

[0045] Fig. 5 shows a flow chart 500 of an operation of the communication gateway module 300 according to an embodiment.

[0046] After the start 501 the IDPS is disabled in step 502. Afterwards, in step 504 it is checked if the mains voltage is present at the mains terminals. If yes, then in step 505 it is checked for an external power supply supplying the DALI bus with power. In particular, it is checked in step 508 if the DALI bus is high. If yes, then a configuration of the system or the communication gateway module 300 is checked in step 509. In particular, a user input can be used to determine whether the internal power supply IDPS of the communication gateway module 300 should or should not operate when the DALI bus is already powered. This ability provides flexibility to scale up the power of the communication gateway module 300 or to avoid exceeding the DALI system requirements. This input can be in many forms, such as configuration file, settings, button in interface, etc. Afterwards, it is checked if there is a multi-power supplied system in step 510. If yes, then the IDPS is enabled in step 507 and the DALI wireless gateway functions are resumed in step 506.

[0047] If the mains is not present in step 504, then the communication gateway module 300 is powered by DALI supply in step 503 and the DALI wireless gateway functions are resumed in step 506.

[0048] Moreover, if the DALI bus is not high in step 508, then the IDPS is enabled in step 507 and, afterwards, the DALI wireless gateway functions are resumed in step 506.

[0049] Furthermore, if in step 510 the system or communication gateway module 300 is not a multi-power supplied system, then the DALI wireless gateway functions are resumed in step 506.

[0050] Fig. 6 shows the first current limiting circuit 403 and the second current limiting circuit 404 of a communication

gateway module 300 according to an embodiment.

[0051] In this embodiment, attention is paid to the current limiting stages 403 and 404, wherein the ability to source or sink current from the DALI bus is implemented.

[0052] In this embodiment, the AC/DC converter 401 comprises a flyback converter for supplying a regulated DC voltage at a capacitor C1. The elements D7, R2, Q1, Q2, R1, D1, Q5 and VCTRL1 are part of DALI power supply and are used when the communication gateway module 300 is mains supplied and supplies DALI bus.

[0053] Moreover, the elements D6, R3, Q3, Q4, R5, D2, Q6, R4 and Q11 are part of the circuit used when the communication gateway module 300 is supplied by a DALI bus. The commands VCTRL1 and VCTRL2 are controller signals that are configured to enable and disable the current limiting circuits 403 and 404 depending on the mode of operation. In particular, the command VCTRL1 enables the DALI power supply when the communication gateway module 300 is mains supplied, while the command VCTRL2 disables the current limiting circuit 403 and 404 when the communication gateway module 300 is mains supplied.

[0054] When DALI bus is already supplied from a source outside the gateway module 300, the command VCTRL1 disables the DALI power supply, while, when DALI bus power is supplied, the current limiting circuit 404 is automatically enabled via R4 and Q6 and can only be disabled via the command VCTRL2.

[0055] Fig. 7 shows a first current limiting circuit 403, a second current limiting circuit 404 and an inverter/rectifying circuit 601 of a communication gateway module 300 according to an embodiment.

[0056] In this embodiment, the inverting stage 601 is added to the previous embodiment shown in Fig. 6.

[0057] In particular, the inverting/rectifying section 601 of the circuit allows any DALI polarity on the DALI bus by enabling Q7 and Q10 or Q8 and Q9 pairs. The DALI voltage polarity can be adjusted to match other power supplies on the bus, if existing. When supplied from the DALI bus, the diodes D4, D5, D8 and D9 rectify the DALI input voltage to allow any wire polarity connection.

[0058] Moreover, especially in case a plurality of power supplies are active on the DALI bus, they all have to supply power with the same polarity to the DALI bus. The inverter/rectifier circuitry 601 allows to switch the polarity of the voltage supplied to the DALI bus terminals 302a and 302b.

[0059] Not shown are an LED lighting system and an LED luminaire according to an embodiments.

[0060] The LED lighting system comprises a communication gateway module 300 according to an embodiment; and an LED driver.

[0061] The LED luminaire comprises a communication gateway module 300 according to an embodiment; an LED driver; and a light source comprising one or more LEDs or OLEDs. The light source is operable by the LED driver.

[0062] All features of all embodiments described, shown and/or claimed herein can be combined with each other.

[0063] While various embodiments of the present invention have been described above, it should be understood that they have been presented by way of example only and not limitation. Numerous changes to the disclosed embodiments can be made in accordance with the disclosure herein without departing from the spirit of scope of the invention. Thus, the breadth and scope of the present invention should not be limited by any of the above-described embodiments. Rather, the scope of the invention should be defined in accordance with the following claims and their equivalence.

[0064] Although the invention has been illustrated and described with respect to one or more implementations, equivalent alternations and modifications will occur to those skilled in the art upon the reading of the understanding of the specification and the annexed drawings. In addition, while a particular feature of the invention may have been disclosed with respect to only of the several implementations, such features may be combined with one or more other features of the other implementations as may be desired and advantage for any given or particular application.

Claims

1. A communication gateway module (300) having:

- a wireless interface (301),
- a wired bus interface (302) comprising wired bus terminals (302a, 302b), e.g. a DALI bus interface,
- a mains supply interface (303) comprising mains voltage terminals (303a, 303b); and
- a control circuit (304) configured to:

- determine whether a mains voltage is supplied to the mains supply terminals (303a, 303b) and to power the gateway module (300) in the positive case, and to

- determine whether a bus with non-zero quiescent state is connected to the wired bus terminals (302a, 302b), and to power the gateway module (300) off the wired bus terminals (302a, 302b) in case no mains voltage is supplied at mains voltage terminals (303a, 303b).

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2. The communication gateway module (300) of claim 1, further comprising a bus voltage supply unit for supplying the bus at the wired bus terminals (302a, 302b), with electrical power off the mains voltage terminals (303a, 303b).
3. The communication gateway module (300) of claim 1 or 2, comprising a first current limiting circuit (403) configured to limit any current drawn from the wired bus terminals (302a, 302b) .
4. The communication gateway module (300) of claim 2, further comprising a second current limiting circuitry (404) configured to limit any current supplied to the wired bus terminals (302a, 302b).
5. The communication gateway module (300) of any one of claims 2 or 4, further comprising an circuitry for commutating the polarity of the voltage of the electrical power supplied to the bus at the wired bus terminals (302a, 302b).
6. The communication gateway module (300) of any one of the preceding claims, further comprising an inverting stage (601) configured to operate as a rectifier when supply is drawn from the bus terminals (302a, 302b).
7. An LED lighting system, comprising
a communication gateway module (300) of any one of the claims 1 to 6; and
an LED driver.
8. An LED luminaire, comprising
a communication gateway module (300) of any one of the claims 1 to 6;
an LED driver; and
a light source comprising one or more LEDs or OLEDs, the light source being operable by the LED driver.

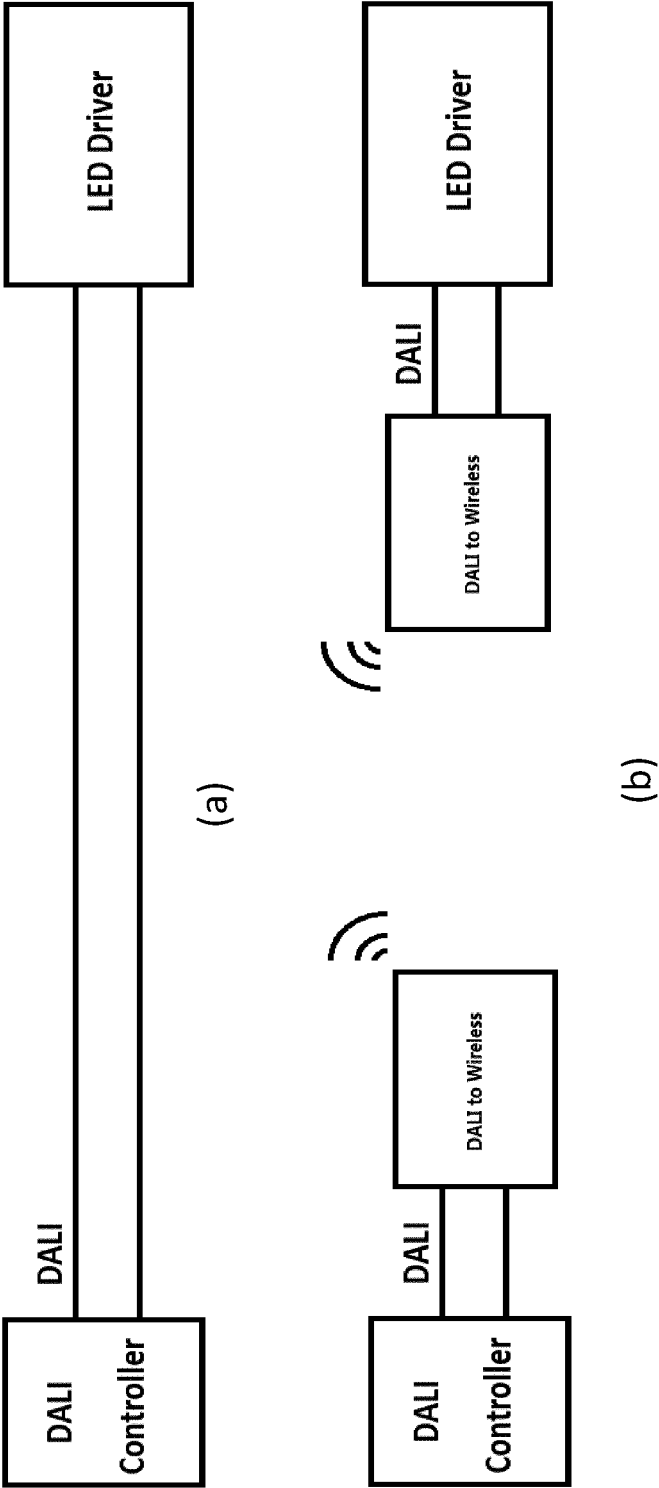


Fig. 1

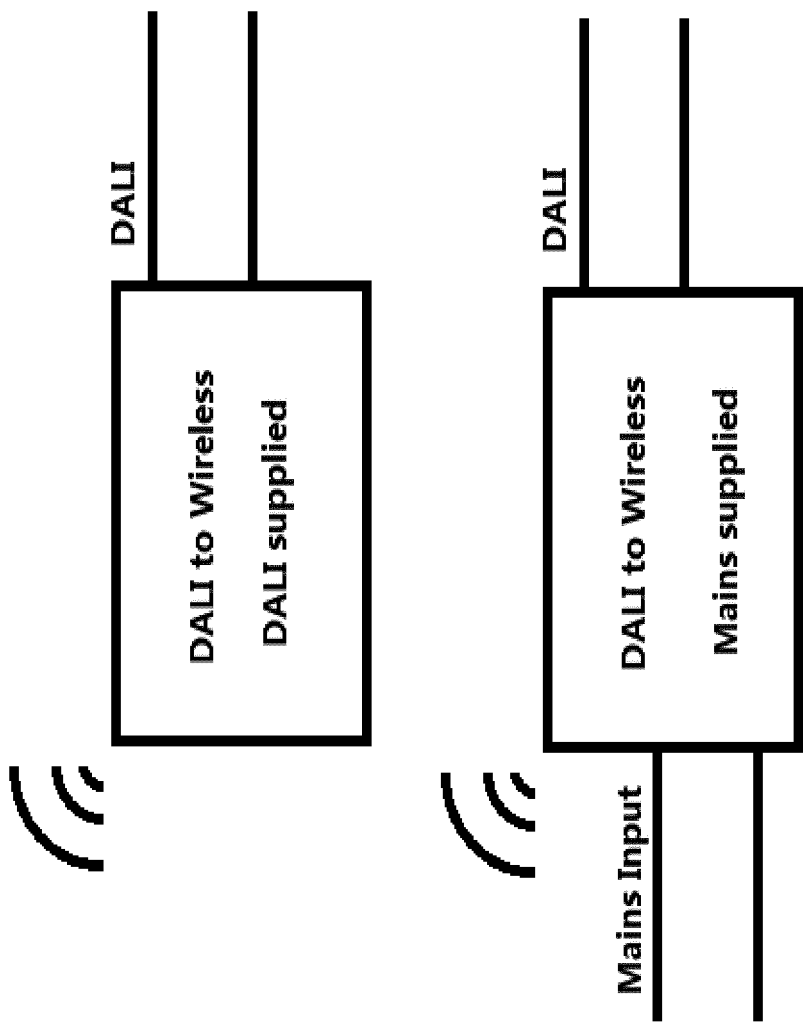


Fig. 2

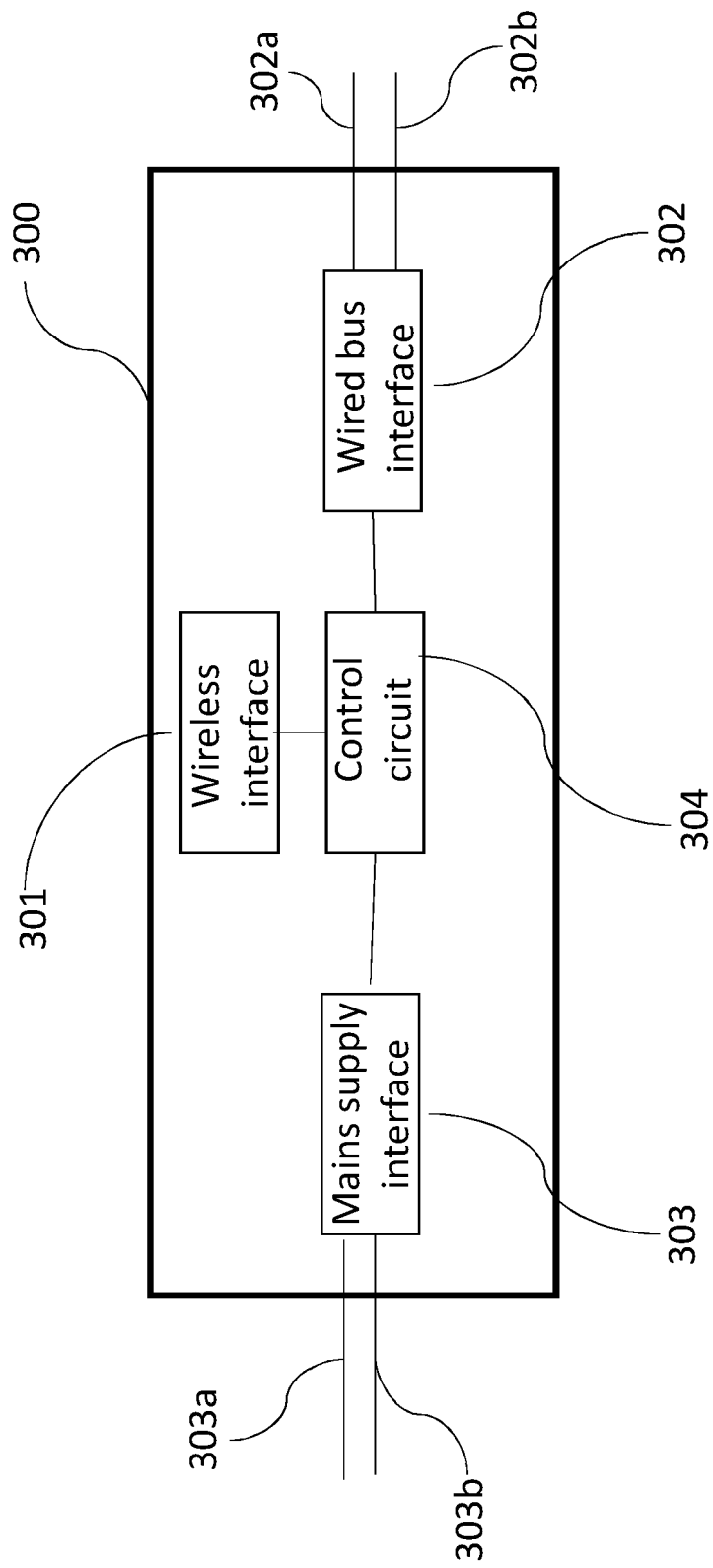


Fig. 3

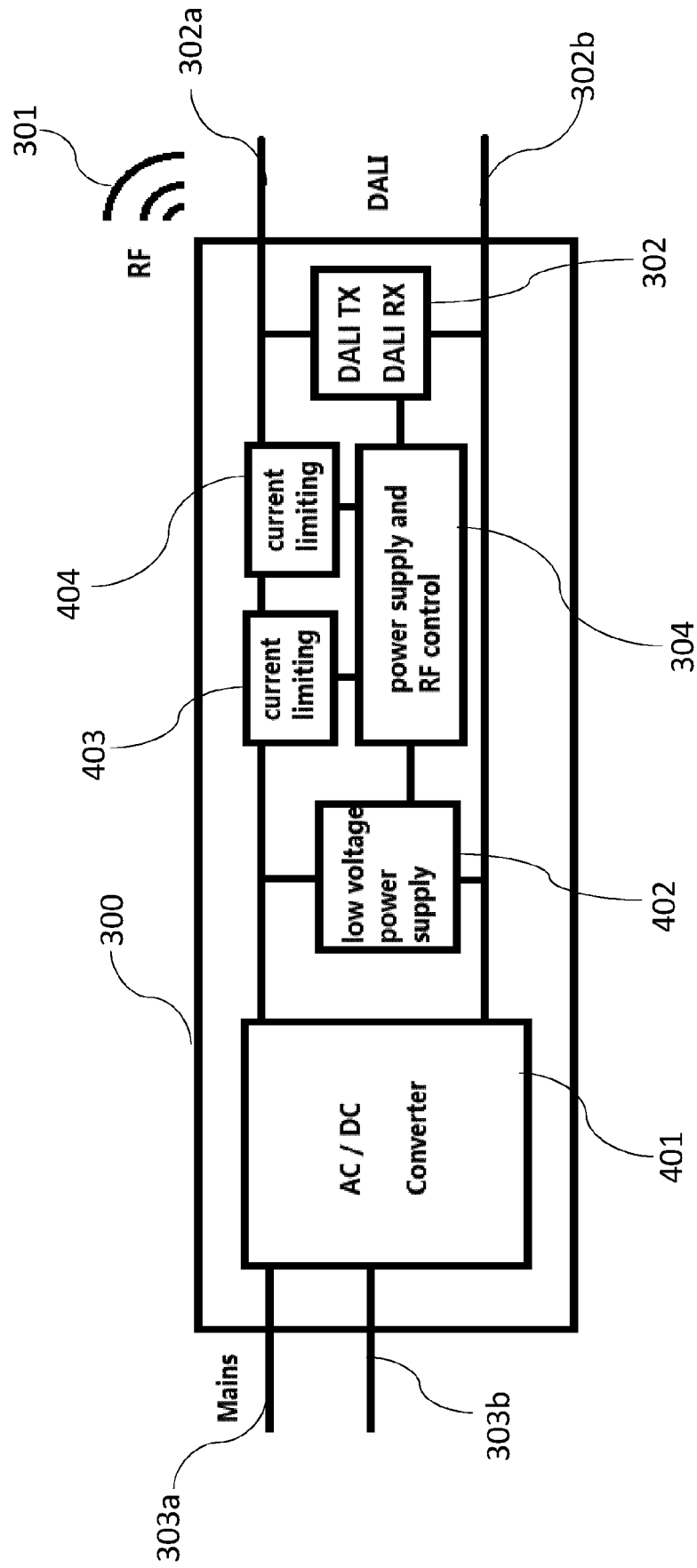


Fig. 4

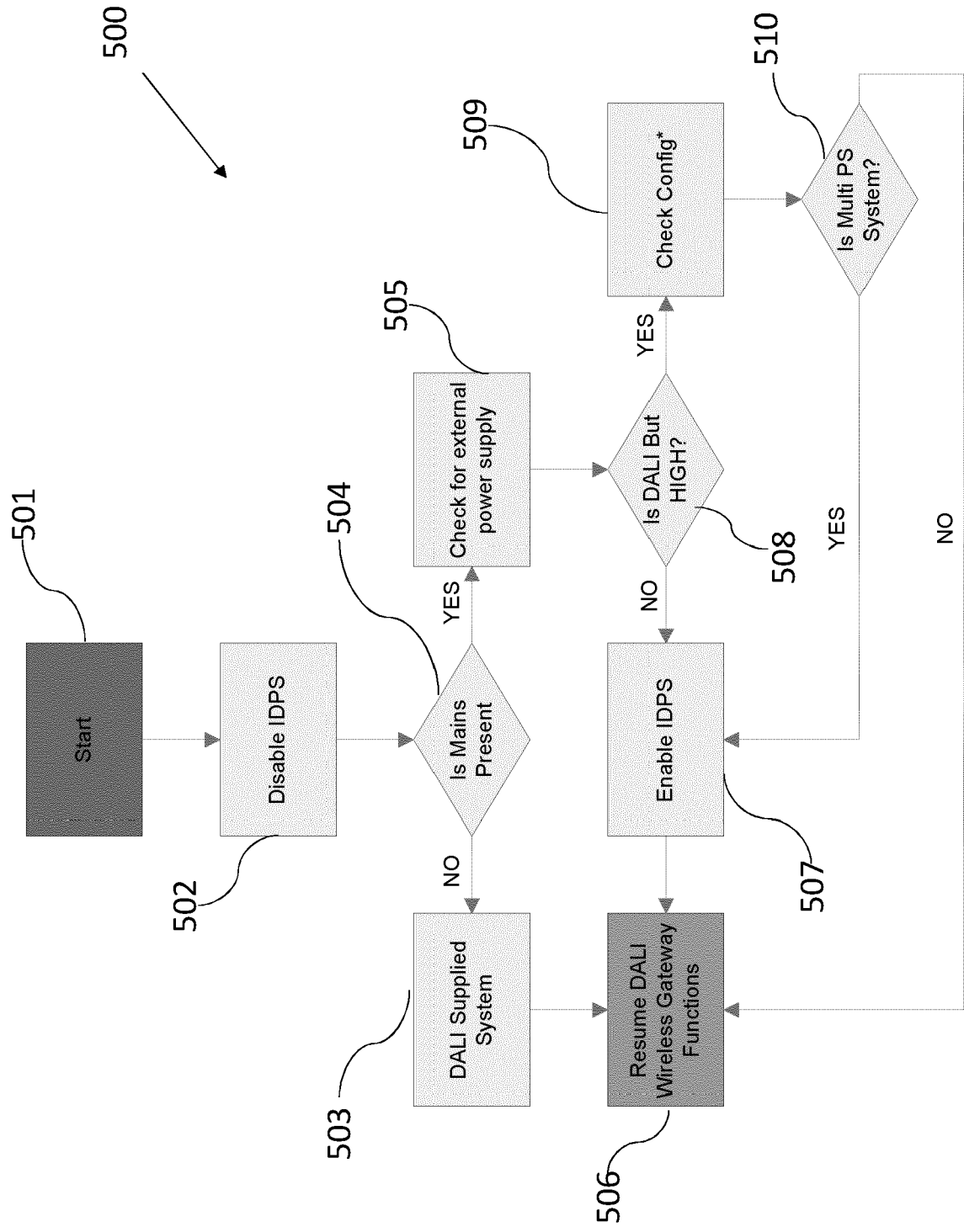


Fig. 5

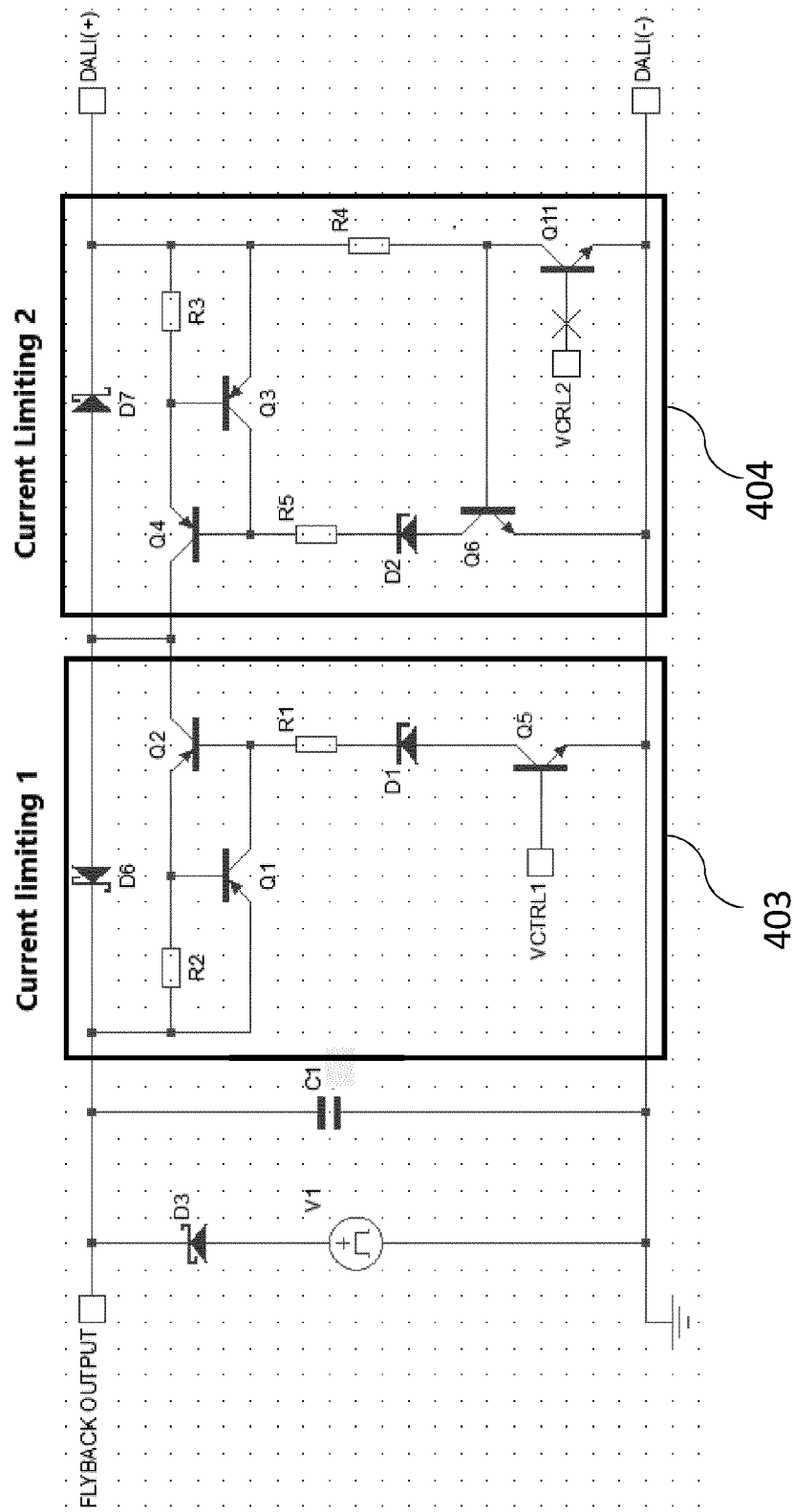


Fig. 6

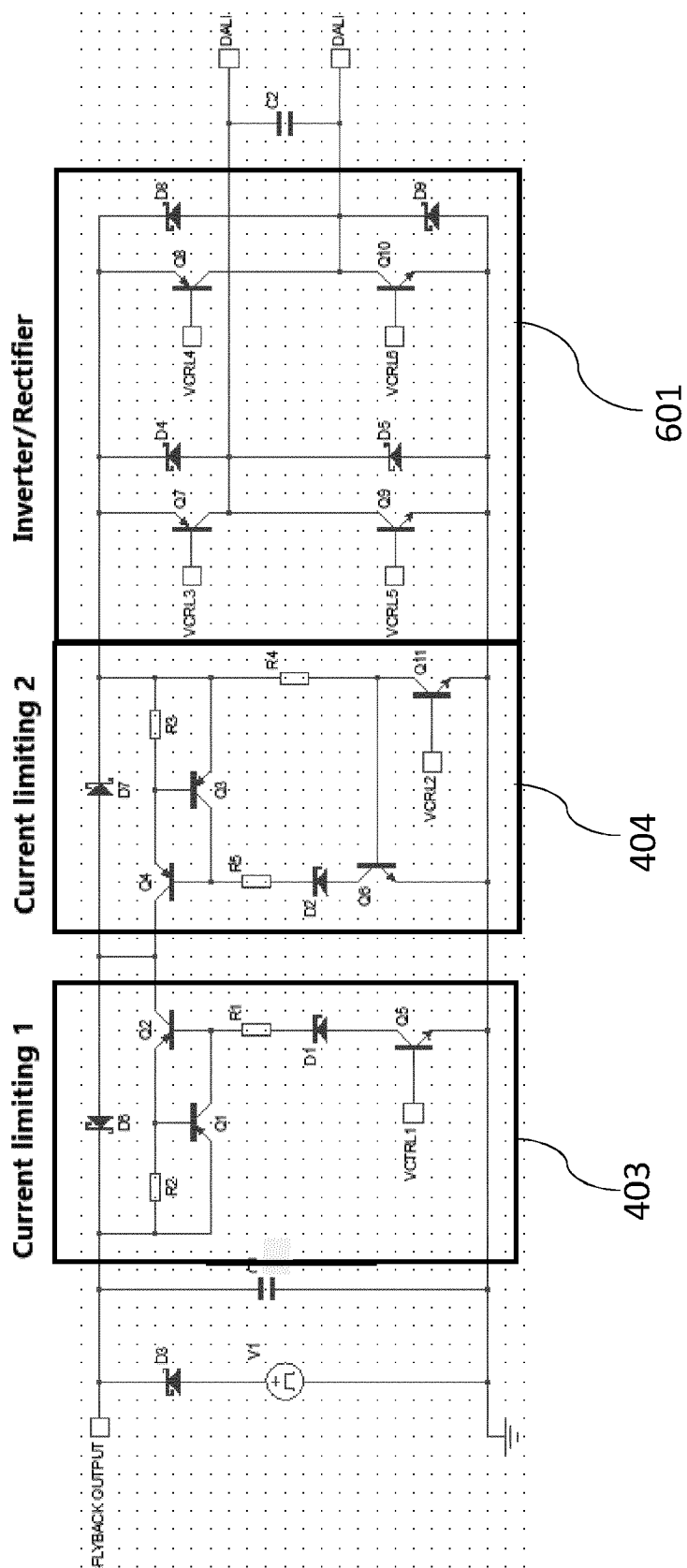


Fig. 7



EUROPEAN SEARCH REPORT

 Application Number
 EP 21 16 5124

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DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)
X	WO 2020/194355 A1 (BEGHELLI SPA [IT]) 1 October 2020 (2020-10-01)	1,3,7,8	INV.
A	* page 7, line 15 - page 9, line 7; figures 3-6 *	2,4-6	H05B47/18 H05B47/19
X	GB 2 554 987 A (LEVITON MANUFACTURING CO [US]) 18 April 2018 (2018-04-18)	1-4,7,8	
A	* paragraphs [0003], [0044] - [0052], [0064] - [0069]; figures 2,4 *	5,6	
A	US 2017/231065 A1 (HERTER JENS [DE] ET AL) 10 August 2017 (2017-08-10) * the whole document *	1-8	
			TECHNICAL FIELDS SEARCHED (IPC)
			H05B
The present search report has been drawn up for all claims			
Place of search Munich		Date of completion of the search 26 August 2021	Examiner Waters, Duncan
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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 EPO FORM 1503 03.82 (P04C01)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 21 16 5124

5 This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report.
The members are as contained in the European Patent Office EDP file on
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26-08-2021

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