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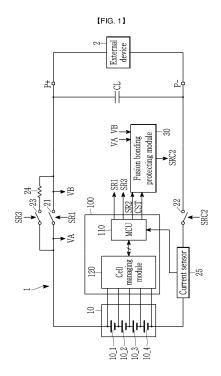
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(54) MELT BONDING PROTECTION MODULE AND BATTERY SYSTEM COMPRISING SAME

A fusion bonding protecting module for controlling a negative electrode contactor connected between a negative electrode of a battery pack and a second output end according to fusion and bonding of a positive electrode contactor connected between a positive electrode of the battery pack and a first output end, including: an SR latch including a set terminal for receiving a fusion bonding sensing signal according to whether the positive electrode contactor is fused and bonded and a reset terminal for receiving a comparison signal according to a voltage difference between a first end and a second end of the positive contactor; and an AND gate for generating a compensation control signal by ANDing a logic control signal that is an inverted output of the SR latch and a control signal for controlling the negative electrode contactor.



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Description

[Technical Field]

5 CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority to and the benefit of Korean Patent Application No. 10-2021-0119710 filed in the Korean Intellectual Property Office on September 08, 2021, the entire contents of which are incorporated herein by reference.

[0002] The present disclosure relates to a fusion bonding protecting module and a battery system including the same.

[Background Art]

[0003] When a negative electrode contactor connected to a negative electrode (-) of a battery is closed while a positive electrode contactor connected to a positive electrode (+) of the battery is fused and bonded, an inrush current is generated to the negative electrode contactor. The positive electrode and the negative electrode contactor may then both be fused and bonded.

[0004] When the positive electrode contactor is fused and bonded by using software installed in a battery management system, the negative electrode contactor may be inoperable. However, a fault clear operation may be performed or other software of the battery management system may be operated to close the negative electrode contactor.

[Disclosure]

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[Technical Problem]

[0005] The present invention has been made in an effort to provide a hardwired fusion bonding protecting module for improving a limit generated in a contactor protecting operation using software, and a battery system including the same.

[Technical Solution]

[0006] An embodiment of the present invention provides a fusion bonding protecting module for controlling a negative electrode contactor connected between a negative electrode of a battery pack and a second output end according to fusion and bonding of a positive electrode contactor connected between a positive electrode of the battery pack and a first output end, including: an SR latch including a set terminal for receiving a fusion bonding sensing signal according to whether the positive electrode contactor is fused and bonded and a reset terminal for receiving a comparison signal according to a voltage difference between a first end and a second end of the positive contactor; and an AND gate for generating a compensation control signal by ANDing a logic control signal that is an inverted output of the SR latch and a control signal for controlling the negative electrode contactor.

[0007] The fusion bonding protecting module may further include a comparator for generating the comparison signal by comparing whether a difference between a first end voltage of the positive electrode contactor and a second end voltage of the positive electrode contactor is equal to or greater than a predetermined threshold value.

[0008] In a condition in which the fusion and bonding of the positive electrode contactor is sensed, the fusion bonding sensing signal becomes a first level, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparison signal has a level for indicating that a voltage difference between a first end and a second end of the positive electrode contactor is less than a predetermined threshold value, the AND gate may generate a compensation control signal with a level for opening the negative electrode contactor according to the logic control signal.

[0009] In a condition in which the fusion and bonding of the positive electrode contactor is sensed, the fusion bonding sensing signal becomes a first level, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparison signal has a level for indicating that a voltage difference between a first end and a second end of the positive electrode contactor is equal to or greater than a predetermined threshold value, the AND gate may generate the compensation control signal according to a logic level of the control signal.

[0010] The SR latch may generate the logic control signal to have a level that corresponds to a logic level '0' when the comparison signal has a level that corresponds to a logic level '0' and the fusion bonding sensing signal has a level that corresponds to a logic level '1', the SR latch may generate the logic control signal to have a level that corresponds to the logic level '1' when the comparison signal has a level that corresponds to the logic level '1' and the fusion bonding sensing signal has a level that corresponds to the logic level '0', and the SR latch may maintain a level of the logic control signal when the comparison signal and the fusion bonding sensing signal have a level that corresponds to the logic level '0'.

[0011] Another embodiment of the present invention provides a battery system including: a positive electrode contactor connected between a positive electrode of a battery pack and a first output end; a negative electrode contactor connected between a negative electrode of the battery pack and a second output end; a main control unit (MCU) for controlling the positive electrode contactor and the negative electrode contactor to be opened and closed, and generating a fusion bonding sensing signal according to whether the positive electrode contactor is fused and bonded; and a fusion bonding protecting module for controlling the negative electrode contactor according to a control signal for controlling the negative electrode contactor received from the MCU when a voltage difference between a first end and a second end of the positive electrode contactor is equal to or greater than a predetermined threshold value, and maintaining the negative electrode contactor to be opened irrespective of the control signal when the voltage difference is less than the predetermined threshold value.

[0012] The fusion bonding protecting module may include: a comparator for generating the comparison signal by comparing whether the voltage difference is equal to or greater than the predetermined threshold value; an SR latch including a set terminal for receiving the fusion bonding sensing signal and a reset terminal for receiving the comparison signal; and an AND gate for generating a compensation control signal by ANDing a logic control signal that is an inverted output of the SR latch and the control signal.

[0013] In a condition in which the fusion and bonding of the positive electrode contactor is sensed, the fusion bonding sensing signal becomes a first level, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparator generates the comparison signal with a level for indicating that the voltage difference is less than the predetermined threshold value, the SR latch may generate the logic control signal with a first level that corresponds to the logic level '0'. The AND gate may generate a compensation control signal with a level for opening the negative electrode contactor according to the logic control signal with the first level.

[0014] In a condition in which the fusion and bonding of the positive electrode contactor is sensed, the fusion bonding sensing signal becomes a first level, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparator generates a comparison signal with a level for indicating that the voltage difference is equal to or greater than the predetermined threshold value, the SR latch may generate the logic control signal with a second level that corresponds to a logic level '1'. The AND gate may generate the compensation control signal with a level according to the control signal by the logic control signal with the second level.

[Advantageous Effects]

[0015] The present disclosure provides the fusion bonding protecting module for preventing risky conditions in advance by preventing multiple fusion and bonding of the contactor when the positive electrode contactor is fused and bonded, and a battery system including the same.

[Brief Description of the Drawings]

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- FIG. 1 shows a schematic diagram of a battery system according to an embodiment.
- FIG. 2 shows a fusion bonding protecting module according to an embodiment.
- FIG. 3 shows a waveform diagram of signals of a battery system in a normal state according to an embodiment.
- FIG. 4 shows a waveform diagram of signals of a battery system while a positive electrode contactor is fused and bonded according to an embodiment.
- FIG. 5 shows a waveform diagram of signals of a battery system while a positive electrode contactor is fused and bonded according to an embodiment.

[Mode for Invention]

[0017] The present disclosure provides a hardwired system for preventing multiple fusion and bonding of a contactor when a positive electrode contactor connected to a positive electrode (+) of the battery is fused and bonded. When software of the battery management system diagnoses fusion bonding of the positive contactor, and an SR latch and an AND gate are used so that arbitrary software of the battery management system performs a closing instruction on the negative electrode contactor connected to the negative electrode (-) of the battery, the negative electrode contactor is not closed. The present disclosure confirms whether the positive electrode contactor is fused and bonded, and resets the SR latch when the fusion and bonding status is solved.

[0018] The terms "module" and "unit" for components used in the following description are used only in order to make the specification easier. Therefore, these terms do not have meanings or roles that distinguish them from each other by themselves. In addition, the terms "-er", "-or", and "module" described in the specification mean units for processing at

least one function and operation, and can be implemented by hardware components or software components, and combinations thereof.

[0019] In describing embodiments of the present specification, when it is determined that a detailed description of the well-known art associated with the present invention may obscure the gist of the present invention, it will be omitted.

The accompanying drawings are provided only in order to allow embodiments disclosed in the present specification to be easily understood and are not to be interpreted as limiting the spirit disclosed in the present specification, and it is to be understood that the present invention includes all modifications, equivalents, and substitutions without departing from the scope and spirit of the present invention.

[0020] Terms including ordinal numbers such as first, second, and the like will be used only to describe various components, and are not interpreted as limiting these components. The terms are only used to differentiate one component from others.

[0021] It is to be understood that when one component is referred to as being "connected" or "coupled" to another component, it may be connected or coupled directly to another component or be connected or coupled to another component with the other component intervening therebetween. On the other hand, it is to be understood that when one component is referred to as being "connected or coupled directly" to another component, it may be connected or coupled to another component without the other component intervening therebetween.

[0022] It will be further understood that terms "comprises" or "have" used in the present specification specify the presence of stated features, numerals, steps, operations, components, parts, or a combination thereof, but do not preclude the presence or addition of one or more other features, numerals, steps, operations, components, parts, or a combination thereof.

[0023] FIG. 1 shows a schematic diagram of a battery system according to an embodiment.

[0024] As shown in FIG. 1, an external device 2 is connected between respective output ends P+ and P- of the battery system 1, and when the relays 21 and 22 are closed, the battery pack 10 may be electrically connected to the external device 2. A link capacitor CL is connected between the respective output ends P+ and P- of the battery system 1, and a voltage between respective ends of the link capacitor CL is an output voltage of the battery system 1.

[0025] When the external device 2 is an electric load, the battery system 1 may be operable as a power source for supplying energy to the electric load 2 and may be discharged. The electric load may be a moving means or an energy storage system (ESS), and the moving means may, for example, be an electric vehicle, a hybrid vehicle, or smart mobility. When the external device 2 is a charger, the battery system 1 may receive energy from a power system through the charger 2, and may be charged.

[0026] The battery system 1 includes a battery pack 10, a positive electrode contactor 21, a negative electrode contactor 22, a precharge contactor 23, a precharge resistor 24, a current sensor 25, a fusion bonding protecting module 30, and a battery management system BMS 100.

[0027] The battery pack 10 includes a plurality of battery cells 10_1 to 10_4 connected in series. FIG. 1 shows that the battery pack 10 includes four battery cells 10_1 to 10_4 connected in series, which is an example and is not limited thereto. For example, five or more battery cells may be connected in series, or two or more battery cells connected in parallel may be connected in series.

[0028] The positive electrode contactor 21 is connected between the positive electrode of the battery pack 10 and the output terminal P+, the negative electrode contactor 22 is connected between the negative electrode of the battery pack 10 and the output terminal P-, and the precharge contactor 23 and the precharge resistor 24 connected in series are connected to the positive electrode contactor 21 in parallel and are connected between the positive electrode of the battery pack 10 and the output terminal P+. In a normal state in which the positive electrode contactor 21 is not fused and bonded, it may be controlled to open and close the positive electrode, the negative electrode, and the precharge contactors 21, 22, and 23 according to control by a main control unit MCU 110 of the BMS 100. The positive electrode contactor 21 may be opened or closed by a control signal SR1, the negative electrode contactor 22 may be opened or closed by a control signal SR3.

[0029] The current sensor 25 may sense a current flowing to the battery pack 10, and may transmit a current sensing signal IS for indicating the sensed current to the MCU 110.

[0030] The BMS 100 includes a MCU 110 and a cell managing module 120.

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[0031] The cell managing module 120 may be connected to the battery cells 10_1 to 10_4, cell voltages and temperatures of the battery cells 10_1 to 10_4 are measured and transmitted to the MCU 110, and a balancing operation may be performed on a specific cell from among the battery cells 10_1 to 10_4 according to a cell balancing control signal received from the MCU 110.

[0032] Software including programs including charging and discharging control, cell balancing control, and protecting operation control on the battery pack 10 is installed in the MCU 110. The MCU 110 may control charging and discharging, cell balancing, and protecting operations on the battery pack 10 based on information and signals received from the cell managing module 120, the current sensor 25, and the outside.

[0033] When the external device 2 is connected to the battery system 1 and charging or discharging is performed, the

MCU 110 may close the negative electrode contactor 22 and the precharge contactor 23, and may close the positive electrode contactor 21 after a predetermined time. In this instance, the predetermined time may be set from a time when the precharge contactor 23 is closed to a time when a link capacitor voltage charged in the link capacitor CL reaches a predetermined ratio (e.g., 95 %) with respect to a voltage of the battery pack 10.

[0034] The MCU 110 may close the positive electrode contactor 21 and may then open the precharge contactor 23. While the battery pack 10 is charged or discharged, the positive electrode contactor 21 and the negative electrode contactor 22 may be closed, and a charging current path or a discharging current path may be configured. The MCU 110 may adjust levels of the control signals SR1 to SR3 t control the positive contactor, the negative electrode contactor, and the precharge contactor to be opened or closed.

[0035] When the battery system 1 is electrically disconnected (i.e., Off), and a voltage is less than a predetermined threshold value between a first end voltage VA of the positive electrode contactor 21 and a second end voltage VB of the positive electrode contactor 21, the MCU 110 may determine the positive electrode contactor 21 to be fused and bonded. The voltage VA may be a voltage at a first end of the positive electrode contactor 21 provided near the positive electrode of the battery pack 10, and the voltage VB may be a voltage at a second end of the positive electrode contactor 21 provided near the output terminal P+.

[0036] The MCU 110 senses the fused and bonded state of the positive electrode contactor 21, and generates a fusion bonding sensing signal CST. The MCU 110 may generate a high-level fusion bonding sensing signal CST when sensing that the positive electrode contactor 21 is fused and bonded, and it may generate a low-level fusion bonding sensing signal CST when the positive electrode contactor 21 is not fused and bonded but is in a normal state. The MCU 110 may generate a low-level fusion bonding sensing signal CST according to a fusion bonding clear signal supplied from the outside. For example, when the fusion and bonding of the positive electrode contactor 21 is physically solved by maintenance, the BMS 100 may receive the fusion bonding clear signal from the outside through communication, and may transmit the same to the MCU 110. The MCU 110 may reset the high-level fusion bonding sensing signal CST to be low-level.

[0037] The fusion bonding protecting module 30 may receive a control signal SR2 from the MCU 110, may generate a compensation control signal SRC2 on a level for opening the negative electrode contactor 22 regardless of a level of the control signal SR2 and may supply the same to the negative electrode contactor 22 when the positive electrode contactor 21 is fused and bonded, and may generate the compensation control signal SRC2 according to the level of the control signal SR2 and may supply the same to the negative electrode contactor 22 when the positive electrode contactor 21 is not fused and bonded but is in the normal state.

[0038] FIG. 2 shows a fusion bonding protecting module according to an embodiment.

[0039] The fusion bonding protecting module 30 includes a comparator 31, an SR latch 32, and an AND gate 33.

[0040] The comparator 31 compares the first end voltage VA and the second end voltage VB of the positive electrode contactor 21, generates a high-level output when the difference between the voltage VA and the voltage VB is equal to or greater than a predetermined threshold value, and generates a low-level output in another case. An output of the comparator 31 will be referred to as a comparison signal CS.

[0041] The comparator 31 determines whether the positive electrode contactor 21 is fused and bonded, and the threshold value between the voltage VA and the voltage VB is equal to or greater than a threshold value when the positive electrode contactor 21 is not fused and bonded but is in the normal state so the comparator 31 may generate a high-level comparison signal CS and may output the same. When the positive electrode contactor 21 is fused and bonded, the difference between the voltage VA and the voltage VB is less than the threshold value so the comparator 31 may generate a low-level comparison signal CS and may output the same.

[0042] The SR latch 32 may provide outputs and inverted outputs according to a logic level of a signal supplied to a set terminal S and a logic level of a signal supplied to a reset terminal R through an output terminal Q and an inverted output terminal Q'. According to an embodiment, an inverted output from the SR latch 32 is provided to the AND gate 33, and the inverted output from the SR latch 32 is referred to as a logic control signal LCS. The logic level of the signal supplied to the set terminal S and the reset terminal R of the SR latch 32 may be '0' or '1'. The fusion bonding sensing signal CST is input to the set terminal S of the SR latch 32, and the comparison signal CS is input to the reset terminal R of the SR latch 32.

[0043] A method for the SR latch 32 to determine the logic levels of the outputs and the inverted outputs according to the logic levels of the signals input to the set terminal S and the reset terminal R is expressed in Table 1.

(Table 1)

S R Q Q'
0 0 previous output previous inverted output
0 1 0 1

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(continued)

S	R	Q	Q'
1	0	1	0
1	1	0	0

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[0044] The AND gate 33 ANDs the control signal SR2 and the logic control signal LCS to generate a compensation control signal SRC2. When the logic control signal LCS has the low level that corresponds to the logic level '0', the AND gate 33 generates a low-level compensation control signal SRC2 irrespective of the level of the control signal SR2. The negative electrode contactor 22 is opened by the compensation control signal SRC2. While the logic control signal LCS is at the low level, the negative electrode contactor 22 may be maintained to be opened. When the logic control signal LCS is at the high level that corresponds to the logic level '1', the AND gate 33 generates a compensation control signal SRC2 according to the logic level of the control signal SR2. That is, when the control signal SR2 has the high level that corresponds to the logic level '1' for indicating the closing of the negative electrode contactor 22, the AND gate 33 generates a high-level compensation control signal SRC2. When the control signal SR2 has the low level that corresponds to the logic level '0' for indicating the opening of the negative electrode contactor 22, the AND gate 33 generates a low-level compensation control signal SRC2. The negative electrode contactor 22 is closed by the high-level compensation control signal SRC2, and is opened by the low-level compensation control signal SRC2.

[0045] An operation of the fusion bonding protecting module according to an embodiment will now be described with reference to FIG. 3 to FIG. 5.

[0046] FIG. 3 shows a waveform diagram of signals of a battery system in a normal state according to an embodiment. [0047] At a time T1, the control signal SR2 becomes high level to close the negative electrode contactor 22. At a time T2, the control signal SR3 becomes high level to close the precharge contactor 23. The time T1 may be prior to the time T2, or the time T1 and the time T2 may be the same. At a time T3, the control signal SR1 becomes high level to close the positive electrode contactor 21, and at a time T4, the control signal SR3 becomes low level to open the precharge contactor 23.

[0048] As the positive electrode contactor 21 is opened before the time T3, the voltage difference between the voltage VA and the voltage VB is equal to or greater than the threshold value and the comparison signal CS is at the high level. As the state is normal, the fusion bonding sensing signal CST is at the low level. Therefore, the logic control signal LCS is at the high level by the high-level comparison signal CS.

[0049] After the time T3, the positive electrode contactor 21 is closed so the voltage difference between the voltage VA and the voltage VB is less than the threshold value, and the comparison signal CS is at the low level. As the fusion bonding sensing signal CST and the comparison signal CS are at the low level, the SR latch 32 maintains the logic control signal LCS at the high level. As the high-level logic control signal LCS is input to the AND gate 33 generates the compensation control signal SRC2 according to the control signal SR2. Therefore, the compensation control signal SRC2 may have a same waveform as the control signal SR2.

[0050] At a time T5, the control signals SR1 and SR2 are at the low level, and the positive electrode contactor 21 and the negative electrode contactor 22 are opened. After the time T5, the voltage difference between the voltage VA and the voltage VB is equal to or greater than the threshold value, and the comparison signal CS is at the high level. The SR latch 32 maintains the logic control signal LCS at the high level according to the high-level comparison signal CS.

[0051] According to an embodiment, the positive electrode contactor 21 is opened, and the MCU 110 then diagnoses whether the positive electrode contactor 21 is fused and bonded. For example, at a time T6, the MCU 110 may sense the fusing and bonding of the positive electrode contactor 21, and the fusion bonding sensing signal CST may increase to the high level.

[0052] FIG. 4 shows a waveform diagram of signals of a battery system while a positive electrode contactor is fused and bonded according to an embodiment.

[0053] FIG. 4 shows a waveform diagram when the positive electrode contactor 21 is fused and bonded, the fusion and bonding of the positive electrode contactor 21 is not solved, a fault clear is performed by the software installed in the MCU 110, and the fusion bonding sensing signal CST is changed to low level from high level at a time T7.

[0054] After the time T7, the fusion bonding sensing signal CST is at the low level, and the comparison signal CS is at the low level so the logic control signal LCS is maintained at the low level. Hence, the AND gate 33 outputs the low-level compensation control signal SRC2 irrespective of the control signal SR2. For example, at a time T8, the control signal SR2 rises to the high level, and the compensation control signal SRC2 is maintained at the low level.

[0055] FIG. 5 shows a waveform diagram of signals of a battery system while a positive electrode contactor is fused and bonded according to an embodiment.

[0056] FIG. 5 shows a waveform diagram when the fusion and bonding of the positive electrode contactor 21 is sensed,

the fusion and bonding of the actual positive electrode contactor 21 is solved, and the fault clear is performed by the software installed in the MCU 110, or a signal for the fault clear is transmitted to the BMS 100 from the outside. For example, it will be assumed that the fusion and bonding of the positive electrode contactor 21 is solved at a time T9.

[0057] At the time T9, the fusion and bonding of the positive electrode contactor 21 is solved so the voltage difference between the voltage VA and the voltage VB becomes equal to or greater than the threshold value, and the comparison signal CS becomes high level. At a time T10, the fusion bonding sensing signal CST is changed to low level from high level by the fault clear. After the time T10, the SR latch 32 generates the high-level logic control signal LCS according to the high-level comparison signal CS and the low-level fusion bonding sensing signal CST. Therefore, the AND gate 33 determines the level of the compensation control signal SRC2 according to the control signal SR2 and outputs a resultant signal.

[0058] For example, when the control signal SR2 rises to the high level at a time T11, the AND gate 33 changes the compensation control signal SRC2 to high level from low level.

[0059] In the present specification, it is described that the signal level that corresponds to the logic level '0' is the low level, and the signal level that corresponds to the logic level '1' is the high level. For example, the control signals SR1 to SR3 output by the MCU 110, the fusion bonding sensing signal CST, input and output signals of the SR latch 32, the output signal of the comparator 31, and the input and output signals of the AND gate 33 may be at the low level corresponding to the logic level '0', and may be at the high level corresponding to the logic level '1'. This is an example for describing an embodiment, and the present disclosure is not limited thereto.

[0060] While this invention has been described in connection with what is presently considered to be practical embodiments, it is to be understood that the invention is not limited to the disclosed embodiments, but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the spirit and scope of the appended claims.

Claims

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A fusion bonding protecting module for controlling a negative electrode contactor connected between a negative
electrode of a battery pack and a second output end according to fusion and bonding of a positive electrode contactor
connected between a positive electrode of the battery pack and a first output end, comprising:

an SR latch including a set terminal for receiving a fusion bonding sensing signal according to whether the positive electrode contactor is fused and bonded and a reset terminal for receiving a comparison signal according to a voltage difference between a first end and a second end of the positive contactor; and an AND gate for generating a compensation control signal by ANDing a logic control signal that is an inverted output of the SR latch and a control signal for controlling the negative electrode contactor.

- 2. The fusion bonding protecting module of claim 1, further comprising a comparator for generating the comparison signal by comparing whether a difference between a first end voltage of the positive electrode contactor and a second end voltage of the positive electrode contactor is equal to or greater than a predetermined threshold value.
- **3.** The fusion bonding protecting module of claim 1, wherein

in a condition in which the fusion and bonding of the positive electrode contactor is sensed, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparison signal has a level for indicating that a voltage difference between a first end and a second end of the positive electrode contactor is less than a predetermined threshold value, the AND gate generates a compensation control signal with a level for opening the negative electrode contactor according to the logic control signal.

4. The fusion bonding protecting module of claim 1, wherein

in a condition in which the fusion and bonding of the positive electrode contactor is sensed, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparison signal has a level for indicating that a voltage difference between a first end and a second end of the positive electrode contactor is equal to or greater than a predetermined threshold value, the AND gate generates the compensation control signal according to a logic level of the control signal.

5. The fusion bonding protecting module of claim 1, wherein

the SR latch generates the logic control signal to have a level that corresponds to a logic level '0' when the comparison signal has a level that corresponds to a logic level '0' and the fusion bonding sensing signal has a level that corresponds to a logic level '1',

the SR latch generates the logic control signal to have a level that corresponds to the logic level '1' when the comparison signal has a level that corresponds to the logic level '1' and the fusion bonding sensing signal has a level that corresponds to the logic level '0', and

the SR latch maintains a level of the logic control signal when the comparison signal and the fusion bonding sensing signal have a level that corresponds to the logic level '0'.

6. A battery system comprising:

a positive electrode contactor connected between a positive electrode of a battery pack and a first output end; a negative electrode contactor connected between a negative electrode of the battery pack and a second output end;

a main control unit (MCU) for controlling the positive electrode contactor and the negative electrode contactor to be opened and closed, and generating a fusion bonding sensing signal according to whether the positive electrode contactor is fused and bonded; and

a fusion bonding protecting module for controlling the negative electrode contactor according to a control signal for controlling the negative electrode contactor received from the MCU when a voltage difference between a first end and a second end of the positive electrode contactor is equal to or greater than a predetermined threshold value, and maintaining the negative electrode contactor to be opened irrespective of the control signal when the voltage difference is less than the predetermined threshold value.

7. The battery system of claim 6, wherein the fusion bonding protecting module includes:

a comparator for generating the comparison signal by comparing whether the voltage difference is equal to or greater than the predetermined threshold value;

an SR latch including a set terminal for receiving the fusion bonding sensing signal and a reset terminal for receiving the comparison signal; and

an AND gate for generating a compensation control signal by ANDing a logic control signal that is an inverted output of the SR latch and the control signal.

8. The battery system of claim 7, wherein

in a condition in which the fusion and bonding of the positive electrode contactor is sensed, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparator generates the comparison signal with a level for indicating that the voltage difference is less than the predetermined threshold value, the SR latch generates the logic control signal with a first level that corresponds to the logic level '0'.

9. The battery system of claim 8, wherein

the AND gate generates a compensation control signal with a level for opening the negative electrode contactor according to the logic control signal with the first level.

10. The battery system of claim 7, wherein

in a condition in which the fusion and bonding of the positive electrode contactor is sensed, and the fusion bonding sensing signal becomes a second level that is different from the first level, and when the comparator generates a comparison signal with a level for indicating that the voltage difference is equal to or greater than the predetermined threshold value, the SR latch generates the logic control signal with a second level that corresponds to a logic level '1'.

11. The battery system of claim 10, wherein

the AND gate generates the compensation control signal with a level according to the control signal by the logic control signal with the second level.

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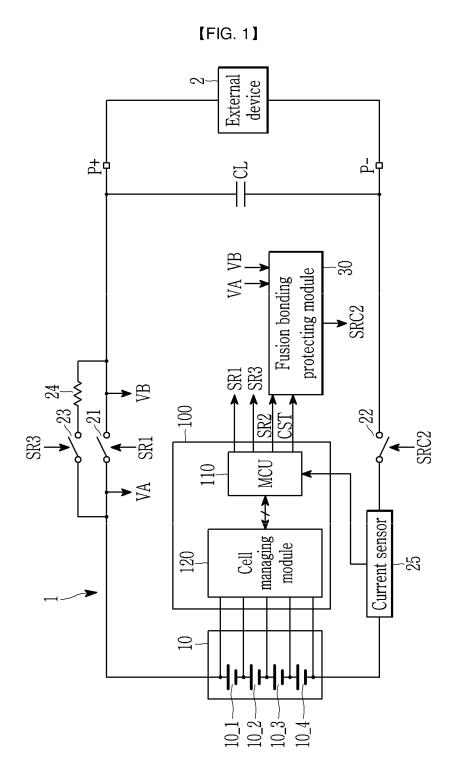
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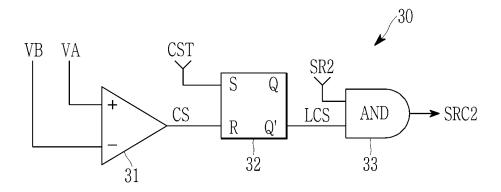
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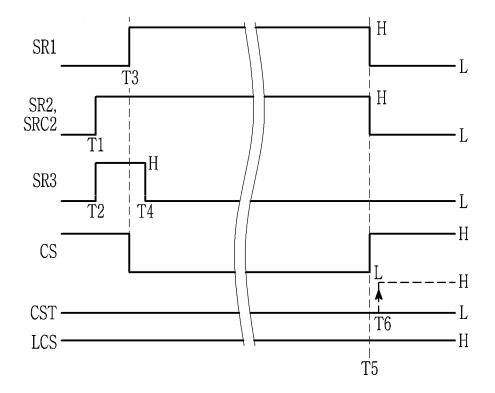
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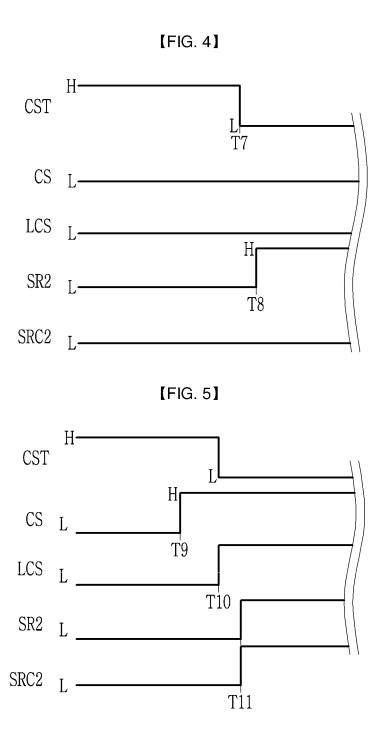


[FIG. 2]



[FIG. 3]





INTERNATIONAL SEARCH REPORT

International application No.

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5 CLASSIFICATION OF SUBJECT MATTER H02J 7/00(2006.01)i; H02H 7/18(2006.01)i; H03K 3/037(2006.01)i; G01R 19/10(2006.01)i; G01R 19/00(2006.01)i; G01R 19/165(2006.01)i According to International Patent Classification (IPC) or to both national classification and IPC FIELDS SEARCHED 10 Minimum documentation searched (classification system followed by classification symbols) H02J 7/00(2006.01); B60L 3/00(2006.01); B60R 16/033(2006.01); G01R 31/02(2006.01); G01R 31/327(2006.01); G01R 31/36(2006.01); H01H 47/00(2006.01); H03K 23/74(2006.01) Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Korean utility models and applications for utility models: IPC as above 15 Japanese utility models and applications for utility models: IPC as above Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) eKOMPASS (KIPO internal) & keywords: 배터리(battery), 응접합(fusion junction), 래치(latch), 컨택터(contactor), AND 개 이트(AND gate) C. DOCUMENTS CONSIDERED TO BE RELEVANT 20 Category* Relevant to claim No. Citation of document, with indication, where appropriate, of the relevant passages KR 10-2017-0097481 A (LG CHEM, LTD.) 28 August 2017 (2017-08-28) See paragraphs [0037]-[0048]; and figure 2. 1-11 Α KR 10-1664745 B1 (HYUNDAI MOTOR COMPANY et al.) 24 October 2016 (2016-10-24) 25 See paragraphs [0033]-[0034]; and figure 2. Α 1 - 11KR 10-2010-0064068 A (HYUNDAI MOTOR COMPANY) 14 June 2010 (2010-06-14) See claims 1-4; and figure 2. 1-11 A JP 2013-219873 A (DENSO CORP.) 24 October 2013 (2013-10-24) 30 See claims 1-2; and figure 1. A 1-11 US 2019-0025362 A1 (PANASONIC INTELLECTUAL PROPERTY MANAGEMENT CO., LTD.) 24 January 2019 (2019-01-24) See claims 1-15; and figure 1. 1-11 35 Further documents are listed in the continuation of Box C. See patent family annex. later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention Special categories of cited documents: document defining the general state of the art which is not considered to be of particular relevance document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "D" document cited by the applicant in the international application 40 "E" earlier application or patent but published on or after the international filling date $\,$ document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) document referring to an oral disclosure, use, exhibition or other being obvious to a person skilled in the art "O" "&" document member of the same patent family document published prior to the international filing date but later than the priority date claimed 45 Date of the actual completion of the international search Date of mailing of the international search report 28 December 2022 02 January 2023

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