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## (54) IMPEDANCE MEASUREMENT METHOD AND PHOTOVOLTAIC SYSTEM

(57) An impedance detection method (800, 900, 1000, and 1100) and a photovoltaic system (400, 500, 1200, 1300, 1400, 1500, and 1600) are provided, to help reduce complexity and costs of impedance detection. The method includes: sampling a voltage between a direct current bus and a protective earthing (PE) at each of a first time point and a second time point, to obtain a first voltage value ( $U_1$ ) and a second voltage value ( $U_2$ ); sampling a leakage current ( $I_{rcd}$ ) on the direct current bus

at each of the first time point and the second time point, to obtain a first current value ( $I_{rcd1}$ ) and a second current value ( $I_{rcd2}$ ); and calculating an insulation impedance value ( $R_{pv}$ ) of a combiner box (1, 2, ..., M, and 240) based on a difference between the first voltage value ( $U_1$ ) and the second voltage value ( $U_2$ ) and a difference between the first current value ( $I_{rcd1}$ ) and the second current value ( $I_{rcd2}$ ), to determine whether impedance of the combiner box (1, 2, ..., M, and 240) is abnormal.

Sample a voltage between a direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value

Sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value

Calculate an insulation impedance value of a combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal

# TECHNICAL FIELD

**[0001]** This application relates to the electronic field, and more specifically, to an impedance detection method and a photovoltaic system.

1

#### **BACKGROUND**

**[0002]** In a distributed inverter grid-connected power generation system, a plurality of combiner boxes are usually directly connected in parallel to a direct current bus of an inverter through a direct current-to-direct current (direct current-direct current, DC-DC) boost circuit. When insulation impedance of a photovoltaic string in a combiner box to the ground is low, the inverter detects, through an insulation impedance detection circuit, that insulation impedance of the system does not meet a safety requirement, and a shutdown operation needs to be performed. Consequently, all the combiner boxes connected in parallel to the direct current bus of the inverter cannot work, resulting in a loss of an energy yield of the inverter.

**[0003]** Currently, different direct current cabinets may be connected in series to direct current switches, and the insulation impedance of the system is separately detected by controlling the direct current switch to be turned off. A direct current switch of a combiner box that is detected to be normal may be turned on, and a direct current switch of a combiner box that is detected to be abnormal may be turned off.

**[0004]** However, in the method, a direct current contactor needs to be used to control the switch to be turned off and turned on. This is high in cost and complex in control.

#### **SUMMARY**

**[0005]** This application provides an impedance detection method and a photovoltaic system, to implement insulation impedance abnormality detection of a combiner box with a simple operation and low costs.

[0006] According to a first aspect, an impedance detection method is provided, and is applied to a photovoltaic system. The photovoltaic system includes a plurality of combiner boxes and an inverter. An input end of each of the plurality of combiner boxes is connected to a photovoltaic string, and output ends of the plurality of combiner boxes are connected to the inverter by using a direct current bus. The combiner box includes at least one direct current switch and at least one DC-DC boost unit. An input end of the direct current switch is connected to the photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, and an output end of the DC-DC boost unit is connected to the direct current bus. The method includes: sampling a voltage between the direct current bus and a

protective earthing (protecting earthing, PE) at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; sampling a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value; and calculating an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

**[0007]** It should be understood that the combiner box in this embodiment of this application has a voltage control function, and may be connected to a plurality of photovoltaic strings connected in parallel. The photovoltaic string is obtained by connecting at least one photovoltaic cell in series.

**[0008]** In this embodiment of this application, the first voltage value and the second voltage value between the direct current bus and the protective earthing and the first current value and the second current value corresponding to the first voltage value and the second voltage value may be obtained through sampling. Therefore, impedance of each combiner box to the ground PE can be calculated, and a specific combiner box with abnormal impedance can be located. This helps reduce complexity and costs of impedance detection, and improve efficiency of impedance detection.

**[0009]** With reference to the first aspect, in some implementations of the first aspect, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in an off state at the first time point, and the power supply unit is in a working state at the second time point.

[0010] In this embodiment of this application, a voltage between a negative direct current bus BUS- and the protective earthing PE (which is briefly referred to as BUS-/PE below) and the leakage current on the direct current bus are first sampled at the first time point at which the power supply unit is in the off state, and then the BUS-/PE voltage and the leakage current on the direct current bus are sampled at the second time point at which the power supply unit is in the working state. In this way, a change of the BUS-/PE voltage of each combiner box and a change of the current on the direct current bus can be obtained by changing a switching state of the power supply unit.

**[0011]** With reference to the first aspect, in some implementations of the first aspect, before the obtaining a second voltage value and the obtaining a second current value, the method further includes: turning on the power supply unit, so that the power supply unit is in the working state; and adjusting the voltage between the direct current bus and the protective earthing to a first preset voltage by using the power supply unit.

[0012] It should be understood that in this embodiment

of this application, the first voltage value and the first current value are obtained through sampling when the power supply unit is in the off state, and the second voltage value and the second current value are obtained through sampling when the power supply unit is in the working state.

**[0013]** In this embodiment of this application, when the power supply unit is in the working state, the BUS-/PE voltage may be first adjusted to the first preset voltage, and after the first preset voltage is stable, the first preset voltage is sampled, to obtain the second voltage value and the second current value. In this way, the insulation impedance value of the combiner box to the ground can be accurately calculated, to improve accuracy of impedance detection.

**[0014]** With reference to the first aspect, in some implementations of the first aspect, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.

[0015] In this embodiment of this application, the BUS-/PE voltage and the leakage current on the direct current bus are sampled twice when the power supply unit is in the working state, to obtain two different BUS-/PE voltages and two different leakage currents on the direct current bus through sampling at two different time points when the power supply unit is in the working state. In this way, a change of the BUS-/PE voltage of each combiner box and a change of the current on the direct current bus can be obtained.

**[0016]** With reference to the first aspect, in some implementations of the first aspect, before the obtaining a first voltage value and the obtaining a first current value, the method further includes: turning on the power supply unit, so that the power supply unit is in the working state; and adjusting the voltage between the direct current bus and the protective earthing to a second preset voltage by using the power supply unit.

**[0017]** It should be understood that in this embodiment of this application, the first voltage value and the first current value are obtained through sampling when the power supply unit is in the working state.

**[0018]** In this embodiment of this application, when the power supply unit is in the working state, the BUS-/PE voltage may be first adjusted to the second preset voltage, and after the second preset voltage is stable, the second preset voltage is sampled, to obtain the first voltage value and the first current value. In this way, the insulation impedance value of the combiner box to the ground can be accurately calculated, to improve accuracy of impedance detection.

**[0019]** With reference to the first aspect, in some implementations of the first aspect, the method further includes: adjusting the voltage between the direct current bus and the protective earthing to a third preset voltage by using the power supply unit after first preset duration.

**[0020]** It should be understood that in this embodiment of this application, the second voltage value and the second current value are obtained through sampling when the power supply unit is in the working state.

[0021] In this embodiment of this application, when the power supply unit is in the working state, the BUS-/PE voltage may be first adjusted to the third preset voltage, and after the third preset voltage is stable, the third preset voltage is sampled, to obtain the second voltage value and the second current value. In this way, the insulation impedance value of the combiner box to the ground can be accurately calculated, to improve accuracy of impedance detection.

[0022] With reference to the first aspect, in some implementations of the first aspect, the method further includes: if it is determined that the impedance of the combiner box is abnormal, adjusting a maximum power point tracking MPPT voltage corresponding to the photovoltaic string in the combiner box; sampling the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value; sampling the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value; and calculating an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

**[0023]** Further, in this embodiment of this application, after it is determined that impedance of a combiner box is abnormal, an MPPT whose positive end has abnormal impedance to the ground in the abnormal combiner box may be located. In this way, location is more accurate and efficient.

[0024] With reference to the first aspect, in some implementations of the first aspect, before the obtaining a first MPPT voltage and the obtaining a third voltage value, the method further includes: adjusting the MPPT voltage to a fourth preset voltage by using the DC-DC boost unit. [0025] In this embodiment of this application, the MPPT voltage is first adjusted to the fourth preset voltage, and after the fourth preset voltage is stable, the fourth preset voltage is sampled, to obtain the first MPPT voltage and the third voltage value. In this way, the insulation impedance value of the MPPT voltage to the ground can be accurately calculated, to improve accuracy of impedance detection.

[0026] With reference to the first aspect, in some implementations of the first aspect, the method further includes: adjusting the MPPT voltage to a fifth preset voltage by using the DC-DC boost unit after second preset duration.

**[0027]** In this embodiment of this application, the MPPT voltage is first adjusted to the fifth preset voltage, and after the fifth preset voltage is stable, the fifth preset voltage is sampled, to obtain the second MPPT voltage

and the fourth voltage value. In this way, the insulation impedance value of the MPPT voltage to the ground can be accurately calculated, to improve accuracy of impedance detection.

**[0028]** With reference to the first aspect, in some implementations of the first aspect, the power supply unit includes at least one of a relay, a resistor, a diode, or a direct current power supply.

**[0029]** It should be understood that the resistor may be an output resistor, the relay may be an output relay, and the relay is configured to control the power supply unit to be turned off and turned on, to control the power supply unit to be in the off state or the working state.

[0030] According to a second aspect, a photovoltaic system is provided, and includes a plurality of combiner boxes, an inverter, and an impedance detection apparatus. An input end of each of the plurality of combiner boxes is connected to a photovoltaic string, and the plurality of combiner boxes are connected to the inverter by using a direct current bus. The combiner box includes at least one direct current switch and at least one DC-DC boost unit. An input end of the direct current switch is connected to the photovoltaic string, an output end of the DC-DC boost unit, and an output end of the DC-DC boost unit is connected to the direct current bus.

[0031] The impedance detection apparatus is configured to: sample a voltage between the direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; and sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value; and calculate an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

**[0032]** With reference to the second aspect, in some implementations of the second aspect, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in an off state at the first time point, and the power supply unit is in a working state at the second time point.

**[0033]** With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to: turn on the power supply unit before the second voltage value and the second current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a first preset voltage.

**[0034]** With reference to the second aspect, in some implementations of the second aspect, the photovoltaic system further includes a power supply unit, one end of

the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.

**[0035]** With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to: turn on the power supply unit before the first voltage value and the first current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a second preset voltage.

**[0036]** With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to adjust the voltage between the direct current bus and the protective earthing to a third preset voltage after first preset duration.

[0037] With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to: if it is determined that the impedance of the combiner box is abnormal, adjust a maximum power point tracking MPPT voltage corresponding to the photovoltaic string in the combiner box; sample the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value; and sample the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value; and calculate an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

**[0038]** With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to adjust the MPPT voltage to a fourth preset voltage before the first MPPT voltage and the third voltage value are obtained.

**[0039]** With reference to the second aspect, in some implementations of the second aspect, the impedance detection apparatus is configured to adjust the MPPT voltage to a fifth preset voltage after second preset duration.

**[0040]** With reference to the second aspect, in some implementations of the second aspect, the power supply unit includes at least one of a relay, a resistor, a diode, or a direct current power supply.

**[0041]** According to a third aspect, an impedance detection apparatus is provided. The impedance detection apparatus may be an electronic device, or may be a chip or a chip system in an electronic device. The impedance detection apparatus may include a sensor detection unit and a processing unit. The sensor detection unit may be a residual current device (residual current device, RCD) sensor.

[0042] When the impedance detection apparatus is an

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electronic device, the processing unit may be a processor. The impedance detection apparatus may further include a storage unit, and the storage unit may be a memory. The storage unit is configured to store instructions, and the processing unit executes the instructions stored in the storage unit, so that the electronic device implements the impedance detection method described in any one of the first aspect or the possible implementations of the first aspect.

**[0043]** When the impedance detection apparatus is a chip or a chip system in an electronic device, the processing unit may be a processor. The processing unit executes instructions stored in a storage unit, so that the electronic device implements the impedance detection method described in any one of the first aspect or the possible implementations of the first aspect. The storage unit may be a storage unit (for example, a register or a cache) in the chip, or may be a storage unit (for example, a read-only memory or a random access memory), in the electronic device, located outside the chip.

**[0044]** According to a fourth aspect, an impedance detection apparatus is provided, and includes at least one processor and an interface. The interface is configured to provide program instructions or data to the at least one processor. The at least one processor is configured to execute the program instructions, so that the impedance detection apparatus performs the method in any possible implementation of the first aspect.

[0045] Optionally, the impedance detection apparatus in the fourth aspect may further include a memory. The processor may invoke a program in the memory. Further, the memory may be integrated with the processor, or the memory and the processor may be separately disposed. [0046] In a specific implementation process, the memory may be a non-transitory (non-transitory) memory, for example, a read-only memory (read only memory, ROM). The memory and the processor may be integrated into one chip, or may be separately disposed on different chips. A type of the memory and a manner of disposing the memory and the processor are not limited in this embodiment of this application.

**[0047]** It should be understood that a related data exchange process such as sending indication information may be a process of outputting the indication information from the processor, and receiving capability information may be a process of receiving the input capability information by the processor. Specifically, data output by the processor may be output to a transmitter, and input data received by the processor may be from a receiver. The transmitter and the receiver may be collectively referred to as a transceiver.

**[0048]** According to a fifth aspect, a chip or a chip system is provided. The chip or the chip system includes at least one processor and a communication interface. The communication interface and the at least one processor are interconnected by using a line, and the at least one processor is configured to run a computer program or instructions, to perform the method in any possible im-

plementation of the first aspect.

[0049] According to a sixth aspect, a computer program product is provided. The computer program product includes a computer program (which may also be referred to as code or instructions). When the computer program is run, a computer is enabled to perform the method in any possible implementation of the first aspect. [0050] According to a seventh aspect, a computer-readable storage medium is provided. The computer-readable storage medium stores a computer program (which may also be referred to as code or instructions). When the computer program is run on a computer, the computer is enabled to perform the method in any possible implementation of the first aspect.

#### **BRIEF DESCRIPTION OF DRAWINGS**

#### [0051]

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FIG. 1 is a circuit topology diagram of a photovoltaic system according to an embodiment of this application;

FIG. 2 is a schematic diagram of an internal circuit of a combiner box according to an embodiment of this application;

FIG. 3 is a circuit topology diagram of another photovoltaic system according to an embodiment of this application;

FIG. 4A, FIG. 4B, and FIG. 4C are a circuit topology diagram of still another photovoltaic system according to an embodiment of this application;

FIG. 5 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 6 is a schematic diagram of a Thevenin equivalent circuit of a combiner box according to an embodiment of this application;

FIG. 7 is a schematic diagram of a multi-machine parallel Thevenin equivalent circuit according to an embodiment of this application;

FIG. 8 is a schematic flowchart of an impedance detection method according to an embodiment of this application;

FIG. 9 is a schematic flowchart of another impedance detection method according to an embodiment of this application;

FIG. 10 is a schematic flowchart of still another impedance detection method according to an embodiment of this application;

FIG. 11 is a schematic flowchart of yet another impedance detection method according to an embodiment of this application;

FIG. 12 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 13 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 14 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 15 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 16 is a circuit topology diagram of yet another photovoltaic system according to an embodiment of this application;

FIG. 17 is a schematic block diagram of an impedance detection apparatus according to an embodiment of this application; and

FIG. 18 is a schematic block diagram of another impedance detection apparatus according to an embodiment of this application.

#### **DESCRIPTION OF EMBODIMENTS**

**[0052]** The technical solutions in this application are described below with reference to the accompanying drawings.

**[0053]** For ease of clearly describing the technical solutions in embodiments of this application, the following descriptions are first provided.

**[0054]** First, in the embodiments shown below, terms and English acronyms and abbreviations such as a photovoltaic panel and a combiner box are examples provided for ease of description, and shall not constitute any limitation on this application. This application does not exclude a possibility of defining another term that can implement a same or similar function in an existing or future protocol.

**[0055]** Second, the terms "first", "second", and various numbers in the embodiments shown below are merely used for distinction for ease of description, and are not intended to limit the scope of embodiments of this application. For example, different voltage values and different time points are distinguished.

[0056] Third, "at least one" means one or more, and "a plurality of" means two or more. The term "and/or" describes an association relationship between associated objects, and indicates that three relationships may exist. For example, A and/or B may indicate the following three cases: Only A exists, both A and B exist, and only B exists, where A and B may be singular or plural. The character "/" usually indicates an "or" relationship between associated objects. "At least one of the following items (pieces)" or a similar expression thereof refers to any combination of these items, including a single item (piece) or any combination of a plurality of items (pieces). For example, at least one of a, b, and c may indicate a, b, c, a and b, a and c, b and c, or a, b, and c, where a, b, and c may be singular or plural.

**[0057]** Embodiments of this application may be applied to a distributed inverter multi-machine grid-connected power generation system (which is also referred to as a photovoltaic system below). For example, FIG. 1 is a circuit topology diagram of a photovoltaic system according

to an embodiment of this application. As shown in FIG. 1, M combiner boxes are connected in parallel to a direct current bus of an inverter, and each combiner box is connected to one photovoltaic panel. The photovoltaic panel is a square array formed by connecting a plurality of photovoltaic strings in parallel. The combiner box combines direct currents, and outputs a combined current to the inverter. The inverter performs maximum power point tracking (maximum power point tracking, MPPT) control, and outputs an alternating current obtained after inversion to a transformer (for example, a double split transformer). Finally, the transformer correspondingly transforms an alternating current voltage, and transmits the alternating current voltage to an alternating current power grid, to meet a power requirement of a load.

**[0058]** It should be understood that the photovoltaic string is formed by connecting a plurality of photovoltaic cells with a low voltage in series, which may multiply a voltage. For the photovoltaic panel formed by connecting a plurality of photovoltaic strings in parallel, a current may be multiplied while there is a same voltage.

**[0059]** In the photovoltaic system, the inverter is mainly configured to implement MPPT control and inversion functions. A medium voltage switch is connected to the inverter, and may be used to turn on, turn off, control, and protect an electric device in a process of power generation, transmission, and distribution in the system.

**[0060]** In a photovoltaic inverter technology, there are safety requirements in most photovoltaic inverter grid-connected power generation technology standards. It is required to detect an insulation impedance value of a combiner box to the ground before grid-connected power generation, and compare the detected insulation impedance value with a minimum impedance value required in the standard, to determine whether the insulation impedance value of the combiner box to the ground meets a safety grid connection requirement.

**[0061]** It should be understood that impedance includes resistance and reactance. The impedance is usually a complex number, and the resistance and the reactance respectively form a real part and an imaginary part of the complex number. The insulation impedance of the combiner box to the ground may be a pure resistance value, or may include inductive reactance or capacitive reactance.

[0062] In FIG. 1, an insulation impedance detection circuit is connected to the direct current bus of the inverter, and is configured to detect whether insulation impedance of the M combiner boxes to the ground is abnormal. When the inverter detects, through the insulation detection circuit, that insulation impedance of one or more of the M combiner boxes to the ground is abnormal, a shutdown operation needs to be performed. Consequently, all the combiner boxes connected in parallel to the direct current bus of the inverter cannot work, resulting in a loss of an energy yield.

**[0063]** FIG. 2 is a schematic diagram of an internal circuit 200 of a combiner box according to an embodiment

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of this application. As shown in FIG. 2, the circuit 200 includes a direct current switch 220 and a DC/DC boost circuit 230. A photovoltaic panel 210 is connected to one end of the direct current switch 220 in the combiner box 240, and the other end of the direct current switch 220 is connected to the DC/DC boost circuit 230.

**[0064]** The photovoltaic panel 210 is formed by connecting N photovoltaic strings in parallel. A photovoltaic cell may convert solar radiation light into electric energy by using a semiconductor material and a photoelectric conversion principle. Compared with a common battery, the photovoltaic cell is more energy-saving and environmental-friendly.

**[0065]** The direct current switch 220 is a circuit protection device, usually includes a conductive part, an arc extinguishing part, an operation part, a rotating part, and the like, may perform operations such as switching off and switching on, and performs a protection tripping function in case of a short circuit, overload, and a reverse current.

**[0066]** The DC/DC boost circuit 230 is a circuit commonly used in a DC/DC converter, and may perform a function of boosting a voltage. Usually, to implement maximum power point tracking MPPT, a DC/DC converter needs to be used at an upstream of an inverter, to convert a changing photovoltaic (PV) voltage into a stable output voltage for a downstream inverter to perform a direct current-to-alternating current DC/AC operation.

[0067] In a possible implementation, each combiner box shown in FIG. 1 may be detected by using a controllable switch. FIG. 3 is a circuit topology diagram of another photovoltaic system according to an embodiment of this application. As shown in FIG. 3, a plurality of direct current power supplies are connected in series to controllable switches S, and the controllable switches S are connected to combiner boxes, or a plurality of direct current power supplies and controllable switches S are integrated into combiner boxes. It should be understood that the combiner box is not shown in FIG. 3. When insulation impedance of the combiner box to the ground is detected, each combiner box may be separately detected by controlling the switch S to be turned off and turned on. A switch of a combiner box that is detected to be normal is turned on, and a switch of a combiner box that is detected to be abnormal is turned off, to ensure that a case in which another integrated machine cannot normally work due to an abnormal combiner box is avoided. [0068] In the possible implementation, a direct current controller needs to be used to control the switch S to be turned on and turned off. However, the direct current controller is high in cost, large in size, and complex in control, and is difficult to produce.

**[0069]** In view of this, embodiments of this application provide an impedance detection method and a photovoltaic system. A power supply unit and a sensor detection unit are added to a combiner box, to implement insulation impedance abnormality detection of the combiner box with a simple operation and low costs. In addition,

a photovoltaic string whose impedance to the ground is abnormal may be further detected by adjusting an MPPT voltage. In this way, an abnormal component of a gridconnected inverter can be quickly and accurately repaired, to help reduce a loss of an energy yield.

[0070] For example, FIG. 4A, FIG. 4B, and FIG. 4C are a circuit topology diagram of still another photovoltaic system 400 according to an embodiment of this application. As shown in FIG. 4A, FIG. 4B, and FIG. 4C, the photovoltaic system 400 includes M combiner boxes (a combiner box 1, a combiner box 2, ..., and a combiner box M) connected in parallel to an inverter. For example, the combiner box 1 is connected to N photovoltaic strings connected in parallel, the combiner box 2 is connected to Q photovoltaic strings connected in parallel, ..., and the combiner box M is connected to P photovoltaic strings connected in parallel. Values of N, Q, and P are all greater than or equal to 1, and the values of N, Q, and P may be the same or different, or the values of any two of N, Q, and P may be the same. This is not limited in this application.

**[0071]** In FIG. 4A, FIG. 4B, and FIG. 4C, the M combiner boxes are connected in parallel to the inverter. It should be understood that the M combiner boxes may alternatively be connected in series to the inverter, or may be connected to the inverter in a manner in which some combiner boxes are connected in parallel and the other combiner boxes are connected in series to the inverter. All the combiner boxes may be connected to a same quantity or different quantities of photovoltaic strings. This is not limited in this embodiment of this application.

**[0072]** In this embodiment of this application, a direct current contactor is not used to control a controllable switch, but a direct current switch built in the inverter is used. In this way, an operation is simple, costs can be reduced, and it is easy to implement productization.

**[0073]** For ease of description, the combiner box 1 is used as an example below for analysis. A circuit structure and an analysis process of another combiner box are the same as those of the combiner box 1. For example, FIG. 5 is a circuit topology diagram of yet another photovoltaic system 500 according to an embodiment of this application. The photovoltaic system 500 is described by using the combiner box 1 in the photovoltaic system 400 as an example.

[0074] As shown in FIG. 5, in the photovoltaic system 500, N photovoltaic strings are connected to an input end of the combiner box 1, and the combiner box 1 includes N direct current switches 401, N DC-DC boost units 402, and an impedance detection apparatus 403. An input end of the direct current switch 401 is connected to the photovoltaic string, an output end of the direct current switch 401 is connected to an input end of the DC-DC boost unit 402, an output end of the DC-DC boost unit 402 is connected to an input end of the impedance detection apparatus, and an output end of the impedance detection apparatus is connected to a direct current bus.

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**[0075]** Optionally, the photovoltaic system 500 includes a power supply unit 404, one end of the power supply unit is grounded, and the other end of the power supply unit is connected to the direct current bus.

**[0076]** The impedance detection apparatus 403 includes a sensor detection unit and a processing unit. The sensor detection unit may be a current sensor, an RCD sensor, or a resistor. It should be understood that the impedance detection apparatus 403 may be located inside the combiner box, or may be located outside the combiner box. This is not limited in this embodiment of this application.

[0077] The impedance detection apparatus is configured to: sample a voltage between the direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; and sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value; and calculate an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

**[0078]** Optionally, the photovoltaic system 500 further includes a power supply unit 404, one end of the power supply unit 404 is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in an off state at the first time point, and the power supply unit is in a working state at the second time point.

[0079] Optionally, the impedance detection apparatus is configured to: turn on the power supply unit before the second voltage value and the second current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a first preset voltage. [0080] Optionally, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.

**[0081]** Optionally, the impedance detection apparatus is configured to: turn on the power supply unit before the first voltage value and the first current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a second preset voltage.

**[0082]** Optionally, the impedance detection apparatus is configured to adjust the voltage between the direct current bus and the protective earthing to a third preset voltage after first preset duration.

**[0083]** Optionally, the impedance detection apparatus is configured to: if it is determined that the impedance of the combiner box is abnormal, adjust a maximum power point tracking MPPT voltage corresponding to the pho-

tovoltaic string in the combiner box; sample the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value; and sample the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value; and calculate an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

**[0084]** Optionally, the impedance detection apparatus is configured to adjust the MPPT voltage to a fourth preset voltage before the first MPPT voltage and the third voltage value are obtained.

**[0085]** Optionally, the impedance detection apparatus is configured to adjust the MPPT voltage to a fifth preset voltage after second preset duration.

**[0086]** Optionally, the power supply unit includes at least one of a relay, a resistor, a diode, or a direct current power supply.

[0087] In FIG. 5,  $I_{bus1+}$  is a current on a positive direct current bus BUS+, Ibus1- is a current on a negative direct current bus BUS-, and  ${\rm I}_{\rm DC1}$  is an output current of the power supply unit; MPPT<sub>1</sub>, MPPT<sub>2</sub>, ..., and MPPT<sub>N</sub> respectively represent MPPT voltages corresponding to the N photovoltaic strings;  $R_{\text{po1}}$  represents an insulation impedance value of a first MPPT positive voltage to the ground, and  $\mathbf{R}_{\mathrm{ne1}}$  represents an insulation impedance value of a first MPPT negative voltage to the ground; R<sub>po2</sub> represents an insulation impedance value of a second MPPT positive voltage to the ground, and R<sub>ne2</sub> represents an insulation impedance value of a second MPPT negative voltage to the ground; and by analogy,  $\mathsf{R}_{\mathsf{poN}}$ represents an insulation impedance value of an Nth MPPT positive voltage to the ground, and R<sub>neN</sub> represents an insulation impedance value of an Nth MPPT negative voltage to the ground.

[0088] In this embodiment of this application, the current sensor can sense information about a detected current, for example, I<sub>bus1</sub>+ and I<sub>bus1</sub>-, and can convert, for output, detected current information into an electrical signal or another form of information that meets a standard.
 [0089] A leakage current sensor can detect, in real time, signals output by branch sensors. When an insulation status of branches is normal, currents flowing through the leakage current sensor are the same, and are opposite in direction, and an output signal of the leakage current sensor is zero. When a branch is grounded, a differential current flows through the leakage current sensor, and an output signal of the leakage current sensor, and an output signal of the leakage current sensor, and an output signal of the leakage current sensor, and an output signal of the leakage current sensor, and an output signal of the leakage current sensor, and an output signal of the leakage current sensor.

**[0090]** For example,  $I_{bus1}$ + and  $I_{bus1}$ - may be sampled by using a discrete device, and  $I_{bus1}$ + and  $I_{bus1}$ - are subtracted to obtain the leakage current  $I_{red}$ .

**[0091]** For example, a precision resistor is usually connected in series to a current loop, and a sampling current

value is calculated based on a voltage difference obtained through conversion at two ends of the resistor.

**[0092]** In an optional embodiment, the power supply unit 404 includes at least one of a relay, a resistor, a diode, or a direct current power supply.

**[0093]** In this embodiment of this application, the relay K is an output relay, is an automatic switch that controls operation of a high current by using a low current, and is used for automatic adjustment, safety protection, circuit conversion, and the like in a circuit. In this embodiment of this application, the relay K may be turned off, so that the power supply unit is in the off state, or the relay K may be turned on, so that the power supply unit is in the working state.

**[0094]** In this embodiment of this application, the resistor  $R_0$  is an output resistor, and is a Thevenin equivalent resistor viewed from an output end of an amplifier. The amplifier may be equivalent to a signal source having the internal resistor  $R_0$ .

**[0095]** The direct current power supply is an adjustable direct current power supply. One end is connected to the ground through a resistor, and the other end is connected to the positive direct current bus BUS+ or the negative direct current bus BUS-, to provide a stable current to a load. For example, the adjustable direct current power supply may be a flyback power supply or a forward power supply. This is not limited in this embodiment of this application.

[0096] According to a Thevenin's theorem, the combiner box may be viewed from the negative direct current bus BUS- and the protective earthing PE (BUS-/PE), to obtain a Thevenin equivalent circuit of the combiner box shown in FIG. 6. An internal circuit may be equivalent to a voltage source  $\mathsf{U}_{oc}$  and a resistor  $\mathsf{R}_x$  connected in series. The resistor  $\mathsf{R}_x$  is equivalent to a resistance value  $\mathsf{R}_{pv}$  (namely, the insulation impedance value of the combiner box to the ground) of a photovoltaic panel side of the combiner box to the ground and resistance  $\mathsf{R}_s$  of an inverter system to the ground that are connected in parallel, and  $\mathsf{I}_{rcd}$  is a sampling current of the sensor detection unit.

**[0097]** Further, when the M combiner boxes are connected in parallel, a multi-machine parallel Thevenin equivalent circuit shown in FIG. 7 may be obtained by using the Thevenin's theorem.

**[0098]** In FIG. 7,  $U_{oc}$  is an equivalent voltage source of an internal circuit,  $R_{x1}$ ,  $R_{x2}$ , ..., and  $R_{xM}$  respectively represent equivalent resistance of a first combiner box, a second combiner box, ..., and an M<sup>th</sup> combiner box,  $I_{r1}$ ,  $I_{r2}$ , ..., and  $I_{rM}$  respectively represent leakage currents in the first combiner box, the second combiner box, ..., and the M<sup>th</sup> combiner box, DC<sub>1</sub>, DC<sub>2</sub>, ..., and DC<sub>M</sub> respectively represent adjustable direct current power supplies in the first combiner box, the second combiner box, ..., and the M<sup>th</sup> combiner box, K represents an output relay, and  $R_0$  represents an output resistor.

[0099] In this embodiment of this application, insulation impedance of the combiner box is analyzed based on

the Thevenin equivalent circuit shown in FIG. 6. The BUS-/PE voltage is changed by using the power supply unit, and insulation impedance of equivalent impedance of each combiner box to the ground may be calculated based on a change of the BUS-/PE voltage and a change of the RCD leakage current sensor in the combiner box, to determine whether the insulation impedance of the combiner box is abnormal. A specific impedance detection method is described below in detail with reference to FIG. 8, FIG. 9, and FIG. 10.

**[0100]** It should be understood that the steps and/or procedures of the impedance detection method in embodiments of this application may be performed by an impedance detection apparatus. The impedance detection apparatus may be integrated into the photovoltaic system described in this application, or may be used as an independent device to perform the steps and/or procedures of the impedance detection method through a detection interface. This is not limited in this application. **[0101]** For example, FIG. 8 is a schematic flowchart of an impedance detection method 800 according to an em-

**[0102]** S801: Sample a voltage between a direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value.

bodiment of this application. The method 800 includes

the following steps.

**[0103]** In this embodiment of this application, sampling may be performed by using a sensor detection unit in an impedance detection apparatus. The sensor detection unit may be a differential resistor, and a BUS-/PE voltage of a combiner box may be sampled by using the differential resistor, to obtain the first voltage value  $U_1$  and the second voltage value  $U_2$ .

**[0104]** For example, a BUS-/PE voltage of a combiner box may alternatively be sampled by using a voltage sensor, to obtain the first voltage value  $U_1$  and the second voltage value  $U_2$ .

**[0105]** S802: Sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value.

**[0106]** In this embodiment of this application, the leakage current on the direct current bus may be sampled by using an RCD leakage current sensor, to obtain the first current value  $I_{rcd1}$  and the second current value  $I_{rcd2}$ .

**[0107]** S803: Calculate an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

**[0108]** In this embodiment of this application,  $I_{rcd1}$  and  $I_{rcd2}$  may be expressed as follows:

$$I_{rcd1} = (U_{oc} - U_1)/R_x$$

$$I_{red2}=(U_{oc}-U_2)/R_x$$

**[0109]** Equivalent impedance  $R_{\chi}$  may be obtained through solving by combining the two formulas, and is expressed as follows:

$$R_x = (U_1 - U_2)/(I_{rcd1} - I_{rcd2})$$

**[0110]** Further, the insulation impedance value  $R_{pv}$  of the combiner box to the ground may be obtained based on the equivalent resistance  $R_x$  and resistance  $R_s$  of an inverter system to the ground, and is expressed as follows:

$$R_{pv}=(R_x\times R_s)/(R_s-R_x)$$

**[0111]** An insulation impedance standard of a normal inverter is not less than Upv  $_{\rm max}/(30~{\rm mA})$ . Therefore, when R $_{\rm pv}$ <Upv $_{\rm max}/(30~{\rm mA})$ , a low insulation impedance alarm may be reported.

**[0112]** It should be understood that Upv\_max is a maximum voltage value of an inverter grid-connected power generation system. There are usually two voltage levels based on different systems. For example, a value of Upv max may be 1500 V or 1100 V

**[0113]** It should be understood that  $U_1$  is not equal to  $U_2$ ,  $I_{rcd1}$  is not equal to  $I_{rcd2}$ , and  $U_1$  and  $U_2$  are limited by Upv\_max. Based on different systems,  $U_1$  is less than 1100 V or 1500 V, and similarly,  $U_2$  is less than 1100 V or 1500 V

**[0114]** In this embodiment of this application, two different voltage values and two different current values are obtained through sampling by using the sensor detection unit, and it may be determined, based on a change of the voltage value and a change of the current value, whether the insulation impedance of the combiner box to the ground is abnormal. This helps reduce complexity of an operation and reduce costs.

**[0115]** Two different types of logic are provided in this embodiment of this application to implement S801 and S802. Detailed descriptions are provided below with reference to FIG. 9 and FIG. 10.

**[0116]** For example, FIG. 9 is a schematic flowchart of another impedance detection method 900 according to an embodiment of this application. In the method 900, a power supply unit is in an off state at a first time point, and the power supply unit is in a working state at a second time point. It should be understood that in this embodiment, the power supply unit is an adjustable direct current power supply, and a relay K is an output relay. The method 900 includes the following steps.

**[0117]** S901: Connect outputs of M combiner boxes in parallel.

**[0118]** S902: Turn off the relay K, so that the adjustable direct current power supply is turned off.

**[0119]** In this embodiment of this application, the DC output relay K is not turned on, so that the power supply of the combiner box is in the off state at the first time point.

**[0120]** S903: For each combiner box, perform sampling to obtain a BUS-/PE voltage U<sub>1</sub>, and perform sampling by using an RCD leakage current sensor, to obtain a leakage current I<sub>rcd1</sub>.

**[0121]** In this embodiment of this application, at the first time point, for each combiner box, sampling is performed based on a differential resistor, to obtain the BUS-/PE voltage  $U_1$ , and sampling is performed by using the RCD leakage current sensor, to obtain the direct current leakage current  $I_{rcd1}$ .

**[0122]** S904: Turn on the relay K, so that the adjustable direct current power supply works, and the adjustable direct current power supply controls a BUS-/PE voltage to be  $U_2$ .

**[0123]** S905: For each combiner box, perform sampling to obtain the BUS-/PE voltage  $U_2$ , and perform sampling by using the RCD leakage current sensor, to obtain a leakage current  $I_{rcd2}$ .

**[0124]** In this embodiment of this application, before a second voltage value and a second current value are obtained, the power supply unit is turned on, so that the power supply unit is in the working state; and the voltage between a direct current bus and a protective earthing is adjusted to a first preset voltage by using the power supply unit.

**[0125]** In this embodiment of this application, the DC output relay K is turned on, the BUS-/PE voltage is first controlled to be the first preset voltage by using the adjustable direct current power supply, and after the first preset voltage is stable, the first preset voltage is sampled to obtain  $U_2$ .

**[0126]** In this embodiment of this application, the BUS-/PE voltage and a leakage current are sampled at the second time point, to obtain  $U_2$  and  $I_{rcd2}$ . The adjustable direct current power supply is in the working state at the second time point.

**[0127]** It should be understood that the first preset voltage is U<sub>2</sub>, the second voltage value is U<sub>2</sub> obtained through sampling, and the second current value is I<sub>rcd2</sub> obtained through sampling.

[0128] S906: Calculate an insulation impedance value of the combiner box to the ground PE based on  $\rm U_1, \, U_2, \, I_{rcd1}, \, and \, I_{rcd2}.$ 

**[0129]** A calculation manner in this embodiment of this application is described in S703. Details are not described herein.

**[0130]** In this embodiment of this application, the BUS-/PE voltage may be sampled once before an inverter is grid-connected, and then the BUS-/PE voltage may be sampled once again in a grid-connected working process of the inverter, to determine whether there is impedance abnormality. In this way, power can be saved, and whether the impedance of the combiner box is abnormal can be detected in a timely manner.

[0131] In the method 800, sampling is separately per-

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formed when an adjustable direct current power supply is in an off state and the adjustable direct current power supply is in a working state, to obtain  $U_1$ ,  $U_2$ ,  $I_{rcd1}$ , and  $I_{rcd2}$ . Another type of logic for implementing S801 and S802 is described below in detail with reference to FIG. 10

**[0132]** For example, FIG. 10 is a schematic flowchart of still another impedance detection method 1000 according to an embodiment of this application. In the method 1000, a power supply unit is in a working state at both a first time point and a second time point. It should be understood that in this embodiment, the power supply unit is an adjustable direct current power supply, and a relay K is an output relay. The method 1000 includes the following steps.

[0133] S1001: Connect outputs of M combiner boxes in parallel.

**[0134]** S1002: Turn on the relay K, so that the adjustable direct current power supply works, and the adjustable direct current power supply controls a BUS-/PE voltage to be  $U_1$ .

**[0135]** In this embodiment of this application, before a first voltage value and a first current value are obtained, the power supply unit is turned on, so that the power supply unit is in the working state; and the voltage between a direct current bus and a protective earthing is adjusted to a second preset voltage by using the power supply unit.

**[0136]** In this embodiment of this application, the DC output relay K is turned on, the BUS-/PE voltage is first controlled to be the second preset voltage by using the adjustable direct current power supply, and after the second preset voltage is stable, the second preset voltage is sampled at the first time point to obtain U<sub>1</sub>.

**[0137]** S1003: For each combiner box, perform sampling to obtain the BUS-/PE voltage  $U_1$ , and perform sampling by using an RCD leakage current sensor, to obtain a leakage current  $I_{rcd1}$ .

**[0138]** It should be understood that in this embodiment of this application, the second preset voltage is  $U_1$ , the first voltage value is  $U_1$  obtained through sampling, and the first current value is  $I_{rcd1}$  obtained through sampling. **[0139]** S1004: Turn on the relay K, so that the adjustable direct current power supply works, and the adjustable direct current power supply controls the BUS-/PE voltage to be  $U_2$ .

**[0140]** In this embodiment of this application, before a second voltage value and a second current value are obtained, the voltage between the direct current bus and the protective earthing is adjusted to a third preset voltage by using the power supply unit after first preset duration. **[0141]** In this embodiment of this application, the DC output relay K is turned on, the BUS-/PE voltage is first controlled to be the third preset voltage by using the adjustable direct current power supply, and after the third preset voltage is stable, the third preset voltage is sampled at the second time point to obtain U<sub>2</sub>.

[0142] After controlling the BUS-/PE voltage to be U<sub>1</sub>

for the first time, the adjustable direct current power supply may adjust the BUS-/PE voltage for the second time after the first preset duration. The first preset duration may be 5s, 10s, or any other duration. This is not limited in this embodiment of this application.

**[0143]** For example, in a process of adjusting the BUS-/PE voltage from  $U_1$  to  $U_2$ , a voltage change may be gradually controlled to adjust the voltage to  $U_2$ , or the voltage may be directly adjusted to  $U_2$ . This is not limited in this application.

**[0144]** S1005: For each combiner box, perform sampling to obtain the BUS-/PE voltage  $U_2$ , and perform sampling by using the RCD leakage current sensor, to obtain a leakage current  $I_{rcd2}$ .

**[0145]** It should be understood that in this embodiment of this application, the third preset voltage is U<sub>2</sub>, the second voltage value is U<sub>2</sub> obtained through sampling, and the second current value is I<sub>rcd2</sub> obtained through sampling.

**[0146]** S1006: Calculate an insulation impedance value of the combiner box to the ground PE based on  $U_1$ ,  $U_2$ ,  $I_{rcd1}$ , and  $I_{rcd2}$ .

**[0147]** In this embodiment of this application, the BUS-/PE voltage may be sampled twice online in a grid-connected running process of the inverter, to determine whether there is impedance abnormality. In this way, two different voltage values can be obtained by adjusting only the BUS-/PE voltage. An operation is simple, and an abnormal combiner box can be more effectively located.

**[0148]** It should be understood that a plurality of combiner boxes may be detected in parallel by using the method for detecting insulation impedance of a combiner box described above. This helps improve detection efficiency.

**[0149]** The method for detecting abnormal insulation impedance of a combiner box to the ground is described above in detail with reference to FIG. 8 to FIG. 10. In an actual detection process, the combiner box is connected to a plurality of photovoltaic strings. Therefore, in embodiments of this application, a specific photovoltaic string that has low insulation impedance to the ground in the abnormal combiner box may be further accurately detected. A method for detecting abnormality in a photovoltaic string is described below in detail with reference to FIG. 11.

**[0150]** For example, FIG. 11 is a schematic flowchart of yet another impedance detection method 1100 according to an embodiment of this application. The method 1100 includes the following steps.

**[0151]** S801: Sample a voltage between a direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value.

**[0152]** S802: Sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value.

[0153] S803: Calculate an insulation impedance value

of a combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

**[0154]** S1110: If it is determined that the impedance of the combiner box is abnormal, adjust a maximum power point tracking MPPT voltage corresponding to a photovoltaic string in the combiner box.

[0155] S1120: Sample the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value. [0156] It should be understood that an adjustable direct current power supply is in a working state at both the third time point and the fourth time point.

**[0157]** S1130: Sample the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value.

**[0158]** S1140: Calculate an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

**[0159]** In this embodiment of this application, the MPPT voltage is adjusted, and insulation impedance values  $R_{po}$  and  $R_{ne}$  of an MPPT voltage of each photovoltaic string to the ground may be calculated based on two different MPPT voltage values obtained through sampling. Herein,  $R_{po}$  is an insulation impedance value of a positive electrode of the MPPT voltage to the ground, and  $R_{ne}$  is an insulation impedance value of a negative electrode of the MPPT voltage to the ground. Therefore, a photovoltaic string corresponding to an MPPT voltage with low insulation impedance can be detected. In this way, an operation is simple, a detection result is efficient and accurate, and it is easy to implement.

**[0160]** In an optional embodiment, before the first MPPT voltage value and the third voltage value are obtained, the MPPT voltage is adjusted to a fourth preset voltage by using a DC-DC boost unit.

**[0161]** In an optional embodiment, before the second MPPT voltage value and the fourth voltage value are obtained, the MPPT voltage may be adjusted to a fifth preset voltage by using the DC-DC boost unit after second preset duration.

[0162] In this embodiment of this application, for a combiner box with an insulation impedance problem, changes of all MPPT voltages may be sequentially controlled by using an internal DC-DC boost unit, to calculate insulation impedance  $R_{po}$  and  $R_{ne}$  of a positive electrode and a negative electrode of each MPPT to the ground. This helps accurately locate a photovoltaic string with abnormal insulation impedance in a scenario in which combiner boxes are connected in parallel, and ensures that grid connection is normally performed.

**[0163]** Calculation of the insulation impedance value of the MPPT voltage to the ground is analyzed by using

a first photovoltaic string as an example. An MPPT voltage corresponding to the photovoltaic string is first controlled to be the fourth preset voltage  $U_{pv11}$  by using the DC-DC boost unit, to obtain that the BUS-/PE voltage is  $U_{11}$ , then the MPPT voltage is controlled to be the fifth preset voltage  $U_{pv12}$ , to obtain that the BUS-/PE voltage is  $U_{12}$ , and then insulation impedance  $R_{po1}$  and  $R_{ne1}$  of the MPPT voltage corresponding to the single photovoltaic string to the ground PE may be obtained through solving based on the following equation set:

$$U_{11} = (U_{pv11} \times R_{ne1})/(R_{po1} + R_{ne1})$$

$$U_{12}=(U_{pv12}\times R_{ne1})/(R_{po1}+R_{ne1})$$

[0164] In the formulas,  $U_{11}$ ,  $U_{12}$ ,  $U_{pv11}$ , and  $U_{pv12}$  are known. Therefore,  $R_{po1}$  and  $R_{ne1}$  can be obtained through solving. Negative electrodes of a plurality of MPPTs are connected in parallel, and therefore a specific MPPT voltage whose negative electrode has abnormal insulation impedance to the ground cannot be determined. However, positive electrodes of the MPPTs can be distinguished. Therefore, when  $R_{po1}$  is less than a safety threshold, it may be determined that insulation impedance of the first photovoltaic string is abnormal. For example, the safety threshold may be 50 K $\Omega$ .

**[0165]** It should be understood that for a combiner box with abnormal insulation impedance, insulation impedance values of MPPT voltages corresponding to other photovoltaic strings to the ground are sequentially detected. A detection method is the same as the foregoing method. Details are not described herein.

**[0166]** Optionally, after S1140, if it is determined that there is a combiner box with abnormal insulation impedance in a photovoltaic system, an instruction is sent to a monitoring board in an inverter, where the instruction is used to indicate that the combiner box has low insulation impedance, a control trip of an input direct current switch of the combiner box with low insulation impedance is turned off, and another combiner box with normal insulation impedance may be put into grid connection. In addition, a background person may receive a low insulation impedance alarm through an application (application, APP), and rectify a fault in a timely manner.

**[0167]** In FIG. 4A, FIG. 4B, FIG. 4C, and FIG. 5, the impedance detection apparatus is connected to the direct current bus, and an output end of the power supply unit 404 is connected to the negative direct current bus. However, it should be understood that connection positions of the sensor detection unit 403 and the power supply unit 404 in a circuit may be changed. Five different circuit topology change forms of the photovoltaic system are described below by still using the combiner box 1 as an example and with reference to FIG. 12 to FIG. 16.

[0168] In photovoltaic systems 1200, 1300, 1400, 1500, and 1600, for example, the combiner box 1 in-

cludes N direct current switches, N DC-DC boost units, an impedance detection apparatus, and a power supply unit.

**[0169]** It should be understood that although a circuit structure changes, the impedance detection methods 800, 900, 1000, and 1100 in embodiments of this application may still be applied to any one of the circuits 1200, 1300, 1400, 1500, and 1600. This is not limited in this application.

**[0170]** As shown in FIG. 12, in the photovoltaic system 1200, an input end of the direct current switch is connected to a photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, an output end of the DC-DC boost unit is connected to an input end of the impedance detection apparatus, and an output end of the impedance detection apparatus is connected to a direct current bus and is located inside the power supply unit.

**[0171]** Different from a case in which the power supply unit in the photovoltaic system 500 is connected to the negative direct current bus BUS-, the power supply unit in the circuit 1200 is connected to a positive direct current bus BUS+.

**[0172]** As shown in FIG. 13, in the circuit 1300, an input end of the direct current switch is connected to a photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, and an output end of the DC-DC boost unit is connected to a direct current bus.

**[0173]** Different from a case in which the power supply unit in the photovoltaic system 500 is connected to the negative direct current bus BUS-, and the impedance detection apparatus is connected to a direct current bus inside the power supply unit, the power supply unit in the photovoltaic system 1300 is connected to a positive direct current bus BUS+, and the impedance detection apparatus is connected to a direct current bus outside the power supply unit.

[0174] As shown in FIG. 14, in the photovoltaic system 1400, an input end of the direct current switch is connected to a photovoltaic string, an output end of the direct current switch is connected to an input end of the impedance detection apparatus, an output end of the impedance detection apparatus is connected to an input end of the DC-DC boost unit, and an output end of the DC-DC boost unit is connected to a direct current bus.

[0175] Different from a case in which the power supply unit in the photovoltaic system 500 is connected to the negative direct current bus BUS-, and the impedance detection apparatus is connected to a direct current bus inside the power supply unit, the power supply unit in the photovoltaic system 1400 is connected to a positive direct current bus BUS+, and the impedance detection apparatus is connected between the direct current switch and the DC-DC boost unit.

**[0176]** As shown in FIG. 15, in the photovoltaic system 1500, an input end of the impedance detection apparatus is connected to a photovoltaic string, an output end of

the impedance detection apparatus is connected to an input end of the direct current switch, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, and an output end of the DC-DC boost unit is connected to a direct current bus.

[0177] Different from a case in which the power supply unit in the photovoltaic system 500 is connected to the negative direct current bus BUS-, and the impedance detection apparatus is connected to a direct current bus inside the power supply unit, the power supply unit in the photovoltaic system 500 is connected to a positive direct current bus BUS+, and the impedance detection apparatus is connected before the direct current switch.

[0178] As shown in FIG. 16, in the circuit 1600, an input end of the direct current switch is connected to a photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, an output end of the DC-DC boost unit is connected to an input end of the impedance detection apparatus, and an output end of the impedance detection apparatus is connected to a direct current bus and is located inside the power supply unit.

**[0179]** Different from a case in which the power supply unit in the photovoltaic system 500 is located inside the combiner box and is connected to the negative direct current bus BUS-, the power supply unit in the photovoltaic system 1600 is located inside an inverter and is connected to a negative direct current bus BUS-.

**[0180]** It should be understood that the circuit change manners in the photovoltaic system 500 and the photovoltaic systems 1200 to 1600 may be combined with each other.

**[0181]** Optionally, on the basis of the photovoltaic system 500, the impedance detection apparatus may be connected to the direct current bus outside the power supply unit with reference to a connection manner of the photovoltaic system 1300.

**[0182]** Optionally, on the basis of the photovoltaic system 500, the impedance detection apparatus may be connected between the direct current switch and the DC/DC boost unit with reference to a connection manner of the photovoltaic system 1400.

**[0183]** Optionally, on the basis of the photovoltaic system, the impedance detection apparatus may be connected before the direct current switch with reference to a connection manner of the photovoltaic system.

**[0184]** Optionally, on the basis of the photovoltaic system 1600, the impedance detection apparatus may be connected between the direct current switch and the DC/DC boost unit with reference to a connection manner of the photovoltaic system 1400.

[0185] Optionally, on the basis of the photovoltaic system 1600, the impedance detection apparatus may be connected before the direct current switch with reference to a connection manner of the photovoltaic system 1500. [0186] It should be understood that there may be another possible combination manner. This is not limited in this embodiment of this application.

**[0187]** The impedance detection method according to embodiments of this application is described above in detail with reference to FIG. 8 to FIG. 11. An impedance detection apparatus according to embodiments of this application is described below in detail with reference to FIG. 17 and FIG. 18.

[0188] FIG. 17 is a schematic block diagram of an impedance detection apparatus 1700 according to an embodiment of this application. The apparatus 1700 includes a sampling module 1710 and a processing module 1720. The sampling module 1710 is equivalent to the sensor detection unit in the foregoing photovoltaic system, and the processing module 1720 is equivalent to the processing unit in the foregoing photovoltaic system. [0189] The sampling module 1710 is configured to: sample a voltage between a direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; and sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value. The processing module 1720 is configured to calculate an insulation impedance value of a combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box is abnormal.

[0190] Optionally, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in an off state at the first time point, and the power supply unit is in a working state at the second time point. [0191] Optionally, the processing module 1720 is configured to: turn on the power supply unit before the second voltage value and the second current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a first preset voltage. [0192] Optionally, the photovoltaic system further includes a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.

**[0193]** Optionally, the processing module 1720 is configured to: turn on the power supply unit before the first voltage value and the first current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a second preset voltage.

**[0194]** Optionally, the processing module 1720 is configured to adjust the voltage between the direct current bus and the protective earthing to a third preset voltage after first preset duration.

**[0195]** Optionally, the processing module 1720 is configured to: if it is determined that the impedance of the

combiner box is abnormal, adjust a maximum power point tracking MPPT voltage corresponding to a photovoltaic string in the combiner box; the sampling module 1710 is configured to sample the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value; the sampling module 1710 is further configured to sample the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value; and the processing module 1720 is further configured to calculate an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnor-

**[0196]** Optionally, before the first MPPT voltage and the third voltage value are obtained, the processing module 1720 is configured to adjust the MPPT voltage to a fourth preset voltage.

**[0197]** Optionally, the processing module 1720 is configured to adjust the MPPT voltage to a fifth preset voltage after second preset duration.

**[0198]** Optionally, the power supply unit includes at least one of a relay, a resistor, a diode, or a direct current power supply.

[0199] It should be understood that the apparatus 1700 herein is embodied in a form of a functional module. The term "module" herein may be an application-specific integrated circuit (application specific integrated circuit, ASIC), an electronic circuit, a processor (for example, a shared processor, a dedicated processor, or a group processor) configured to execute one or more software or firmware programs, a memory, a combinational logic circuit, and/or another appropriate component that supports the described function. In an optional example, a person skilled in the art may understand that the apparatus 1700 may be an electronic device, or may be a chip or a chip system in an electronic device, or a function of a chip or a chip system in an electronic device may be integrated into the apparatus 1700, and the apparatus 1700 may be configured to perform the procedures and/or steps of any impedance detection method in the foregoing method embodiments. To avoid repetition, details are not described herein.

**[0200]** The apparatus 1700 has a function for implementing the corresponding steps performed by the impedance detection apparatus in the foregoing method. The function may be implemented by hardware, or may be implemented by executing corresponding software by hardware. The hardware or the software includes one or more modules corresponding to the function.

**[0201]** In this embodiment of this application, the apparatus 1700 in FIG. 17 may alternatively be a chip or a chip system, for example, a system on chip (system on chip, SoC).

[0202] FIG. 18 is a schematic block diagram of another

impedance detection apparatus 1800 according to an embodiment of this application. The apparatus 1800 includes a processor 1810, a transceiver 1820, and a memory 1830. The processor 1810, the transceiver 1820, and the memory 1830 communicate with each other by using an internal connection path. The memory 1830 is configured to store instructions. The processor 1810 is configured to execute the instructions stored in the memory 1830, to control the transceiver 1820 to send a signal and/or receive a signal.

[0203] It should be understood that the apparatus 1800 may be specifically the impedance detection apparatus in the foregoing embodiment, or a function of the impedance detection apparatus in the foregoing embodiment may be integrated into the apparatus 1800, and the apparatus 1800 may be configured to perform the steps and/or procedures corresponding to the impedance detection apparatus in the foregoing method embodiments. Optionally, the memory 1830 may include a read-only memory and a random access memory, and provide instructions and data to the processor. A part of the memory may further include a nonvolatile random access memory. For example, the memory may further store information of a device type. The processor 1810 may be configured to execute the instructions stored in the memory, and when the processor executes the instructions, the processor can perform the steps and/or procedures corresponding to the impedance detection apparatus in the foregoing method embodiments.

**[0204]** It should be understood that in this embodiment of this application, the processor 1810 may be a central processing unit (central processing unit, CPU), or the processor may be another general-purpose processor, a digital signal processor (digital signal processor, DSP), an application-specific integrated circuit (application specific integrated circuit, ASIC), a field programmable gate array (field programmable gate array, FPGA) or another programmable logic device, a discrete gate or a transistor logic device, a discrete hardware component, or the like. The general-purpose processor may be a microprocessor, or the processor may be any conventional processor or the like.

[0205] In an implementation process, the steps of the foregoing methods may be completed by using an integrated logic circuit of hardware in the processor, or by using instructions in a form of software. The steps of the methods disclosed with reference to embodiments of this application may be directly performed and completed by a hardware processor, or may be performed and completed by using a combination of hardware in the processor and a software module. The software module may be located in a mature storage medium in the art, for example, a random access memory, a flash memory, a read-only memory, a programmable read-only memory, an electrically erasable programmable memory, or a register. The storage medium is located in the memory, and the processor executes the instructions in the memory, and completes the steps of the foregoing methods in

combination with the hardware in the processor. To avoid repetition, details are not described herein.

[0206] A person of ordinary skill in the art may be aware that the example modules, algorithms, and steps described with reference to embodiments disclosed in this specification can be implemented by electronic hardware or a combination of computer software and electronic hardware. Whether the functions are performed by hardware or software depends on particular applications and design constraints of the technical solutions. A person skilled in the art may use different methods to implement the described functions for each particular application, but it should not be considered that the implementation goes beyond the scope of this application.

**[0207]** It may be clearly understood by a person skilled in the art that for the purpose of convenient and brief description, for a detailed working process of the system, apparatus, and module described above, refer to the corresponding process in the foregoing method embodiments. Details are not described herein.

[0208] In the several embodiments provided in this application, it should be understood that the disclosed system, apparatus, and method may be implemented in other manners. For example, the described apparatus embodiment is merely an example. For example, division into the modules is merely logical function division and may be other division in actual implementation. For example, a plurality of modules or components may be combined or integrated into another system, or some features may be ignored or not performed. In addition, the displayed or discussed mutual couplings or direct couplings or communication connections may be implemented by using some interfaces. The indirect couplings or communication connections between the apparatuses or modules may be implemented in electrical, mechanical, or other forms.

**[0209]** The modules described as separate parts may or may not be physically separate, and parts displayed as modules may or may not be physical modules, that is, may be located at one position, or may be distributed on a plurality of network modules. Some or all of the modules may be selected based on actual requirements to achieve the objectives of the solutions of embodiments. In addition, functional modules in embodiments of this application may be integrated into one processing module, each module may exist alone physically, or two or more modules may be integrated into one module.

[0210] When the functions are implemented in a form of a software functional module and sold or used as an independent product, the functions may be stored in a computer-readable storage medium. Based on such an understanding, the technical solutions of this application essentially, or the part contributing to the conventional technology, or some of the technical solutions may be implemented in a form of a software product. The computer software product is stored in a storage medium, and includes several instructions for instructing a computer device (which may be a personal computer, a serv-

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er, a network device, or the like) to perform all or some of the steps of the methods described in embodiments of this application. The storage medium includes any medium that can store program code, for example, a USB flash drive, a removable hard disk, a read-only memory (read-only memory, ROM), a random access memory (random access memory, RAM), a magnetic disk, or an optical disc.

**[0211]** The foregoing descriptions are merely specific implementations of this application, but the protection scope of this application is not limited thereto. Any variation or replacement readily figured out by a person skilled in the art within the technical scope disclosed in this application shall fall within the protection scope of this application. Therefore, the protection scope of the claims.

#### Claims

1. An impedance detection method, applied to a photovoltaic system, wherein the photovoltaic system comprises a plurality of combiner boxes and an inverter, an input end of each of the plurality of combiner boxes is connected to a photovoltaic string, output ends of the plurality of combiner boxes are connected to the inverter by using a direct current bus, the combiner box comprises at least one direct current switch and at least one DC-DC boost unit, an input end of the direct current switch is connected to the photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, an output end of the DC-DC boost unit is connected to the direct current bus, and the method comprises:

bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; sampling a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value; and calculating an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value, to detervalue and the second current value, to deter-

mine whether impedance of the combiner box

sampling a voltage between the direct current

2. The method according to claim 1, wherein the photovoltaic system further comprises a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in

is abnormal.

an off state at the first time point, and the power supply unit is in a working state at the second time point.

3. The method according to claim 2, wherein before the obtaining a second voltage value and the obtaining a second current value, the method further comprises:

turning on the power supply unit, so that the power supply unit is in the working state; and adjusting the voltage between the direct current bus and the protective earthing to a first preset voltage by using the power supply unit.

- 15 4. The method according to claim 1, wherein the photovoltaic system further comprises a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.
  - **5.** The method according to claim 4, wherein before the obtaining a first voltage value and the obtaining a first current value, the method further comprises:

turning on the power supply unit, so that the power supply unit is in the working state; and adjusting the voltage between the direct current bus and the protective earthing to a second preset voltage by using the power supply unit.

- 6. The method according to claim 5, wherein before the obtaining a second voltage value and the obtaining a second current value, the method further comprises: adjusting the voltage between the direct current bus and the protective earthing to a third preset voltage by using the power supply unit after first preset duration.
- 7. The method according to any one of claims 1 to 6, wherein the method further comprises:

if it is determined that the impedance of the combiner box is abnormal, adjusting a maximum power point tracking MPPT voltage corresponding to the photovoltaic string in the combiner box; sampling the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value;

sampling the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value: and

calculating an insulation impedance value of the

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MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

8. The method according to claim 7, wherein before the obtaining a first MPPT voltage value and the obtaining a third voltage value, the method further comprises:

adjusting the MPPT voltage to a fourth preset voltage by using the DC-DC boost unit.

- 9. The method according to claim 8, wherein before the obtaining a second MPPT voltage value and the obtaining a fourth voltage value, the method further comprises: adjusting the MPPT voltage to a fifth preset voltage
  - adjusting the MPPT voltage to a fifth preset voltage by using the DC-DC boost unit after second preset duration.
- 10. The method according to any one of claims 1 to 9, wherein the power supply unit comprises at least one of a relay, a resistor, a diode, or a direct current power supply.
- 11. A photovoltaic system, comprising a plurality of combiner boxes, an inverter, and an impedance detection apparatus, wherein an input end of each of the plurality of combiner boxes is connected to a photovoltaic string, output ends of the plurality of combiner boxes are connected to the inverter by using a direct current bus, the combiner box comprises at least one direct current switch and at least one DC-DC boost unit, an input end of the direct current switch is connected to the photovoltaic string, an output end of the direct current switch is connected to an input end of the DC-DC boost unit, an output end of the DC-DC boost unit is connected to an input end of the impedance detection apparatus, and an output end of the impedance detection apparatus is connected to the direct current bus; and

the impedance detection apparatus is configured to: sample a voltage between the direct current bus and a protective earthing at each of a first time point and a second time point, to obtain a first voltage value and a second voltage value; and sample a leakage current on the direct current bus at each of the first time point and the second time point, to obtain a first current value and a second current value; and

calculate an insulation impedance value of the combiner box based on a difference between the first voltage value and the second voltage value and a difference between the first current value and the second current value, to determine whether impedance of the combiner box

is abnormal.

- 12. The photovoltaic system according to claim 11, wherein the photovoltaic system further comprises a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, the power supply unit is in an off state at the first time point, and the power supply unit is in a working state at the second time point.
- **13.** The photovoltaic system according to claim 12, wherein the impedance detection apparatus is configured to:

turn on the power supply unit before the second voltage value and the second current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a first preset voltage.

- 14. The photovoltaic system according to claim 11, wherein the photovoltaic system further comprises a power supply unit, one end of the power supply unit is grounded, the other end of the power supply unit is connected to the direct current bus, and the power supply unit is in a working state at both the first time point and the second time point.
- 15. The photovoltaic system according to claim 14, wherein the impedance detection apparatus is configured to:

turn on the power supply unit before the first voltage value and the first current value are obtained, so that the power supply unit is in the working state; and adjust the voltage between the direct current bus and the protective earthing to a second preset voltage.

- **16.** The photovoltaic system according to claim 15, wherein the impedance detection apparatus is configured to: adjust the voltage between the direct current bus and the protective earthing to a third preset voltage after first preset duration.
- 17. The photovoltaic system according to any one of claims 11 to 16, wherein the impedance detection apparatus is configured to:

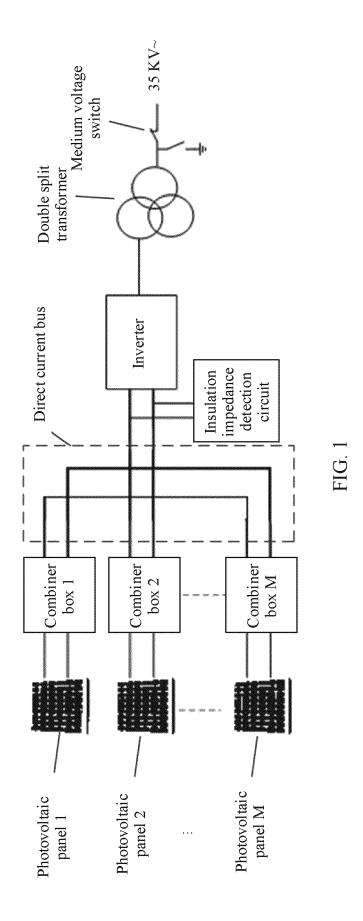
if it is determined that the impedance of the combiner box is abnormal, adjust a maximum power point tracking MPPT voltage corresponding to the photovoltaic string in the combiner box;

sample the MPPT voltage at each of a third time point and a fourth time point, to obtain a first MPPT voltage value and a second MPPT voltage value; and sample the voltage between the direct current bus and the protective earthing at each of the third time point and the fourth time point, to obtain a third voltage value and a fourth voltage value; and calculate an insulation impedance value of the MPPT voltage based on the first MPPT voltage value, the second MPPT voltage value, the third voltage value, and the fourth voltage value, to determine whether impedance of the photovoltaic string is abnormal.

- **18.** The photovoltaic system according to claim 17, wherein the impedance detection apparatus is configured to:
  - adjust the MPPT voltage to a fourth preset voltage before the first MPPT voltage and the third voltage value are obtained.

19. The photovoltaic system according to claim 18, wherein the impedance detection apparatus is configured to: adjust the MPPT voltage to a fifth preset voltage after second preset duration.

**20.** The photovoltaic system according to any one of claims 11 to 19, wherein the power supply unit comprises at least one of a relay, a resistor, a diode, or a direct current power supply.



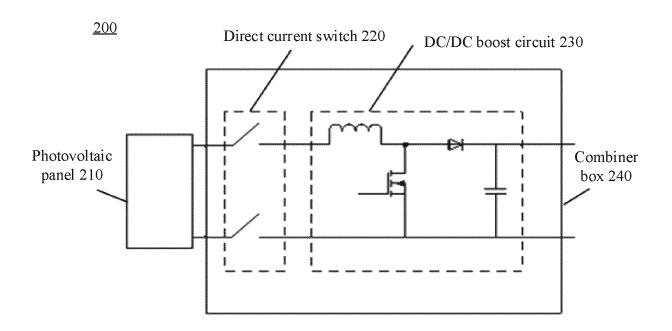
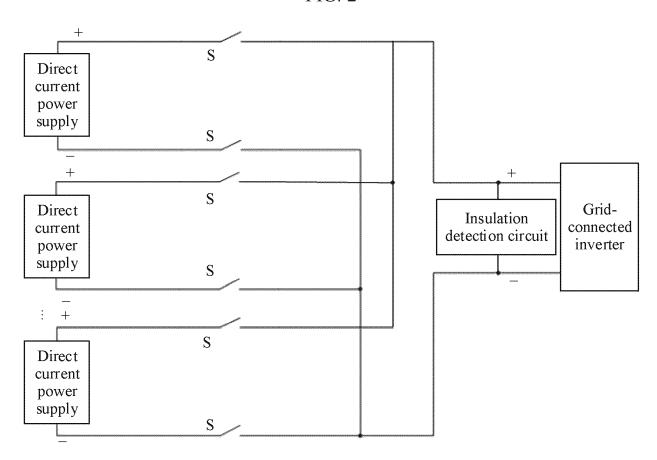
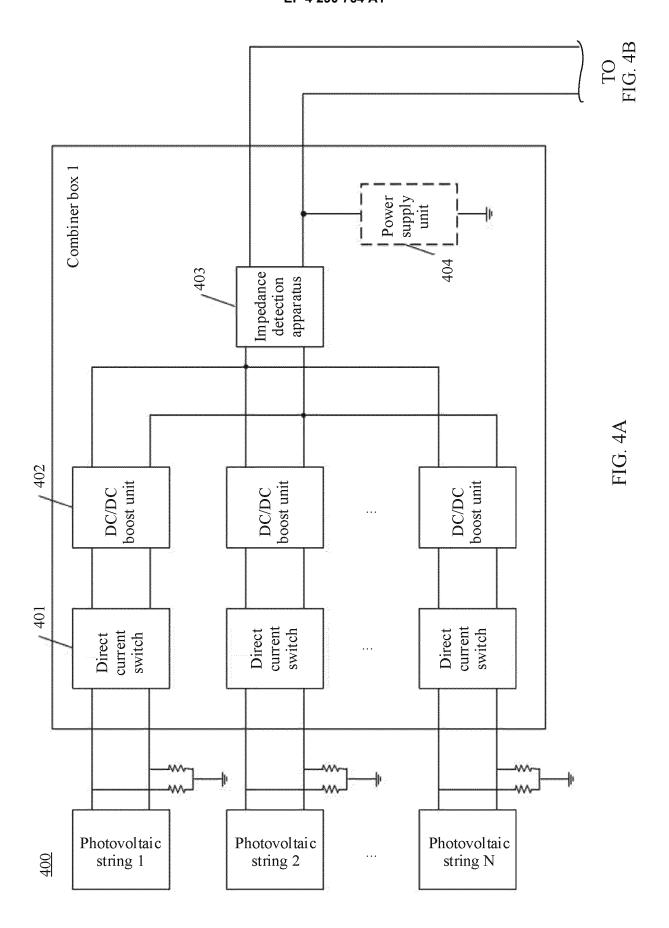
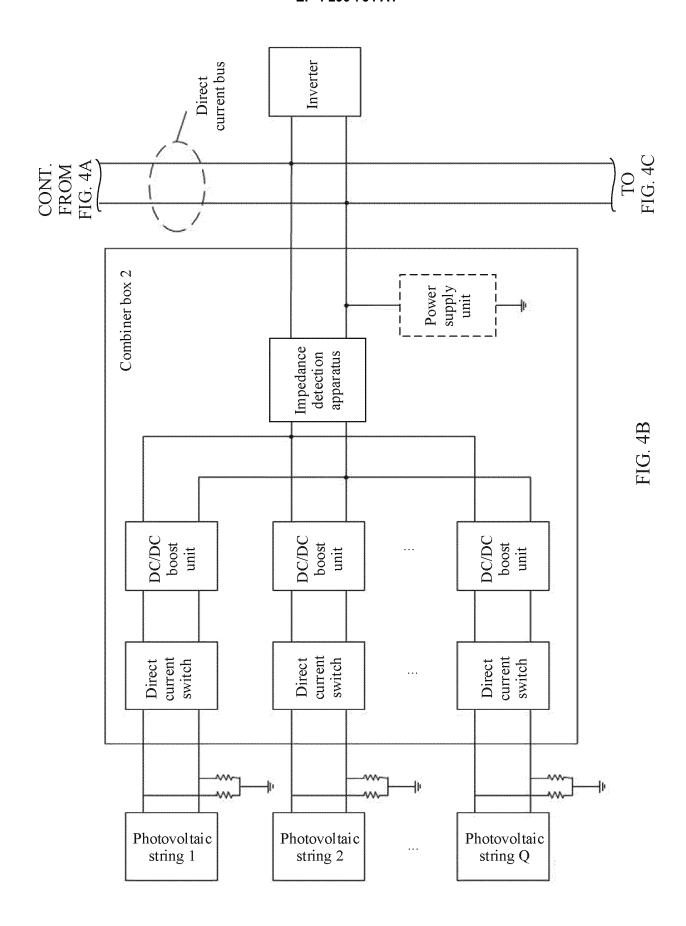
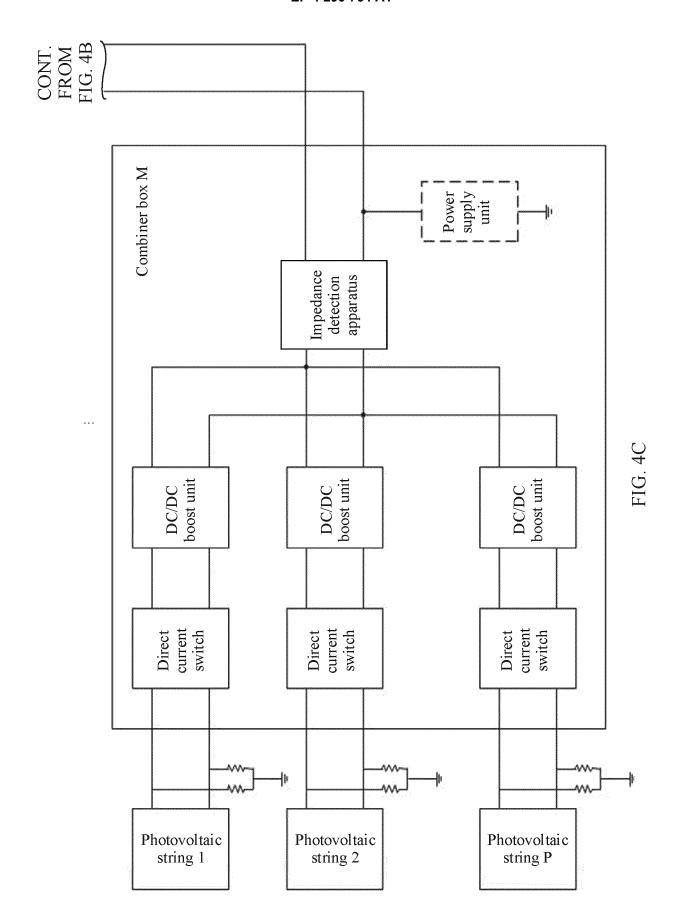


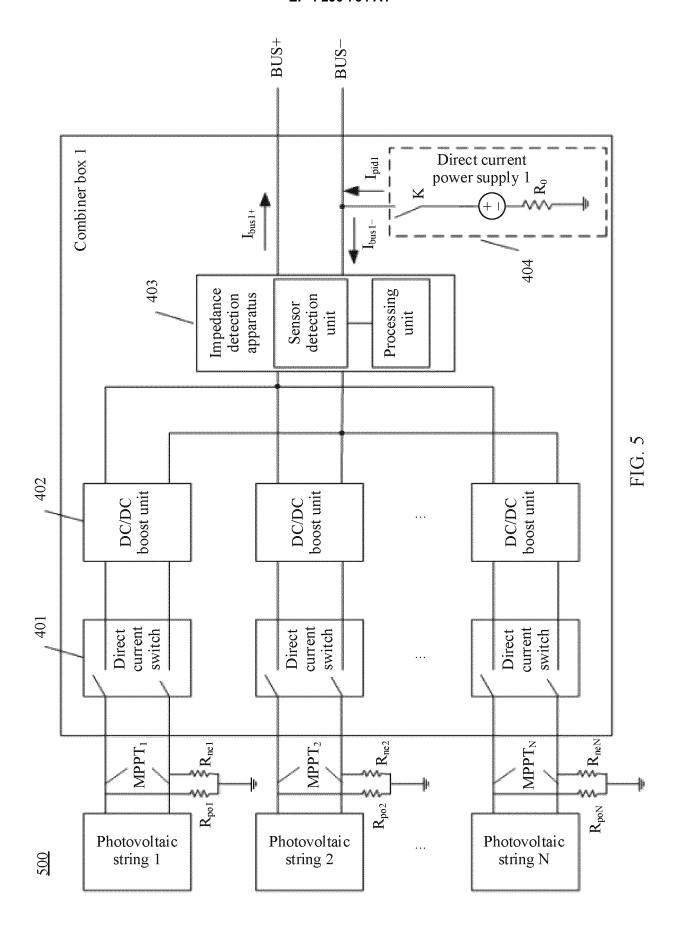
FIG. 2











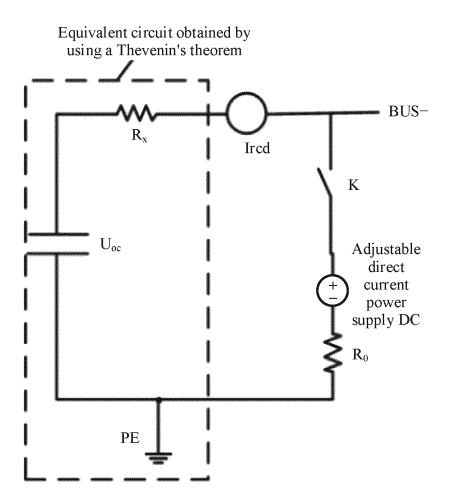


FIG. 6

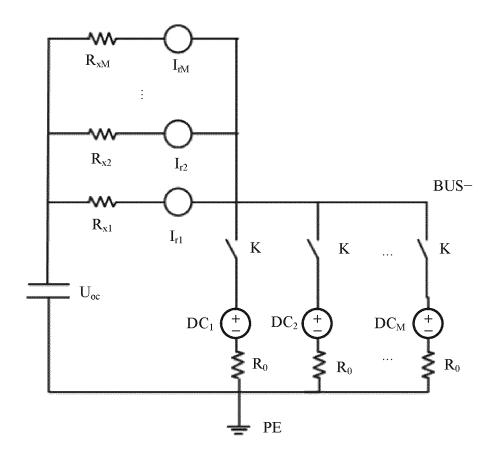


FIG. 7

<u>800</u>

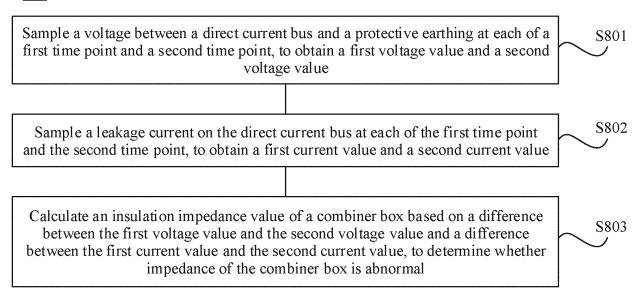


FIG. 8

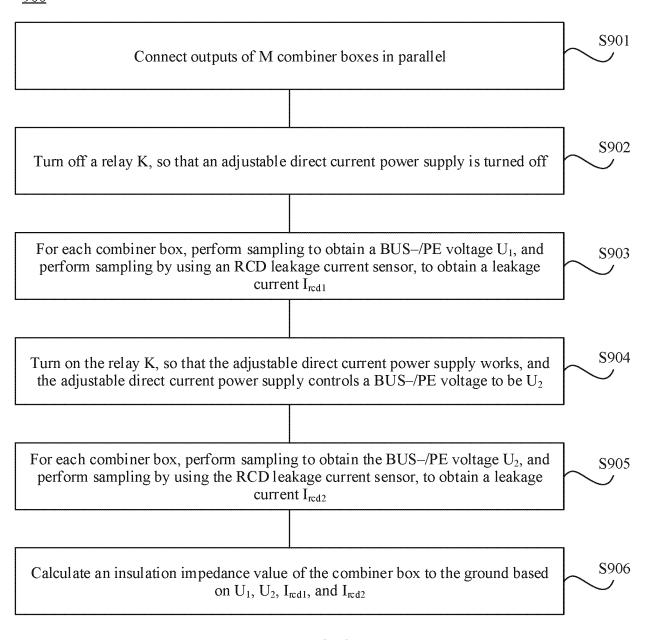


FIG. 9

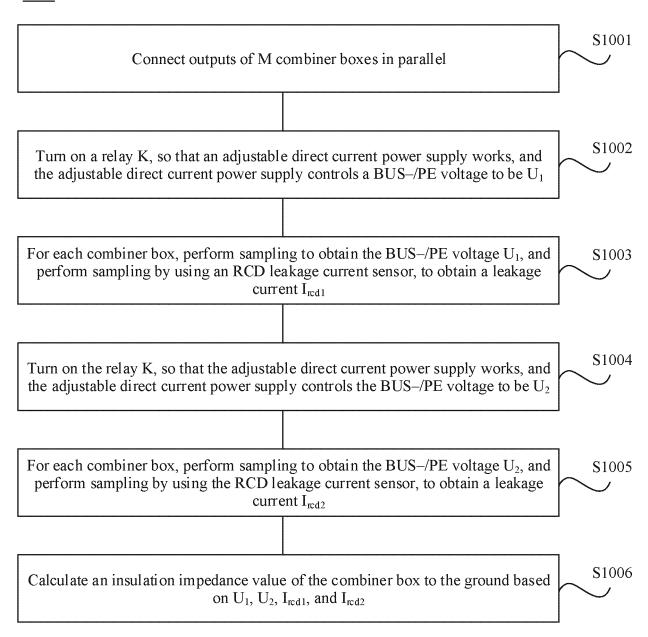


FIG. 10

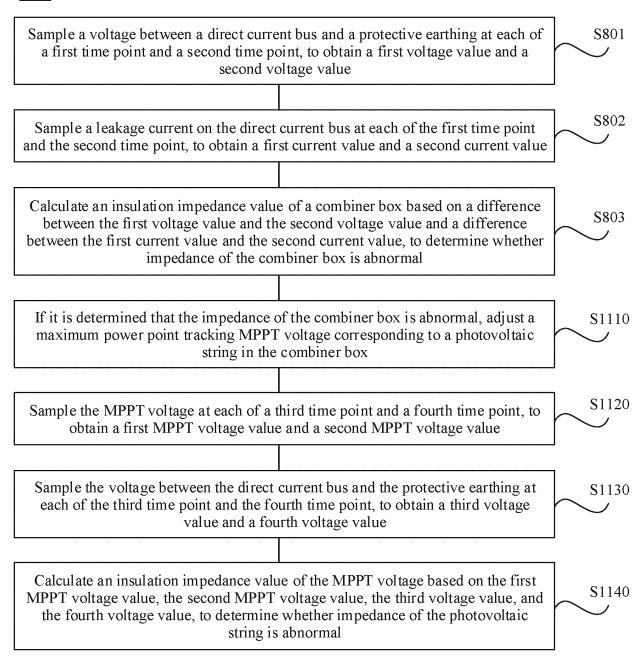
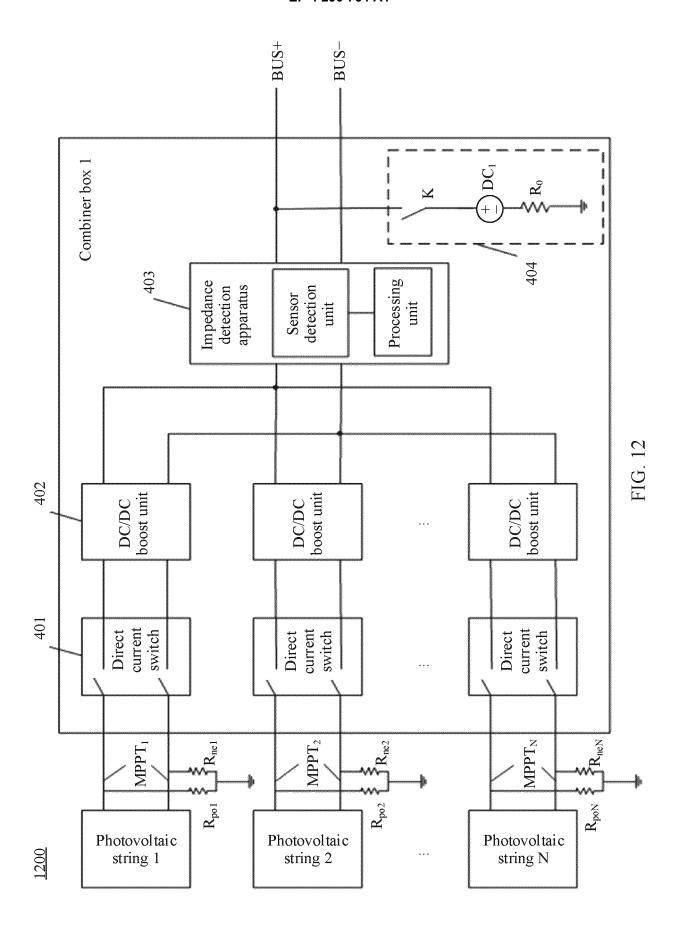
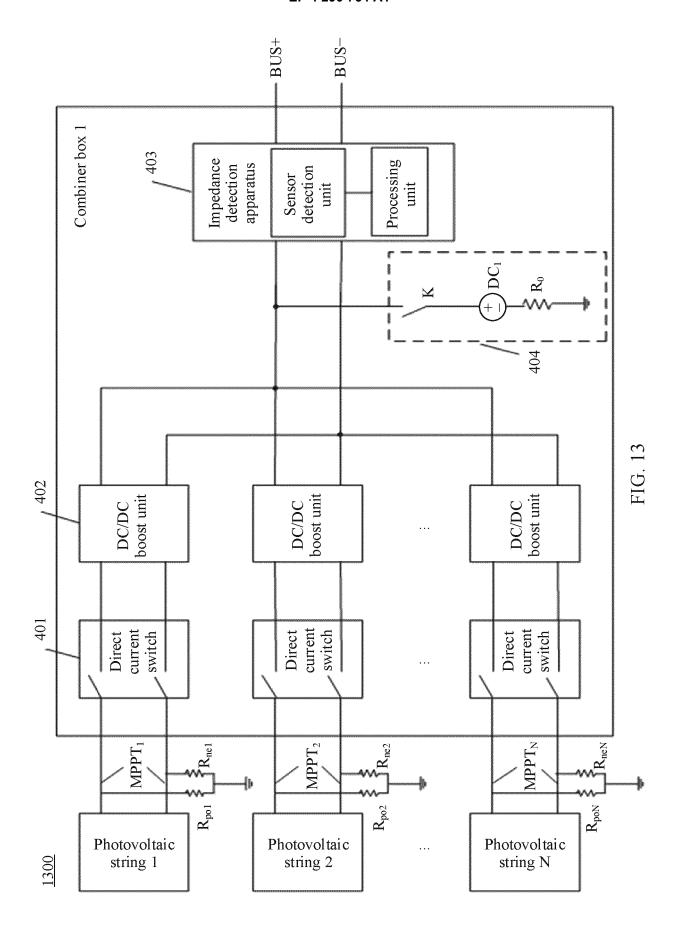
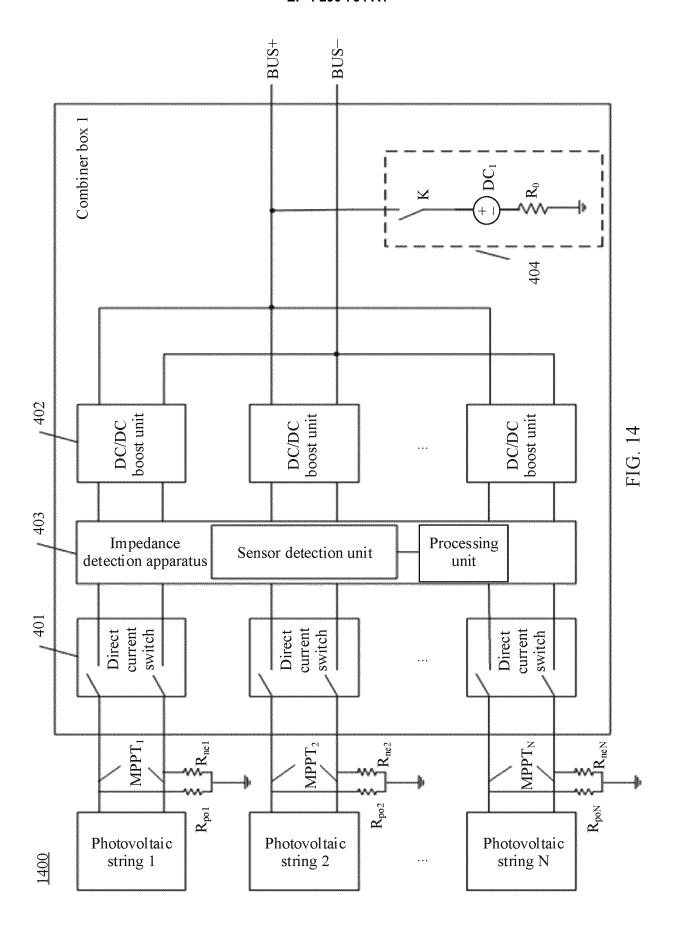
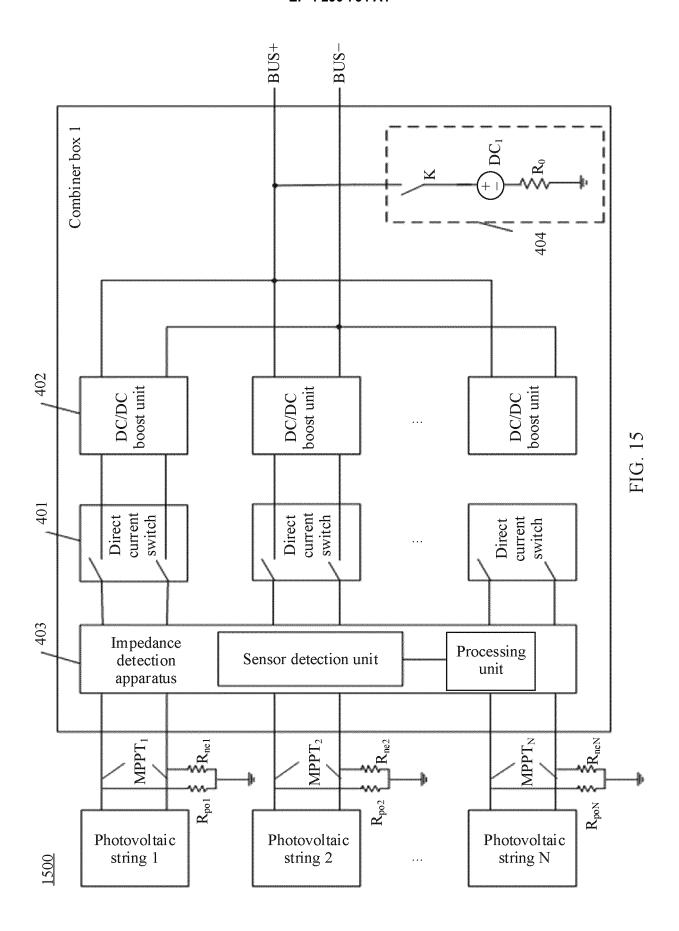


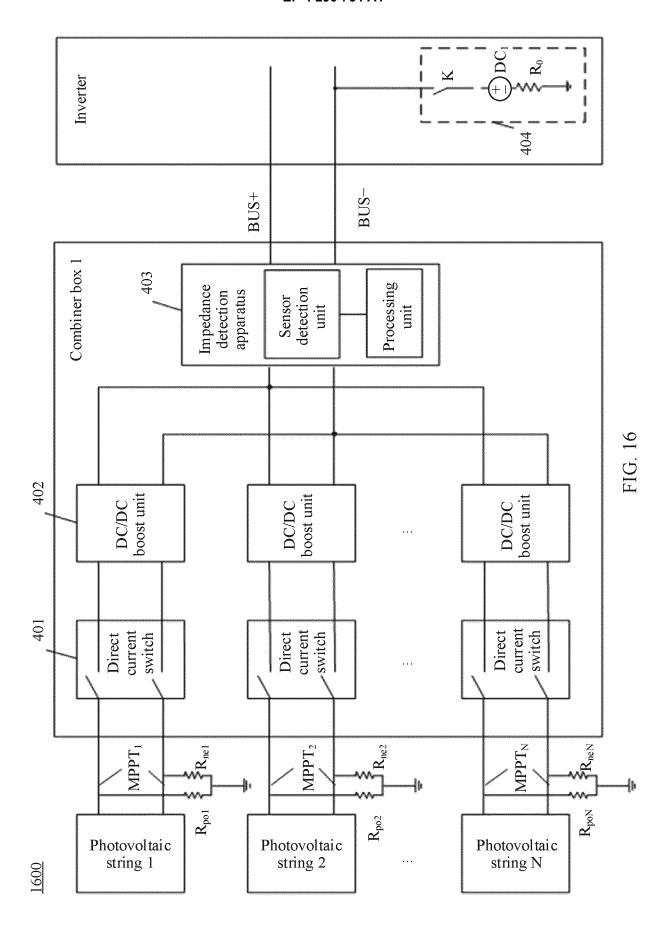
FIG. 11











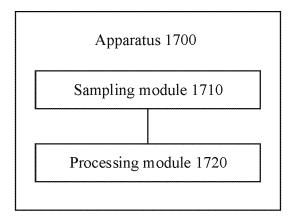


FIG. 17

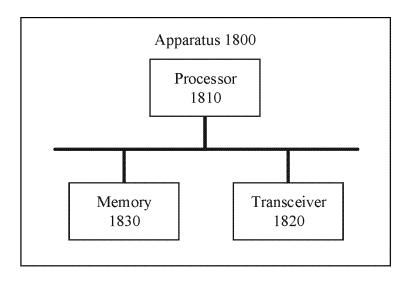


FIG. 18

International application No.

INTERNATIONAL SEARCH REPORT

PCT/CN2021/075168 5 CLASSIFICATION OF SUBJECT MATTER H02S 50/00(2014.01)i; G01R 27/18(2006.01)i According to International Patent Classification (IPC) or to both national classification and IPC FIELDS SEARCHED 10 Minimum documentation searched (classification system followed by classification symbols) H02S: G01R Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched 15 Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) CNABS, CNTXT, VEN, USTXT, EPTXT, WOTXT, CNKI: 绝缘阻抗, 绝缘电阻, 绝缘检测, 阻抗检测, 电阻检测, 母线, 总 线, 汇流箱, 光伏, 最大功率, 漏电流, 电压, MPPT, BUS?, ins?lation impedance?, resistance?, leakage current, combiner box??, photovoltaic, voltage DOCUMENTS CONSIDERED TO BE RELEVANT 20 Category\* Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No. CN 109600115 A (FONRICH (SHANGHAI) NEW ENERGY TECHNOLOGY CO., LTD.) 09 1-6, 10-16, 20 April 2019 (2019-04-09) description, paragraphs [0004]-[0063], and figures 1-8 25 Y US 2017343593 A1 (SMA SOLAR TECHNOLOGY AG.) 30 November 2017 (2017-11-30) 1-6, 10-16, 20 claims 1-12, description paragraphs [0019]-[0029], figures 1-3 Α CN 106992550 A (SUNGROW POWER SUPPLY CO., LTD.) 28 July 2017 (2017-07-28) 1-20 entire document A CN 109696582 A (SUNWODA ELECTRONIC CO., LTD.) 30 April 2019 (2019-04-30) 1-20 entire document 30 CN 109725202 A (KEHUA HENGSHENG CO., LTD. et al.) 07 May 2019 (2019-05-07) 1 - 20Α entire document Α CN 105606897 A (HAMI INNOPOWER TECHNOLOGY CO., LTD.) 25 May 2016 1 - 20(2016-05-25)entire document 35 Α CN 111610411 A (HUAWEI TECHNOLOGIES CO., LTD.) 01 September 2020 (2020-09-01) 1-20 entire document Further documents are listed in the continuation of Box C. ✓ See patent family annex. later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention Special categories of cited documents document defining the general state of the art which is not considered to be of particular relevance 40 earlier application or patent but published on or after the international filing date document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) when the document is taken alone document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art document referring to an oral disclosure, use, exhibition or other 45 document published prior to the international filing date but later than the priority date claimed document member of the same patent family Date of the actual completion of the international search Date of mailing of the international search report 27 September 2021 18 October 2021 Name and mailing address of the ISA/CN Authorized officer 50 China National Intellectual Property Administration (ISA/ CN) No. 6, Xitucheng Road, Jimenqiao, Haidian District, Beijing 100088, China Facsimile No. (86-10)62019451 Telephone No

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