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H01L 29/06 (2006.01) H01L 29/66 (2006.01)

H01L 29/775 (2006.01)

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(54) INTEGRATED CIRCUIT DEVICE WITH BACKSIDE FIN TRIM ISOLATION

An IC device includes a backside fin trim isolation (FTI, 150) separating a first transistor (170A) from a second transistor (170B). The FTI may be between a source region of the first transistor and a drain region of the second transistor. The source region of the first transistor and the drain region of the second transistor may be different portions of a semiconductor structure, e.g., a fin or nanoribbon. The IC device may also include a frontside metal layer (125). The semiconductor structure has a first surface and a second surface opposing the first surface. The first surface of the semiconductor structure is closer to the metal layer and may be larger than the second surface of the semiconductor structure. The FTI may has a first surface and a second surface opposing the first surface. The first surface of the FTI is closer to the metal layer but may be smaller than the second surface of the FTI.

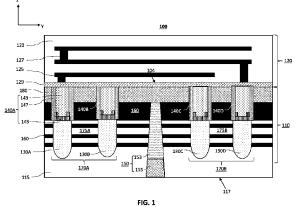


FIG. 1

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[0049]; figures 2B, 2C, 3A *

AL) 28 July 2016 (2016-07-28) * paragraph [0016]; figure 1B *



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EUROPEAN SEARCH REPORT

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CLASSIFICATION OF THE APPLICATION (IPC)

H01L21/8234

H01L27/088 H01L29/06

H01L29/66

H01L29/775

TECHNICAL FIELDS SEARCHED (IPC

H01L

INV.

Relevant

to claim

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The present search report has	been drawn up for all claims		
Place of search	Date of completion of the search		Examiner
The Hague	7 June 2024	Sec	k, Martin

CATEGORY OF CITED DOCUMENTS

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- T: theory or principle underlying the invention
 E: earlier patent document, but published on, or after the filing date
 D: document cited in the application
 L: document cited for other reasons

- & : member of the same patent family, corresponding document

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CLAIMS INCURRING FEES The present European patent application comprised at the time of filing claims for which payment was due. 10 Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due and for those claims for which claims fees have been paid, namely claim(s): 15 No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for those claims for which no payment was due. 20 LACK OF UNITY OF INVENTION The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely: 25 see sheet B 30 All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims. 35 As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee. Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims: 40 45 None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims: 50 The present supplementary European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the 55 claims (Rule 164 (1) EPC)



LACK OF UNITY OF INVENTION SHEET B

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The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-6

IC device with reverse tapered dielectric structure between source/drain regions and optionally with tapered semiconductor structure comprising the source/drain regions $\frac{1}{2}$

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2. claims: 7-11

IC device with deep isolation structure having a bottom surface closer to the frontside than to the backside of the substrate

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3. claims: 12-15

A method comprising forming an IC device, comprising forming different dielectric materials in a recess

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ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

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